CLASS	S: FIRST				Theory: 3	Hı	s/wk
SUBJE	<u>CT: basi</u>	CS OF ELECTRICAL	ENGINEE	RING	Tutorial:1	Hr	s/wk
EE	101	Units	6		Practical:	_ Hrs	s/wk
							Hrs
BASIC	CONCE	A PTS∙_	rucie				15
Voltage laws ser	& current ies & para	; Power & Energy; D llel connections; Delt	ependent a ta- star con	nd In nectio	dependent sons and tran	sources; Ohm's sformations.	15
<b>D.C.</b> N Source Theorer analysis	ETWORI transform ns; Source	<b>X THEOREMS:-</b> ation; Linearity & e transportation; sou	superposi rce superp	tion; oositic	Thevenin's on; Nodal a	s & Norton's analysis; Mesh	35
ENER The cap transien	GY STOR pacitor; Th t circuits; l	<b>AGE ELEMENTS:</b> e Inductor; Analysis RLC transient circuits	- of RC-tra S.	nsient	circuits; A	nalysis of RL-	25
ANAL The Pha average resonand delta an	<b>ANALYSIS OF AC- CIRCUITS:-</b> The Phasor equivalent circuit; Methods of Ac-circuit Analysis; Power factor and average power in the sinusoidal Ac-circuits; Complex power; Series & parallel resonance; Calculation of current, voltage, and power in three–phase circuits with delta and star connections					38	
MAGN	ETIC CI	RCUITS AND TRA	NSFORM	ERS			7
						Total	120

Text book1:" Engineering Circuit Analysis" By W. Hayt 2: "Introductory Circuit Analysis" By Boylested

CLASS:	First			Theory: 3	Hrs/wk
SUBJEC	CT: MATH	EMATICS		Tutorial:1	Hrs/wk
EE	102	Units	6	Practical:	Hrs/wk

Article	Hrs
<b>Review of Vectors:</b> i) Representation of vectors in space (i;j;k) unit vectors. ii) Scalar product	4
iii) Vector product	l
<b>Review of Complex Numbers:</b> 1) The Argand diagram. 11) Addition; Subtraction; Product;	4
Iv) Demoiver's Theorem	•
	I
MATRICES AND DETERMINANTS:-	12
i) Definitions ii) Properties. iii) Inverse of a matrix iv)Solution of Equations (Cramer's	14
rule ).	I
DIFFERENTIATION:-	I
Applications of differentiation; Chain rule; Implicit differentiation; Higher order differentiation;	12
trigonometric functions	I
	I
TRANSCENDENTAL FUNCTIONS:-	12
(Inverse trigonometric; Natural logarithmic; Exponential and power)	
i) Definitions ii) properties iii) graphs iv) derivatives and integrals.	I
<b>ADDI ICATIONS OF THE DEFINITE INTECDAL</b> $\cdot$ i) Volumos of revolution ii) Longth	I
of the curve iii) Surface area of revolution	12
	I
METHODS OF INTEGRATION:-	• •
i) Trigonometric Substitutions. ii) Quadratics. iii) Partial fractions. iv) Integration by parts.	20
v) Further Substitutions.	I
VECTOP CALCULUS: i) vector function versus scalar function ii) Del operator: Gradient:	12
Divergence and Curl	
	I
POLAR COORDINATES:-	I
i) The Polar Coordinate system. ii) Graphs of polar equations.	16
SEQUENCES AND SERIES.	I
i) Sequences: convergence; Test of monotone ii) series: geometric series; nth partial sum;	16
tests of convergence; alternating series. iii) Power and Taylor's series.	10
Total	120

Text book:"Calculus", By Thomas ,12<sup>th</sup> Ed.

CLASS	FIRST			Theory: 2	Hrs/wk
SUBJE	CT: PHYS	CAL ELECTRONI	CS	Tutorial: 1	Hrs/wk
EE	103	Units	4	Practical:	Hrs/wk

Article	Hrs
ENERGY BANDS IN SOLIDS:-	12
Charged particles, field effect intensity; Potential energy, The ev units of energy -Nature of	
atom. Electronic structure of elements, Energy band theory of crystals; Lattice structure of	
crystals.	
TRANSPORT PHENOMENA IN SEMICONDUCTOR:-	12
Mobility and conductivity; Properties of intrinsic P and N type semiconductors; Mass action	
law, conductivity modulation; Thermistors; Generation and recombination of charges;	
Diffusion current; continuity equation; Injection minority carrier charges; Potential variation	
within a graded semiconductors.	
JUNCTION DIODE CHARACTERISTICS:-	0
PN junction in equilibrium; Volt Ampere characteristics; Temperature dependence; diffusion	9
capacitance;	
DIODE CIRCUIT ANALYSIS:-	12
Non-linear properties; Ideal diode; Basic theory and analysis of simple diode circuit; DC load	
line; Small signal analysis and concept of dynamic resistance; AC load line; Diode	
capacitance; Temperature effects of diode; Different types of diodes (Zener; schottckey;	
Varactor; Tunnel and negative resistance diodes).	
RECTIFIERS:-	9
Circuit analysis of halfwave and full wave rectifiers, Bridge rectifier; Ripple and form factor	
calculations; Efficiency and IV for above circuits; Types of filters; C filters, L filter, L.C.	
filter, PIE filter; Analysis of filter and calculation of ripple and regulation.	
CLIPPING AND CLAM PING CIRCUIT:-	6
<b>OPTOELECTRONIC DEVICES:-</b>	12
Principle of operation and characteristics of Photoconductive; photovoltaic and photoemissive	
sensors and light emitters; photodiode; photodetectors; phototransistors; Solar cell construction	
and characteristics and applications; LED characteristics; LED Eye Response, Curve and	
Geometric and applications; Optoisolators.	
TRANSISTORS:-	18
Normal operation; PNP; NPN; Current components in transistor; Current gain; Common base;	10
Input and output characteristics; Common emitter; Input and output characteristics; Common	
collector; Input and output characteristics.	
Total	90

Textbook1:INTEGRATEDELECTRONICS''MCGRAWHILL;9THREPRINT,1995.ByMILLMAN&HALKIES 2: " ELECTRONICS DEVICES AND COMPONENTS", PITMAN, 1995 By MOTTERSHED,. 3: " SOLID STATE DIVICES", PHI; 4TH EDITION, 1995.By STREETMAN,

4" SEMICONDUCTOR DEVICES & CIRCUITS", JOHN WILEY & SONS, 1992.By : M.S. TYAGI 5: " ELECTRONICS DEVICES & CIRCUITS THEORY", HI; By BOYLSTED & NASHELSKY,

CLASS:	FIRST			Theory: 2	Hrs/wk
SUBJCT	<b>: DIGIT</b>	AL TECHNIQUES		Tutorial: 1	Hrs/wk
EE	104	Units	5	Practical: 1	Hrs/wk

Article	Hrs
NUMBER SYSTEMS:-	10
Decimal number system; Binary; Octal and hexadecimal number systems; Conversion from one number to another number system; Addition; Subtraction; Multiplication and division using different number system; Representation of binary number insignia-magnitude; Sign 1's Complement and align 2's complement notation; Rules for addition and subtraction with complement Representation; BCD; EBCDIC; ASCII; Extended ASCII; Gray and other codes. <b>LOGIC GATES AND BOOLEAN ALGEBRA:-</b> AND; OR; NOT; NAND; NOR; Ex-OR logic gates; Positive and negative logic; Fundamental concepts of Boolean algebra; De-murrage's laws; Principles of duality; Simplification of Boolean expressions; Canonical and standard forms for Boolean function; SOP and POS, forms; Realization of Boolean functions using only NAND and NOR gates. <b>BOOLEAN FUNCTION MINIMIZATION:-</b>	10
Objectives of the minimization procedures; Karnaugh map method; Don't care conditions; Quine-McCluskey tabulation method; Concept of prime implicants.	10
COMBINATIONAL LOGIC CIRCUITS USING DISCRETE LOGIC GATES:- Half adder and full adder; Half subtractor and full subtractor; Parity generator and checker; Code converters; Binary multiplier; Majority circuits; magnitude comparator	10
<b>COMBINATIONAL LOGIC CIRCUIT USING MSI INTEGRATED CIRCUITS:</b> - Binary parallel adder; BCD adder; Encoder; priority encoder; decoder; Multiplexer and demultiplexer circuits; Implementation of Boolean functions using decoder and Multiplexer; BCD to 7-segment decoder; Common anode and common cathode 7-segment displays; Random access memory; Read only memory and erasable programmable ROMS INTRODUCTION TO SEQUENTIAL LOGIC CIRCUITS:-	15 15
Basic concepts of sequential circuits; Cross coupled SR flip-flop using NAND or NOR gates; JK flip- flop; Clocked flip- flop; D-type and Toggle flip-flops; Truth tables and excitation tables for flip- flops; Master- slave configuration; Edge triggered and Level triggered flip- flops; Elimination of switch bounce using flip- flops; Flip-flops with preset and clear. LOGIC DESIGN USING SSI CHIPS LOGIC DESIGN USING MSI CHIPS (multiplexer; and decoders); expansion	15
theorem; multiplexes ROM PAL; PLA; PLD; PALASM; examples. LOGIC HAZARDS LOGIC FAMILIES AND THEIR COMPARISON	5
Total	90

Text book1: Digital Logic and Computer Design By MORRIS MANO,"
2: Digital Computer Fundamentals" By BARTEE THOMAS,"
3: Digital Integrated Electronics" By TAUB AND SCHILLING,
4:"Modern Digital Design" By RICHARD SANDIGE,

CT ASS.	FIDET			Theory:2	Hrs/wk	
CLASS: SUB.IEC	TIKSI	FER SCIENCE		Tutorial:_	Hrs/wk	
EE	105	Units	6	Practical:2	Hrs/wk	
		A	rticles	<u> </u>	Hrs	
General	overview of p	ersonal compute	r architect	ure	2	
Computer peripherals, keyboard, screen, mouse, and storage media						
Compute	er busses, port	s, interfaces			2	
Overviev	v of MSDOS o	perating system	l		2	
MSDOS	internal comr	nands			2	
MSDOS	external com	nands			2	
Using the	e text editor				2	
Overviev	v of windows o	operating system	1		2	
Windows	s desktop, cha	nging settings, s	tarting pro	grams	2	
Creating	, deleting, cop	ying, moving, se	arching for	r files and folders	2	
Using my	y computer, m	y document, and	d help facil	ity	2	
Using wi	ndows control	panel			2	
Using the	e windows acc	essories paint, n	otepad, wo	rd pad,etc	4	
Setup ap	plications to v	vindows, remove	e applicatio	ns from windows	2	
Connecti	ng to the inte	rnet, using the w	indows exp	olorer	2	
Using the	e Microsoft W	ord			6	
Using the	e Microsoft Ex	ccel			6	
Using the	e Matlab				16	
				Total	60	

Text book 1: "Computer Science" 2: "MATLAB Handbook"

CLASS	: FIRST			Theory:	Hrs/wk
SUBJE	CT: ENGI	NEERING DRAW	ING	Tutorial:	Hrs/wk
EE	106	Units	2	Practical: 3	Hrs/wk

Article	Hrs
BASIC CONCEPTS:-	6
Dimensioning; Symbols and terms used in drawing; Proper use of drawing instruments; Use of international metric system; Names and dimensions of lines used in drawings.	
<b>LETTERING AND NUMERALS:</b> - Arabic and Latin lettering and numerals.	3
	12
DRAWING OF GEOMETRICAL PATTERNS:- Drawing various types of Geometrical patterns (Tracery); Various methods of drawing ellipses; Various types of tangents.	
DRAWING OF SECTIONAL VIEWS AND TANGENTS:-	12
Drawing according to scale; Drawing various views of an actual object; Projections of all views necessary for a given object; Projection of views using first and third angle projection methods.	
ISOMETRIC PROJECTIONS:-	12
FREEHAND SKETCHING; proper and reasonable proportions.	
COMPLITED AIDED ENCINEEDING DDAWING AUTO CAD.	45
Preparing to draw with Auto cad; Basics of 2D Drawing; Edit & Modifications commands; Placing dimensions & Text on drawing; Isometric drawing; Basics of 3D drawing.	
Total	90

Text book1: Engineering drawing & Graphic Technology'' By Thomas E. French 2:" Autocad LT for windows'' By KIRKPATRICK, J

CLASS:	FIRST				Theory:2	Hrs/wk
SUBJEC	<b>T: PRINC</b>	IPLE OF MECH	ANICAL E	NGINEERING	Tutorial:1	Hrs/wk
EE	107	Units	4		Practical:_	Hrs/wk

Article	Hrs
<b>STATICS:-</b> Force system; Units system; Parallelogram law; Forces + components; Resultant of coplanar forces, components of force in space; Moment of a force; Moment of couples, equilibrium: Free body diagram; Coplanar system;; Friction: Nature of friction; Theory of friction; Coefficient of friction; Centroids & center of gravity – centroids of area; Centroids determined by integration; Moments of inertia: radius of gyration;	26
<b>DYNAMICS:-</b> Kinetics of particle; Rectilinear motion; Curvilinear motion; Rectangular components of curvilinear motion; Normal and tangential components of acceleration; Kinetics: Force; Mass and acceleration; Kinetics of particle Newton's 2 <sup>nd</sup> law.	24
<b>THEOREMDYNAMICS:-</b> Introduction; Active materials & their specifications; Work and heat in ideal gasses and steam, 1 <sup>st</sup> law of thermodynamics, particle law in steam and gasses; 2 <sup>nd</sup> law of thermodynamics, particle law in steam and gasses.	20
<b>STRENGTH OF MATERIALS:-</b> Hook's law; Tension and compression stress; Stresses in beams (initial principal).	20
Total	90

Text book1:Engineering Mechanics (statics) By: J.L.MERIAM

2: Engineering Mechanics(Dynamics) By: J.L.MERIAM

- 3: Applied heat for engineers, By :Sneeden &Kerr
- ميكانيك المواد تأليف أيان جون هيرن :4

CLASS	: FIRST			Theory:	Hrs/wk
SUBJE	CT: LABO	DRATORY		Tutorial	Hrs/wk
EE	108	Units	2	Practical: 3	Hrs/wk

## Article

The principal objective is to ensure that students have a good quality capstone design & experience to integrate concepts from a range of classes in the core. The students are to apply modern engineering practices and techniques. Each student should submit a written technical report for each experiment. The experiments cover:-

- Principles of measurements and measuring equipment.
- Principles of CRT and oscilloscopes.
- D.C. circuits.
- A.C. circuits.
- Diode characteristics and applications.
- Transistor characteristics and biasing.
- Digital circuits fundamentals.

Total

Hrs

Text book1:	
2:	

CLASS: FIRST	Theory: 2	Hrs/wk
SUBJECT: Humanitarians subject	Tutorial	Hrs/wk
EE 109 Units 2	Practical:	Hrs/wk
Article		Hrs
Aitteit		
-	ل:- التطور التاريخي لحقوق الإنسان	القسم الأو
	تحمعات البدائية	أولا:- ا
	رحلة ما قبل التاريخ	- a
رة الفرعونية نموذجاً)	لحضارات الشرقية (بلاد وادي الرافدين والحضا	.1 —
(	- الحضارات الغربية (اليونانية والرومانية نموذجاً	-
	الشرائع السماوية	ثانيا:-
	– الديانة اليهودية	
	– الديانة المسيحية	
	- الديانة الإسلامية(بصوره أكثر تفصيلاً)	
	طور حقوق الانسان في القوانين الوضعية	ثالثاً:– ت
	ر جتماعي	-نظرية العقد ال
	المية وأثرها في حقوق الانسان	– الحروب الع
	– التنظيم الدولي	
	ني :- حقوق الإنسان التعريف بما وأنواعها	القسم ألثا
	أولا- التحديد والتعريف	
	– الحق في الفقه الإسلامي	
	– الحق في الفقه القانوني	
	- تعريف حقوق الإنسال ثاناً تتابت متاق الانان ( ت	
سم بدراسة معصلة ومعارفة بين الطانون والسريعة الإسار مية) حد التربية الحد قد بئة مناسبة حد الانسان قد العبث بسلام)	اللقوق الج واعبقات محقوق الإنسان (ود	
ن الثقافة. الحقوق المدنية والسياسية الحقوق الصيغة بالشخصية)	-الحقوق الفردية (الحقوق الاقتصاديا	
	ث: - ضمانات احتام محماية حقوق الانسان	القسم الثال
	أولا – الضوانات في الشروة الإسلاد	
	رود محمد على في مسريد ، و سار. ثانياً:- الضمانات على الصعيد الوط	
ي	ثالثا:- الضمانات على الصعيد الدوا	
	Tota	al 30
Text book1.		
2:		

SUBJECT: Technical English Tutorial Hrs/wk   EE 110 Units   Quits 2   Article Hrs Hrs/wk Practical: Hrs/wk Practical: Hrs/wk Output: Practical: Hrs/wk Output: Practical: Hrs/wk Practical:
EE       110       Units       2       Practical:       Hrs/wk         Article       Hrs       Article       Hrs         Jnit One:       6       6         Reading passage about electronic current and circuits.       6       6         Jnit Three:       6       6         Reading passage about electromagnetism       6       6         Jnit Four:       6       6         Jnit Four:       6       6         Linit Five:       6       6         Linit Five:       6       6         Linit Five:       6       6
EE       III       Units       Z         Article       Hrs         Onit One:       6         Reading passage about electricity and electronics.       6         Unit Two:       6         Reading passage about electronic current and circuits.       6         Unit Three:       6         Reading passage about electromagnetism       6         Jnit Four:       6         Reading passage about miniaturization and microminiaturization       6         Jnit Five:       6         Reading passage about radio waves and vacuum tubes       6
Article       Inis         Unit One:       6         Reading passage about electricity and electronics.       6         Unit Two:       6         Reading passage about electronic current and circuits.       6         Unit Three:       6         Reading passage about electromagnetism       6         Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Unit Five:       6         Reading passage about radio waves and vacuum tubes       6
Reading passage about electricity and electronics.       6         Unit Two:       6         Reading passage about electronic current and circuits.       6         Unit Three:       6         Reading passage about electromagnetism       6         Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Init Five:       6         Reading passage about radio waves and vacuum tubes       6
Unit Two:       6         Reading passage about electronic current and circuits.       6         Unit Three:       6         Reading passage about electromagnetism       6         Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Unit Five:       6         Reading passage about radio waves and vacuum tubes       6
Onit Two:       •         Reading passage about electronic current and circuits.       6         Unit Three:       6         Reading passage about electromagnetism       6         Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Unit Five:       6         Reading passage about radio waves and vacuum tubes       6
Unit Three:       6         Reading passage about electromagnetism       6         Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Unit Five:       6         Reading passage about radio waves and vacuum tubes       6
6         Reading passage about electromagnetism         0 <t< td=""></t<>
Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Unit Five:       6         Reading passage about radio waves and vacuum tubes       6
Unit Four:       6         Reading passage about miniaturization and microminiaturization       6         Unit Five:       6         Reading passage about radio waves and vacuum tubes       6
Unit Five: Reading passage about radio waves and vacuum tubes 6
Jnit Five:       6         Reading passage about radio waves and vacuum tubes       6
Total 20
Text book1:

CLASS: SECOND	
SUBJECT: ENGINEERING ANALYSIS Tutorial: 1	Hrs/wk
EE   201   Units   6   Practical:	Hrs/wk

Article	Hrs
MULTIPLE INTEGRALS:-	8
<ul> <li>i) Double Integral. ii) Area and volumes. iii) Double Integral in Polar Coordinates</li> <li>iv) Evaluation of volume and triple Integrals. v) Evaluation of surface &amp; surface Integrals.</li> <li>SEQUENCES AND SERIES:-</li> </ul>	8
<ul> <li>i) Sequences: convergence; Test of monotone ii) series : geometric series; nth partial sum; test of convergence; alternating series. iii) Power and Taylor's series.</li> <li>VECTORS FUNCTIONS:-         <ul> <li>i) Equations of lines and planes. ii) Product of three or more vectors.</li> <li>iii) Vector function &amp; motion : velocity and acceleration iv) Tangential vectors</li> </ul> </li> </ul>	10
<ul> <li>v) Curvature and normal vector.</li> <li>ORDINARY DIFFERENTIAL EQUATIONS:-</li> <li>i) First order (variables separable; homogeneous; linear – Bernoulli and exact).</li> <li>ii) Second order (Homogeneous and non homogeneous).</li> <li>iii) Higher order differential equations.</li> </ul>	10
SOLUTION OF DIFFERENTIAL EQUATIONS BY POWER SERIES:- Legendre s equation; Legendre s polynomials; Bessel function of the first and second kinds; Bessel	10
function properties. <b>PARTIAL DIFFERENTIATION EQUATION:-</b> Wave equation; laplace equation; solution of boundary condition problems; general solution; solution by separation of variables	10
<ul> <li>NUMERICAL ANALYSIS:-</li> <li>i)Solution of non-linear equations (Iteration; bisection and Newton-Raphson).</li> <li>ii) Finite differences.</li> <li>iii) Numerical differentiation and Integration.</li> </ul>	10
<ul> <li>iv) Numerical solution of 1<sup>st</sup> order ordinary differential equations.</li> <li>MATRIX ANALYSIS:-</li> <li>Review of matrix theory; Linear transformation; eigen values &amp; eigen vectors; lace Lap</li> </ul>	10
transform of matrices; Application of matrices to electric circuits.	10
Definition; Frequency distribution (relative & commutative; Mean; Standard deviation). PROBABILITY:-	10
Definition; mutually exclusive & conditional probability; permutations & combinations; Probability distribution: Binomial; Normal & Poisson distributions. COMPLEX VARIABLE THEORY:-	10
Function of complex variable; complex differentiation; Analytic function & its properties; Integration in the complex plane; Cauchy s theorem; Cauchy s integral formula for simply & multiply connected regions; Complex variable theory:	
Taylor's theorem; Laurent series; The residue theorem.	14
Total	120

Text book 1: "Advanced Engineering Mathematics" By KREYSIK 2: "Calculus" By Finney& Thomas

Class:	: SECON	D	IS
Subje	ct: SIGN	IALS AND SYSTEM	
EĔ	202	Units 6	]

Theory: 2	Hrs/wk
Tutorial: -	Hrs/wk
Practical: 2	Hrs/wk

Article	Hrs
SIGNALS AND SYSTEMS :- Basic Definitions; Mathematical Models; Continuous- Time and Discrete-Time Systems.	4
SIGNAL AND SYSTEM CHARACTERISTICS AND MODELS:- Basic Operations on Signals; Signal Characteristics; System Representations and Models; System Characteristics	4
CONTINUOUS- TIME SIGNALS AND SYSTEMS:-	4
Fime –Domain Representations of Continuous- Time Signals; Sinusoidal and Complex Exponential Signals; Singularity Function Signals; Signal Energy and Power.	-
TIME DOMAIN ANALYSIS OF CONTINUOUS-TIME SIGNALS:-	1
System Equation Solution; System Impulse Response; Zero-State Response of Linear; Time	-
invariant system, The Superposition integral, Continuous Convolution and Properties.	6
FREQUENCY-DOMAIN REPRESENTATION OF CONTINUOUS- TIME SIGNAL:-	0
Signals; Amplitude and Phase Spectra of Periodic signals; The Fourier Transform and Spectra of Aperiodic Energy Signals; The Fourier Transform and Spectra of No energy signals.	
FREOUENCY-DOMAIN ANALYSIS OF CONTINUOUS- TIME SYSTEM:-	4
System Frequency Response; Frequency-Response Determination; Frequency Response of Electric Circuits: Phase Delay and Group Delay: Bode Plots of Amplitude and Phase	
Responses.	
ANALYSIS OF CONTINUOUS- TIME SYSTEM USING THE LAPLACE TRANSFORM:-	6
The Laplace Transform; Laplace Transform Evaluations and Theorems; Evaluations of	
Inverse Laplace Transform; Solution of Linear Integrodifferential Equations; System Transfer Function; Frequency Response.	
CONTINUOUS TIME FILTER -	4
Distortion less Transmission; Ideal Filters; Approximation of Ideal Filters; Butterworth and Chebyshev Filters Design.	

SAMPLED CONTINUOUS TIME SYGNALS:- Ideal Sampling and The Sampling Theorem; Practical Sampling Effects.	6
<b>DISCRETE- TIME SIGNALS AND SYSTEMS:-</b> Time Domain Representation of Discrete-Time Signals; Sinusoidal and Complex- Exponential Signals; Exponential Signals; Unite Step; Unit Ramp and Pulse Signals; signals Energy and Power.	4
<b>TIME – DOMAIN ANALYSIS OF DISCRETE-TIME SYSTEMS:-</b> System Equation Solution; Recursive Solution of System Equation; System Unit Pulse Response; Zero-State Response or Linear-Time Invariant System; Discrete Convolution and Priorities.	4
<b>FREQUENCY-DOMAIN ANALYSIS OF DISCRETE-TIME SIGNALS:</b> Spectra and Bandwidth of Discrete-Time Fourier Series and Spectra of Periodic Signal; System Frequency Response; Frequency-Response Determination.	4
ANALYSIS OF DISCRETE-TIME SYSTEMS USING THE Z-TRANSFORM:- The z-Transform; Transform Evaluation and Theorem; Evaluation of Inverse z-Transform; z- Transform of Linear Difference Equations; The System Transfer Function; System Stability and Frequency Response Using The Transfer Function.	6
Total	60

Text book1: "Introduction to Signals & Systems" By D.K. Lindner, 2: "Signals & Systems" By Carlson

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CLASS:	SECOND			Theory: 2	Hrs/wk
SUBJEC	CT: ELECT	<b>FRONICS</b>		Tutorial: 1	Hrs/wk
EE	203	Units	4	Practical:	Hrs/wk

Article	Hrs	
. INTRODUCTION TO TRANSISTOR CIRCITS: NPN & PNP Bipolar Transistor; Current Flow Mechanism in Transistor Junctions; Transistor configurations; Current Gain Calculation [Alpha] and [Beta]; Transistor input/ output characteristics; DC Load line ; Operating point; Different DC circuit biasing.		
<b>SMALL SIGNAL ANALYSIS AND DESIGN:</b> Small signal equivalent circuit for CB, CE and CC configuration; Input/Output resistance; Calculation of current and voltage Gain in small signal amplifier; Graphical Analysis for voltage gain; Hybird parameters to analyze transistor circuits.		
<b>BAISING STAIBBILITY:</b> Stability factor analysis due to temperature variation (Effect of Ico, Vbe and Beta ); Temperature compensation using diode biasing.	6	
<b>FET and MOS transistor :</b> Introduction to the theory and operations of JFFT & MOSFET; FET Transistor configurations; Transistors transfer characteristics; Amplifier Circuit Biasing; transistor Equivalent circuit; Small signal analysis of FET transistor.	15	
<b>FREQUENCY RESPONSE:</b> Definition and Concepts; Gain in decibel; Bode plot for the gain; The effect of the Coupling capacitor; Low frequency analysis due to the R-C Coupled amplifier in BJTs; the Effect of emitter bypass capacitor; Calculation of the Low cut-off frequency. Transistor amplifier at high frequencies; Hybrid PIE equivalent circuit at high frequency; High frequency behavior of CB & CE amplifier; High cut-off frequency; Gain Band-Width products for the above circuits; FET at high frequencies; CD and CS amplifier at high frequency;		
<b>NEGATIVE FEEDBACK IN AMPLIFIERS :</b> Basic concept of feedback amplifier ; Effect on gain due to feedback ; Input and out put impedances ; Feed back amplifier and sensitivity function ; Voltage series , voltage shunt , current series and current shunt configuration circuits ; Design analysis ; Frequency response of a feedback amplifier .	12	
<b>OPERATIONAL AMPLIFIER:</b> Ideal Op-amp equivalent circuit; Operational Amplifier Specification; Circuit analysis of an Op-amp; Closed loop Op-amp Circuit (Inverting and Non-Inverting Circuit ).		
<b>POWER ELECTRONIC DEVICES:</b> UJT Construction, Operation and characterises; Thyrsistor Equivalent Circuit ; Thyrsistor Characteristics and operation ; Application of the devices.	9	
Total	90	

Text book1: 'Electronic Devices 'By MILLMAN 2: 'Electronic Devices' By FLOYD

CLAS	s: SECON	<b>VD</b>		Theory: 2	Hrs/wk
SUBJ	ECT: DIG	TAL DESIGN	]	Tutorial: 1	Hrs/wk
EE	204	Units	4	Practical:	Hrs/wk

Article		Hrs
INTRODUCTION FIVE VARIABLES MINIMAZATION ADDITIONAL MINIMAZATION TECNHNIQUES: Tabular; Prime; Implicit; Mcklusky; Entered Variables		3 6 15
<b>TOP-DOWN DESIGIN OF COMBAINAONAL CIRCUITS:-</b> Gate Level ;Adders Subtracter; Multiplexer; Decoders		15
ARITHMETIC AND LOGIC UNIT		3
<b>SEQUENTIAL LOGIC CIRCUITS DESIGIN:-</b> Basic concepts of counters and registers; Binary counters; BCD counters; Up counter; Johnson counter; module-n counter; Design of counters using state diagra tables; Sequence generators; Shift left and right register; Registers with parallel load; Serial –in parallel-out (SIPO) and parallel-in-serial-out (PISO); Register using di Type of flip-flops; Sequence generator .	down ms and fferent.	18
<b>SEQUENTIAL LOGIC CIRCUITS:-</b> Delay Model; Characteristics equation, PS/NS Table; State Diagram; Asm Chart; Ka Map; Transition Map; Timing Diagram of flip-flops.	rnaugh	9
SYNCHROUS SEQUENTIAL LOGIC:- Mealy And Moore Circuits; Timing Diagram; Implicit Table State Reductio Assignment.	n And	12
SYNCHROUS COUNTERS:- Shift Registers; Twisted Ring Counter; Maximum Length Shift Counter.		9
Tot	tal	90

Text book1: "Modern Digital Design", By Richard S. Sandige

		Theory: 2	Hrs/wk
CLASS: SECOND SUBJECT: MACHINES		Tutorial: 1	Hrs/wk
205	4	Practical:	Hrs/wk

Article	Hrs
<b>D.C. MACHINE</b> Construction of DC machine, Direct current Generator and motor principles, principles of generator action , principles of motor action	3
Types of DC Generators	3
Shunt DC Generators, series DC Generators	2
Types of DC Motors	3
Shunt DC Motors, series DC Motors, compound DC motors, separately excited DC motors	2
Speed control of DC motors	3
Shunt DC motor speed control, series DC motor speed control	
Stepper motors	6
Type of stepper motor, construction of stepper motor	
Transformers	18
Construction of transformers, principle of operation, approximate equivalent circuit, phase diagram, voltage regulation, open circuit test, short circuit test, transformer efficiency, auto transformer theory, three phase transformer	
A.C Machines	10
Tree phase induction motor	10
Construction, Equivalent circuit, torque speed characteristic, starting torque, condition for maximum torque, condition for maximum starting torque, method of speed control, no load test, blocked rotor test, power flow diagram, applications	
Single phase Induction motor	18
Construction, theories of operation, torque speed characteristic, Equivalent circuit, no load test, Blocked rotor test, Power flow diagram, application	
Three phase synchronous generator	9
Construction, Equivalent circuit, applications	
Single phase synchronous motors	6
Reluctance motor	0
Construction of Reluctance motor, applications	
Hysteresis motor	
Construction of Hysteresis motor, application	2
AC Commutator machine	5
Universal motor	
Total	90

Text book1: electrical machines and transformer by: Ancieron and Macneiil

Class:	SECON	D		Theory: 2	Hrs/wk
Subject: ELICTROMAGNETIC FIELDS			NETIC F	IELDS Tutorial: 1	Hrs/wk
EE	206	Units	4	Practical:	Hrs/wk

Article	Hrs
REVIEW OF VECTOR CALCULUS.	5
<b>COULOMB'S LAW AND ELECTRIC FIELD INTENSITY:-</b> Experimental law of coulomb; Electric field intensity; Field of a continuous and volume charge distributions; line charge and sheet charge; Streamlines and sketches of fields.	10
<b>ELECTRIC FLUX DENSITY AND GAUSS'S LAW:-</b> Electric flux density; Gauss's law; Application of Gauss's law; some symmetrical charge distributions.	10
ENERGY AND POTENTIAL:-	10
Energy expended in moving a point charge in an electric field; Definition of potential difference and potential; Potential field of a point charge and system of charges; Potential gradient; Dipole. <b>CONDUCTORS; DIELECTRICS AND CAPACITANCE:-</b> Current and current density; continuity of current; Conductor Properties and boundary conditions. Nature of Dielectric Materials; Boundary Conditions for Perfect dielectric Materials; Capacitance; Several Capacitance Examples.	10
<b>POISSON'S AND LAPLACE'S EQUATIONS:-</b> Poisson and Laplace 's equations; Examples of the solution of Laplace equation; Examples of the solution of Poisson's equation.	10
<b>STEADY MAGNETIC FIELD:-</b> Boit – Savart law; Amperes law; Magnetic Flux & Magnetic Flux Density; Inductance; Scalar and Vector Magnetic Potentials.	10
MAGNETIC FORCES AND MATERAILS:- Force on Moving Charge; Force on Differential Current. Elements; Force Between Current Differential Elements; Force and Torque on a Closed Circuit; Magnetization and Permeability;	10
Magnetic Boundary Conditions; Magnetic Circuit . <b>TIME - VARYING FIELLDS AND MAXWELL'S EQUATIONS:-</b> Faraday's Law; Displacement Current; Maxwell's Equations in Point Form; Maxwell's Equations in Integral Form; Wave Equations; Wave Propagation in Different Medii.	15
Total	90

Edition; 1992 ; 7<sup>™</sup> Rep 1; .By WILLAIM H.HAYT

2:: Elements of engineering electromagnetic Prentice Hall; 3<sup>rd</sup> Edition; 1992 By . N. N. RAO
 3: Theory and problems of electromagnetics Mc-Graw Hill; 2<sup>nd</sup> Edition; 1993.By JOSEPH A.

CI AGG	GEGOND		Theory: 2	Hrs/wk	
CLASS: SUBJEC	SECOND	RAMING IN C/C	Tutorial:	Hrs/wk	
EE	207	Units	6	Practical: 2	Hrs/wk

Articles		Hrs			
Introduction, Why Programming, Programming Languages, 1	Importance of C	2			
Definition of statement, directives, header files, Simple C prog	gram	2			
printf function, strings, escape characters		2			
Console input output sample functions, sample programs					
Identifiers, keywords, Basic C data types					
Format strings, characters, and width specifiers		2			
scanf function		2			
Operators, arithmetic and logical, sample programs		4			
Mathematical functions, representing mathematical functions solving equations using C	as C statements,	2			
if and if else statements, sample programs, switch statemen	it	2			
Loops (for, while, dowhile) statements, sample programs		4			
nested loops, break and continue statements		2			
Sample C standard functions, getchar, putchar, getch, getche, delay, sound, random,	putch, gets, puts,	2			
Macros, the define directive, sample applications					
Functions, types of functions, sample functions					
Arrays, one, two and multidimensional arrays, initialization, i programs	indexing, sample	4			
Passing arrays to functions		2			
Variable types, definition of local, global, constant, static and	volatile variables	2			
Pointers, definitions, near and far pointers		2			
Referencing, sample pointers applications		2			
Pointers and strings, pointers and functions		2			
Data structures, definition, sample applications		2			
Data structures and pointers, passing structers to functions					
Introduction to object oriented programming, C++ classes					
Private and public members of class, sample classes					
Constructor and destructor of class					
	Total	60			

**Text book 1: "Theory and problems of programming with C"** By Byron S. Gottfried **2: "Application Programming in ANSI C"** By Richard Johnsonbaugh & Martin Kalin

CLASS	SECONE	)		Theory:	Hrs/wk
SUBJE	CT: LAB	ORATORY		Tutorial:	Hrs/wk
EE	2.08	Units	2	Practical: 5	Hrs/wk

Article	Hrs
The principal objective is to ensure that students have a good quality capstone design & experience to integrate concepts from a range of classes in the core. The students are to apply modern engineering practices and techniques. Each student should submit a written technical report for each experiment. The experiments cover the related topics in electronics and communication as well as computer programming.	

Total

Text book 1: 2:

Class	: SECOND	)		Theory: 2	Hrs/wk
Subje	ct: INDUS	TRIAL N	MANAGE	MENT Tutorial: -	Hrs/wk
CE	209	Units	2	Practical:-	Hrs/wk

المفردات	الفصل			
مفاهيم عامه(General concepts)	1			
أنواع المنظمات حسب مكيفاتها(Owner ship)	2			
ضع القرار في المنظمة (Decision making)				
مفهوم النظم وتحليل القيمة(Systems concept and value analysis)	4			
نظام الإنتاج والتصميم المنتج وتطويره (Production system and product design and development)	5			
الرقاب على جودة المنتج(Product Quality control)	6			
إدارة الصيانة والاستبدال (Maintenance and replacement management)				
إدارة المواد(المشتريات,الخزن) Material management purchase management purchase and inventory				
إدارة التسويق(Marketing management)	9			
إدارة الموارد البشرية(Human resource management)	10			
إدارة التمويل(Financial management)				
السلامة الصناعية(Industrial safety)	12			
Total	30			

Text book1:, 2:

Class	SECON				Theory: 2	Hrs/wk
Subject	secont ct: DEMC	, )CRACY			Tutorial: -	Hrs/wk
CE	210	Units	2		Practical:-	Hrs/wk
			_			
				المفردات		الفصل
			واطية	دات ماده الديمة	مفر	
				الشريعة والقانون	كورس الأول:- يتضمن ماده الحريات العامة بين	ÚI
				ن الشريعة والقانون	كورس الثاني:- يتضمن ماده نظم إدارة الدولة بي	ÚI
			لة والقانون )	لعامة (بين الشريع	الحريات ا	
			\		 لا:– المقدمة	أوا
					ياً:- التعريف بالحريات العامة	ثان
					- الأصل اللغوي - الأصل	
					– الأصل التاريخي	
					– الأساس القانوني	
					<ul> <li>الأساس الشرعي</li> </ul>	
					:- أسس الحريات العامة	ثالثا
					– العدالة	
					– المساواة	
					– الحرية	
					:- الحريات العامة الو صفية	رابعاً
					– حرية الرأي	
					– حرية الفكر	
					– حرية الأعلام	
					<ul> <li>المساواه</li> <li>المساعات</li> </ul>	
				e teste to tom a	<ul> <li>الشريعة الإسلامية والحريات العامة</li> <li>الشريعة الإسلامية والحريات العامة</li> </ul>	خامس
				واج, تولي الوطائف)	- موقف الإسلام من المراه (الميرات, الزو	
					– موقف الإسارم من حريد العليدة	
				نظم إدارة الدولة		
					في تحديد النظم السياسية	أولا:-
					– فكره النظام السياسي	
					– شرعية النظم السياسية	
					– أنواع النظم السياسية	
					في النظام الديمقراطي	ثانياً:-
l					– مقدمة تأصيلية	
					– تعريف الديمقراطية	
1					– أركان ومرتكزات النظام الديمقراطي	
l					<ul> <li>نماذج الديمقراطية</li> </ul>	ثالثاً:-
					<ul> <li>الديمقراطية المباشرة</li> </ul>	

Total	30
- الحطاب الإسلامي المعاطير	
ب المطالب الأو الأربي	
– الخطاب الإسلامي التقليدي	
خامساً:- موقف الإسلام من الديمقراطية	
– إشكاليات النظام الديمقراطي	
– النظام اللامركزية	
– النظام المركزي	
رابعاً:- الديمقراطية ونظم إدارة الدولة	
– كيف يتم التحول إلى الديمقراطية	
<ul> <li>الديمقراطية شبة المباشرة</li> </ul>	
– الديمقراطية غير المباشرة	

2:

CLASS: THIRD	Theory:2	Hrs/wk	
SUBJECT: Electronic II		Tutorial:1	Hrs/wk
301	4	Practical:	Hrs/wk

Article	Hrs
<b>OP-AMP APPLICATIONS:</b> inverting, non-inverting amplifier, buffer, summing amplifier, difference amplifier, integrator and differentiator, comparator, sample and hold, zero crossing detector, peak detector, precision diode and fast rectifier, analog computation.	12
<b>FILTERS:</b> filter approximations, passive RLC design, active filter design methods (ladder, and cascaded design technique).	9
<b>OSCILLATORS :</b> BarkHausen's criteria for oscillators ; Satiability concept Three pole amplifier ; Nyquist criteria ; Stabilizing networks ; frequency compensation and sinusoidal oscillator ; phase shift , Wien bridge , Colpitts , Hartley , Crystal and Tune circuit type oscillator (AF &RF Range ).	9
<b>TUNED AMPLIFIER</b> : Introduction to single tuned amplifier ; G.B. response calculations & design ; Cascade amplifier ; Neutralization methods ; Synchronously tuned amplifier ; Elementary treatment of stagger tuned and doubly tuned amplifiers .	9
<b>AUDIO FREQUENCY LINEAR POWER AMPLIFIERS:</b> Introduction to Class A, B, AB, a ,C operation , Class A – common –emitter power amplifier ; Transformer coupled amplifier ; Class push –pull power amplifier ; Amplifiers using complementary symmetry ; Class C amplifier .	12
<b>Comparators and Converters :</b> Zero crossing detector , Schmitt trigger , Comparator ,Voltage limiters and window detector , Clippers and clampers , Peak detector , introduction to A / D and D /A converters and sample and hold circuit .	9
MULTIVIBRATORS: Astable, monostable, 555 timer, and bistable	12
<b>INTEGRATED CIRCUITS AND DEVICES :</b> Introduction of IC families ; Fabrication Steps and evolving transistor , Diode and Resistor ; capacitors families.	9

<b>SPECIALIZED IC APPLICATIONS</b> : phase locked generator, Voltage Controlled Oscillator, XR 2240 pr	l loops , ICL 8038 function ogrammable timer / counter	9
•		
	Total	90
<b>Text boobs:</b> 1- integrated electronics by Milmann		
2- Microelectronics by Milma		

CLASS: THIRD					Theory: 3	Hrs/wk
SUBJECT : DIGITAL SIGNAL PROCESSING				Tutorial: -	Hrs/wk	
EE	302	Units	6		Practical:	Hrs/wk

Article	Hrs		
REVIEW OF DISCRETE SIGNALS AND SYSTEMS	6		
DISCRETE FOURIER SERIES:-	9		
Spectra of periodic digital signals, Properties of series.			
DICSRETE FOURIER TRANSFORM:-	9		
Properties, Frequency response of LTI systems.			
	6		
CONVOLUTION AND CORRELATION	Ŭ		
	0		
DISCRETE AND FAST FOURIER TRANSFORM	9		
Z- TRANSFORM:- Review, Z-plane poles and zeros.	9		
FRAMEWORK FOR DIGITAL FILTER DESIGN	6		
FINITE IMPULSE RESPONSE DIGITAL FILTER DESIGN:-	12		
window method, frequency sampling method, realization of FIR.			
INFINITE IMPULSE RESPONSE DIGITAL FILTER DESIGN:-	12		
Pole-zero method, Bilinear Z-transform, Realization of IIR.			
	12		
APPLICATIONS OF FILTER BANKS IN DSP			
	00		
Total	90		
Text book 1: " Digital Signal Processing", By Emmanuel and Barrie,			
2: "Digital Signal Processing with Computer Applications", John Wiley & S . 1997 By PAUL A. LYNN.	Sons		

CLASS:	THIRD		Theory3	Hrs/wk	
SUBJECT: CONTROL ENGINEERING				Tutorial: -	Hrs/wk
EE	303	Units	6	Practical:	Hrs/wk

Article	Hrs
<b>INTRODUCTION AND BASIC DETINITION:</b> CLOSED LOOP AND OPEN LOOP CONTROL SYSTEMS	3
TRANSFER FONCTION: ELECTRCAL SYSTEM; MECHANICAL SYSTEM; SERVO SYSTEM	6
BLOCK DIAGRAM: BLOCK DIAGRAM REDUCTIOIN ALGEBRA.	3
SIGNAL FLOW GRAPH: MASON GAIN RULE.	6
<b>TIME RESPNSE:</b> TYPICAL TEST SIGNALS & TYPES OF SYSTEM; STEADY STATE ERRORS; TRANSIENT RESPONSE OF 1st and 2 <sup>nd</sup> ORDER SYSTEM.	9
STABILITY OF CONTROL SYSTEM: ROUTH-HURWTIZ CRITERION:	3
<b>ROOT LOCUS ANALYSIS:</b> ROOT LOCUS PLOT; GENENAL RULSES OF CONSTRUCTING ROOT LOCI; ROOT LOCUS ANALYSIS OF CONTROL SYSTEMS.	9
FREQUENCY RESPONSE: INTRODUCTION TO FREQUENCY RESPONSE.	3
<b>BODE PLOT:</b> ; BODE ANALYSIS; RULES FOR SKETCHING THE BODE PLOTS; PHASE AND GAIN MARGINES; RELATIVE STABILITY.	9
<b>CONT ROL SYSTEM DESIGN BY FREQUENCY RESPONSE:</b> PROPORTIONAL GAIN ONLY; LEAD COMPENSATION; LAG COMPENSATION.	12
THE PID CONTROLLER; DEFINITION; TUNING BY ZIEGLER-NICHOLES METHODS.	6
<b>DIGITAL CONTROL SYSTEMS :</b> Z- TRANS FORM &INREVSE Z-TRANSFROM; PULSE TRANSFER FUNCTION ; OPEN LOOP AND CLOSED LOOP RESPONSES OF DISCRETE-TIME SYSTEMS;DESCRETIZATION METHODS; STABILITY TEST FOR DIGITAL CONTROL SYSTEM (JURY'S TEST).	15
STATE-SPACE ANALYSES: STATE EQUATION; SOLUTION OF STATE EQUATION CONTROLABILITY AND OBSERVABILITY	6
Total	90

Text book1: "AUTOMATIC CONTROL SYSTEM" By B. KUO 20012: "MODERN CONTROL SYSTEM" By K.OGATA2001

CLASS: THIRD SUBJEC<u>T : MICR</u>OPROCES<u>SORS</u>

EE	304
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Units

4

Article	Hrs	
INTRODUCTION TO 16BIT MICROPRPCESSOR:-	6	
8086 Architecture, Machine language, instruction, Internal execution and timing.	12	
8086 FAMILY ASSEMBLY LANGUAGE PROGRAMMING :-	14	
Data Transfer instructions ;Arithmetic instructions, logical, Shift and rotate instructions ; Branch instructions ; Loop instruction;NOP ; HLT and flag manipulation instructions; Assembler directives.		
8086 SYSTEM CONNECTIONS AND TIMING : -	12	
8086 Hardware overview ; Basic Signal flow on 8086 buses ;Analyzing a minimum mode system; 8086		
addressing and address decoding; 8086 timing parameter.	12	
INTERRUPTS AND INTERRUPT SERVICE PROCEDURE :-	1.	
8086 interrupts and interrupt response ; 8086 interrupt types ; Hardware and software consideration for using interrupt .		
I/O PROGRAMMING :-	12	
Fundamentals I/O consideration ;Programmed and interrupt I/O ; Block transfers and DMA ,I/O design		
example.		
INTERFACING :-	12	
Programmable Parallel ports and handshake input/ output ; Interfacing microprocessors to keyboard and		
display ; D/A converter operation ; Interfacing and applications ;A/D converter ; Specifications and		
interfacing ; Serial communication interfaces .		
PARALLE I/O AND INTERFACING APPLICATION :-	12	
Interfacing memory; Memory; Mapped I/O.		
GENERAL PURPOSE PROGRAMMABLE PERIPHERAL DEVICES: -	12	
PPI 8255 , Timer 8254.		
Total	90	

 Text book 1 : "The Intel Microprocessor" By BARRY B. BREY,

 2:"The 8088 & 8086 mp`s programming , interfacing S/W, H/W & applications", Prentice

 Hall, 2003 By

 W. A. Triebel & A. Singh

				Theory: 2	Hrs/wk
CLAS	SS: THIRE				
SUBJ	ECT: DI	GITAL SYSTEM	DESIGN	Tutorial: 1	Hrs/wk
EE	305	Units	4	Practical:	Hrs/wk

Article		Hrs
Asynchronous Circuit Analysis and Design		
Analysis of asynchronous sequential network		3
Asynchronous design, primitive flow tables,		3
implication tables, adjacency diagram & state assignment.		3
Systematic state assignment procedure		3
Standard Buses		
Internal, External buses		3
Serial, Parallel buses		3
Memory Expansion		
ROM, RAM		3
Programmable Logic Devices		
GAL, SPLD, CPLD, OLMC, ISP, FPGA		12
V-Hardware Description Language (VHDL)		
Introduction to VHDL		3
VHDL description of combinational network		3
Modeling flip-flops using VHDL process		3
Compilation and simulation of VHDL code		3
Modeling a sequential machine		3
Variables, Signals and Constants		3
Arrays		3
VHDL operators, functions, procedures		3
Packages and Libraries		3
Programmable Logic Controller PLC		
Basic Elements of PLC		3
PLC inputs and outputs interfaces		3
Ladder programming		3
PLC Instructions: Latching,		3
Timers, Counters,		6
Sequencers, Comparisons, Shift Registers		6
System Projects		6
	Total	90

## Text book1:

- 1- Digital Fundamental by Floyd
- 2- PLC Software Manual
- 3- Digital System Design using VHDL By Charles H

CLA	SS. THIRI	Π			Theory:2	Hrs/wk			
SUB.	JECT: CO	D DMMUNIC	CATION		Tutorial:1	Hrs/wk			
E	ΈE	306	Units	4	ctical:	Hrs/wk			
Article									
Tra refl calo	ansmission ection coer culation an	fficient, stand ficient, stand	uivalent circuit, anding waves, qu atching.	charact arter –	eristic impedan wave transform	ce, phase velocity, ner, smith chart	15		
Ana moo VSI Mo	<b>alog Input</b> dulation in B transmis dulator, SS	Analog O dex, spect ssion, DSE B signal ge	<b>utput Schemes:</b> A rum of AM, DSE 3,C amplitude m eneration and Den	Amplita 3 transi odulato nodulat	nde Modulation mission with an ors, Envelope of ion schemes.	; Equation for AM, nd without carriers, letectors, Balanced	15		
Fre for bety nois	equency m sinusoidal ween FM a se in AM a	odulation: waveform and PM, NI nd FM syst	Equations for FM and Bessels func BFM, frequency r tems.	1, mod tion tal nodulat	ulation Index, s ple, phase modu ors ( Armstron	pectrum calculation ulation, relationship g method) Types of	15		
<b>Dig</b> Tra	gital Input	t Analog nd receiver	<b>Output Scheme</b> block diagrams.	s : A	SK, FSK, QA	M, BPSK, QPSK,	15		
Ana cod Cor	alog Input le modulati mpounding	t <b>Digital O</b> on PCM, I method, N	<b>utput Schemes :</b> Delta modulation I loise in digital sys	Variou DM. C tems.	s pulse modula omparison betw	tion methods, pulse een PCM and DM,	15		
<b>Dig</b> Ma	gital Input nchester, an	t <b>Digital</b> ( nd multilev	<b>Dutput Schemes</b> yel encoding meth	: Lino ods and	e encoding me l comparison of	thods : NRZ, RZ, these schemes	15		

2:

CLASS: THIPD	Theory: 2	Hrs/wk					
SUBJECT:ELECTRONIC_INSTRUMINTATION	Tutorial: 1	Hrs/wk					
EE 307 Units 4	Practical:	Hrs/wk					
Article		Hrs					
INSTRUMINTATION ERRORS		6					
TRANSDUCERS:- Resistive, Capacitive, Inductive. Active Transducers.							
SIGNAL CONDITIONING:- Input signal modification, scaling of measuring variables, delay lines, noise, signal averaging, interference, grounding, shielding, signal filtering, signal correlation, current-mode amplifier.							
SIGNAL CONVERSION:- Conversion by transducer bridge, electronic multipliers, signal generator, a.c to d.c signal conversion, logic elements, sample & hold, A/D and D/A signal conversion, isolation amplifier.							
INSTRUMENTATION AMPLIFIER:- Circuit design, characteristics, CMMR .		9					
ANALOG ELECTRONIC INSTRUMENTS:- Analog (voltmeter, multi-meter, vector impedance meter, frequency meter, distortion analyzer, spectrum analyzer.							
<b>DIGITAL INSTRUMENTS:-</b> Digital indicator, voltmeter (dual slop, multi-slop, successive approximation, and voltage to frequency converter, ammeters, ohmmeters, multi-meters, counters (frequency, frequency ratio meter, time-interval meter, energy meter), digital multiplexers, microprocessor-based meters.							
INTERFACE BUSES:- Parallel port, RS-232, GPIB.							
	Т	otal 90					

Text books: 1- "Electronic Instrumentation and Measurement Techniques" ByWillliam David Cooper and Albert D. Helfrick.

2-Principles of Measurement systems By John P. Bentley

3-Electrical and Electronic Measurement By Ahmed A.Montaser and Karam A. sharshar

CLASS	: THIRD			Theory:	Hrs/wk
SUBJE	CT::LA	BORTORY		Tutorial:	Hrs/wk
EE	308	Units	4	Practical: 6	Hrs/wk

Alucie
The principal objective is to ensure that students have a good quality capstone design & experience to integrate concepts from a range of classes in the core. The students are to apply modern engineering practices and techniques. Each student should submit a written technical report for each experiment. The lab include experiments in electronic circuit analysis, communication Systems and microprocessor technology.
Total

Text book1:		
2:		

Class: FOURTH			Theory:	2	Hrs/wk
SUBJECT: Indu	strial Electronic		Tutorial:	1	Hrs/wk
EE 401	Units	4	Practical:	Hrs/wk	ζ.

Article	Hrs
Introduction : Scope of power electronics , power converter specification .	
Power Semiconductor Devices : Thyristor families , V-I characteristics of SCR ,	
Triac, GTO, Diac, Source of thyristor triggering, turn On \ turn Off	12
characteristic and Gate triggering requirements, series/parallel operation, device	
ratings.	12
<b>Power transistor devices:</b> Basic structure and V-I characteristics of power	
MOSFET, IGBT, SIT. Switching characteristic, Gate/Base drive circuits, Safe	
operating area, di/dt / dv/dt limitation, series/parallel operation, ratings.	10
<b>Phase Control Converters:</b> Signal phase central taped transformer connection,	18
controlled Bridge converters. Use of flywheeling diede operation with resistive	
inductive and Back EME load line commutated inverter effect of source	
inductance on converter performance, power factor, ripple factor calculation	
firing scheme, linear alpha and cosine angle control, application of D.C motor	
speed control, regulated power supply, battery charger.	
Thyristor commutation techniques : Natural commutation, Force commutation	15
, Voltage / Current commutation, DC chopper, Principle of Voltage control,	
analysis of Morgan chopper circuit, Johns chopper circuit, regenerative chopper	
circuit .	
Inverters : Single phase series and parallel inverters , classification of CSI	15
and VSI inverters , single phase and three phase inverter circuit , methods of	
voltage controlled inverter circuits , comparison of thyristor and transistor	
based inverters, application to speed control of AC motors , uninterrupted	12
power supply , induction melting , heating furnaces .	
Industrial Applications: DC Motor Control, Induction Motor Control, Pulse	
width Modulation & Speed Control, Static Relays & Contactors.	
Total	90

Text book1: power electronics by: Muhammad Rashid 2:

CLASS	S: FOURTH	I			Theory: 2	Hrs/wk
SUBJEC	T: DATA TR	ANSMISSION& (	COMPUTER I	ETWORKS	Tutorial:1	Hrs/wk
EE	402	Units	4		Practical:	Hrs/wk

Article	Hrs
INTRODUCTION AND DEFINITIONS:-	3
Data Communication, Networks, Protocols, Standards, and Standard organizations.	
BASIC CONCEPTS:-	6
Line configuration, Topology, Categories of networks.	
TRANSMISSION MEDIA:-	9
Electromagnetic spectrum.	-
Guided media: Unshielded Twisted Pair (UTP) Cable.	
Shielded Twisted Pair (STP) Cable.	
Coaxial Cable.	
Optical Fiber.	
Unguided media: Radio Transmission.	
Microwave Transmission.	
Satellite Microwave.	6
INTERFACES AND MODEMS:-	U
Data transmission: parallel, serial, synchronous and asynchronous.	
DIE-DCE interface and standards.	
Modems.	
THE USI AND TCP/IP MODELS NETWORKING AND INTERNETWORKING DEVICES.	6
NETWORKING AND INTERNETWORKING DEVICES:-	6
Networking devices: NICs, Hubs, Repeaters, Bridges and Switches.	
Internetworking devices: Router and Gateways.	
DATA LINK CONTROL:-	6
Link Discipline, Flow control, Error control.	
DATA LINK PROTOCOLS:-	3
Asynchronous protocols, Synchronous protocols.	
EUCAL AREA NET WORK (LAN):-	12
Eulernei Takan Bus – musicat 802	
Token Bus project 802	
ICP/IP MODEL AND PROTOCOLS	9
WIRELESS LAN (WLAN):-	9
introduction and history of (wLANS), Standardization and frequency bands, IEEE 802.11	
STANUALU. WIDE ADEA NETWORK (WAN)	6
WIDE AREA NET WORK (WAN) WIDELESS WAN	6
WIRELESS WAN INTERNET WORKING AND INTERNET	3
	90
	70
<b>I ext Dook1:</b> "Introduction to data comm. And networking", By Pehrouz Forouzan.	
<b>2: "Computer Networks and internets", Douglas</b> By E. Comer (4 <sup>th</sup> edition)	

CIASS: ]	FOURTH	-			Theory:	2	Hrs/wk
SUBJEC	T: Microj	processor & M	licro Cont	roller	Tutorial:	1	Hrs/wk
EE	403	Units	4		Practical:	Hrs/wł	K

Article	Hrs				
<b>Introduction to Advanced Microprocessors :</b> Overview of 80186,80286, 80386, 80486 Architecture, Descriptor table, Privilege levels, paging. Detail study of Pentium, Pentium MMX architecture, Pentium II, Memory and Microprocessor, The Programming Model, Real mode and protected mode	12				
Memory addressing, Data formats. <b>The 80386 and 80486 Microprocessor :</b> Architecture – Real mode and Protected mode, 80386 Memory Management, Memory segmentation, Memory paying Mechanism, On chip cache organization.	12				
Assembly language and Programming concepts : The instruction set , Addressing modes , Data movement instructions , Arithmetic and logic instructions , programming the Microprocessor .					
Microprocessors and Microcontrollers : Comparing Microprocessors and Microcontrollers , The Z80 and MCS 51 , Microcontroller survey .	9 6				
Microprocessor & Micro Controller : Comparing Microprocessors and					
<b>The MCS-51 Architecture :</b> Introduction, MCS-51 family microcontrollers hardware, Input/output pin, ports and circuits, External memory interfacing, counter, timer, serial data input/output. Interrupts.	9				
<b>Basic Assembly Language Programming Concept :</b> Addressing mode, External data, move, Code memory read – only data moves, Push and Pop opcodes, Data Exchanges, Logical operations, Arithmetic operations, Branching Instructions, Interrupts and Returns.	12				
An MCS-51 Microcontroller Design : Microcontroller Specification, External memory and Memory space Decoding, Expanding I/O, Memory map I/O, Memory address decoding, Testing the Design, Lookup table for the 8051, Serial data Transmission.	9				
Total	90				

Text book1: "The 80386-80486 and Pentium processor" By Walter A. Tribel; 2: "The intel Microprocessors " By Barry B. Bery 3: "The 8051 micro-controller" By I. Scott Mackenzie.

CIASS: FOURTH	Theory: 3	Hrs/wk	
SUBJECT: Microelectroni	ics	Tutorial: -	Hrs/wk
EE 404	Units 6	Practical:	Hrs/wk

Article	Hrs
<b>Semiconductor Fundamental :</b> Energy band model of solid, intrinsic and extrinsic semiconductor, free carrier density in semiconductor, carrier concentration and Fermi level. Carrier transport and recombination, carrier diffusion, the drift current, Hall effect. MIS, MOS, Schottky barriers, magnetic effect, bipolar junction, p-n junction, FET.	6
<b>IC fabrication processes :</b> Crystal growth, diffusion, doping, evaporations, and photo masking, Ion implementation, Thin and thick film fabrication, sputtering, mounting, package, and hybrid integrated circuits.	12
<b>LSI and VLSI Design and Application :</b> Discrete device design, bipolar transistor fundamental, technology, and miniaturization. Linear I.C's: fabrication, and general consideration. Current sources, LSI oriented bipolar technology. Logic Families based on bipolar transistor (RTL, DTL, TTL, ECL, TRL, I <sup>2</sup> L). TTL gate circuit analysis. Metal- Semiconductor junction, Metal-Oxide Semiconductor junction. FET theory and analysis.	18
MOS Transistor Fundamentals and MOS I.C Technology : MOS capacitor, static characteristics of the MOS transistor, MOS device fabrication. MOSFET's. Logic circuits based on MOSFET, PMOS, NMOS, CMOS,DMOS, SOS, VMOS. NMOS inverter and gate circuit analysis. CMOS inverter and gate circuit analysis. Charge-coupled devices and non-volatile memory devices, software applications.	18
ASIC Design methodologies and system design consideration	9
LCA, Standard cell, Gate array, Structured array]	9

	Total	90
	Total	90
arrays FPGA		,
Field programmable gate arrays FPGA and Field	programmable analog	Q
Full-Custom and Semi-Custom Design :Design mo discrete component, full-custom and semi-custom des	tivations; design either sign approaches.	9

Text book1: Microelectronic By Millmann 2:principle of CMOS VLSI design By Neil Weste and Karmran Eshrahian

CLASS	FOURTH	1		Theory: 2	Hrs/wk
SUBJEC	TOORIT	vave Enginee	ring	Tutorial: 1	Hrs/wk
EE	405	Units	4	Practical:	Hrs/wk

Article	Hrs
REVIE OF ELECTROMAGNETIC THEORY	3
TRANSMISSION LINES IN MICROWAVES	9
MICROWAVE NETWORK ANALYSIS USING S-PARAMETERS	9
PASSIVE COPONENTS IN MICROWAVES AND ACTIVE MICROWAVE CIRCUIT.	12
MICROWVE FILTERS	3
MICROWAVE DIODE AND TRANSISTOR S.	6
DESIGN OF MICROWAVE AMPLIFIERS	3
DESIGN OF MICROWAVE OSCILLATORS	3
MICROWAVE INTEGRATED CIRCUITS	5
APPLICATION OF MICROWAVE IN ELECTRONIC SYSTEMS	6
PINCIPLE OF RADIATION	9
ANTENNAS	9
PINCIPLE OF PROPAGATION	9
METHODS OF PROPAGATION	
Total	90

Text book 1: "Microwave Circuits and devices" by Liao 2: "Microwave Engineering" by Pozar

CLASS : FOURTH			Theory: 2	Hrs/wk	
SUBJEC	T: Comp	uter aided de	esign	Tutorial: -	Hrs/wk
EE	406	Units	4	Practical:	Hrs/wk

Article
<b>Introduction</b> Linear circuits, A.C circuits, A.C circuits matrix analysis, two port analysis, graph theory.
Numerical solution for non linear network simple search algorithm convergence properties, secant method.
<b>Simulation</b> Algorithms, stability and accuracy in Eulers methods, higher-order, Runge-kutto Algorithms.
<b>State variable analysis</b> Generation of state equation from topological data , finding a tree, solution of state equations.
Sensitivity analysis Sensitivity measures, sensitivity calculation tolerance analysis.
<b>Optimization</b> Gradient algorithms, numerical solution of gradient algorithm, stability, search methods.
<b>C.A.D for integrated circuits</b> Layout algorithm routing algorithm , testability analysis.
<b>Genetic algorithms</b> Application of GA in electronics.
Total

Text book1: computer assisted network and system analysis by: by Mastacusa 2:

CLASS	: FOURT	H		Theory: 1	Hrs/wk
SUBJE	CT: ENGI	NEERING I	PROJECT	Tutorial:	Hrs/wk
EE	407	Units	4	Practical 3	Hrs/wk

Article	Irs
Collaborative team work of the nature in a research environment is expected, including extensive interaction with other students. Each student should submit a written technical report and should attend the final oral examination . The students apply verbal written and oral technical skills to document the design process .	
Total 12	.20

Text book1:	
2:	

CLASS	• FOURTH			The	eory:	Hrs/wk
CLASS SUBJEC	CT: LABC	RATORY		Tut	torial:	Hrs/wk
EE	408	Units	4	Pra	ictical: 6	Hrs/wk

Article	Hrs
The principle objective is to ensure that students have a good quality capstone design & experience to integrate concepts from a range of classes in the core. The students are to apply modern engineering practices and techniques. Each student should submit a written technical report for each experiment.	
Total 1	180

Text book1: 2: