

# CV

## **Personal Information:**

**Name:** Zahraa siddiq yahya ahmad alsaegh

**Educational Qualification:** Master's degree in electrical engineering/electronic.

**Scientific Title:** Assistant Lecturer.

**Department:** Electronic Engineering.

## **Published Researchs:**

1. Architectural Design and Implementation of Bit Error Rate Tester on FPGA
2. Study of Efficiency-limiting Defects in silicon solar cells

## **Academic achievement and contribution to university activities:**

- 1- The subjects I have taught for the last academic years:
  - A- Basics of Electrical Networks / First Stage
  - B- Electrical Networks Lab / First Stage
- 2- Supervising the fourth stage graduation projects
- 3- Participation in the International Conference of the University of Nineveh
- 4- Participation in courses held by the college
- 5- Participation in the seminars and electronic lectures of the University of Nineveh
- 6- Obtaining letters of thanks from the Minister of Higher Education, the President of the University of Nineveh, and the Dean of the College of Electronics Engineering
- 7- Contributing to an afforestation campaign on campus
- 8- Participating in scientific visits to enhance the practical aspect of students