

وزارة التعليم العالي والبحث العلمي  
جهاز الإشراف والتقويم العلمي  
دائرة ضمان الجودة والاعتماد الأكاديمي

وصف البرنامج الأكاديمي لقسم هندسة الالكترونيك

للعام الدراسي ٢٠٢٣ - ٢٠٢٤

نموذج وصف البرنامج الأكاديمي

اسم الجامعة: جامعة: نينوى

الكلية/ المعهد: كلية هندسة الالكترونيات

القسم العلمي: قسم هندسة الالكترونيك

اسم البرنامج الأكاديمي او المهني: بكالوريوس علوم هندسة الالكترونيك

اسم الشهادة النهائية: بكالوريوس علوم هندسة الالكترونيك

النظام الدراسي: سنوي مع نظام بولونيا للصف الاول والثاني

تاريخ اعداد الوصف: ٢٤-٥-٢٠٢٤

تاريخ ملء الملف: ٢٤-٥-٢٠٢٤

التوقيع

اسم رئيس القسم: أ.م.د. حارث احمد محمد

التاريخ

التوقيع

اسم المعاون العلمي: أ.م.د. بلال علاء الدين جبر

التاريخ

دقق الملف من قبل شعبة ضمان الجودة والأداء الجامعي

اسم مدير شعبة ضمان الجودة والأداء الجامعي:

التاريخ

التوقيع

مصادقة السيد العميد

أ.د. خالد خليل محمد

## ١. رؤية البرنامج

ان يكون قسم هندسة الالكترونيك متخصصا في علوم هندسة الالكترونيات وأن يتميز بتعليمه الهندسي والبحث العلمي المؤدي الى تقدم المعرفة وتطوير المهنة وخدمة المجتمع من خلال الشراكة مع الصناعات والمؤسسات الهندسية والخدمية وتخريج الكوادر لرفد المجتمع بكوادر عالية الكفاءة.

## ٢. رسالة البرنامج

١. التعليم: توفير برامج هندسية تعليمية متخصصة ذات تخصص دقيق للدراسات الأولية والعليا. وتوفير بيئة تعليمية متميزة معترف بها بحيث يمتلك خريجوها خبرة مهنية عالية وتعليم هندسي اساسي تمكنهم من المساهمة بفعالية في خدمة مجتمعهم ورفع مستوى وتقدم مهنتهم. كل ذلك يقع ضمن المواصفات العالمية القياسية ويتم تنفيذه باعتماد نظام الجودة ABET في مجال التعليم الهندسي
٢. البحث: توفير بيئة بحثية عالية المستوى بحيث يتمكن اساتذته وباحثوه وطلابه من قيامهم بالبحوث في المجالات الهندسية الاساسية والتطبيقية والاستكشافية ونشر وتطبيق المعرفة المتوافرة والجديدة بما يخدم المجتمع والمنطقة ويتفاعل مع العالم.
٣. القيادة: توفير بيئة بحثية عالية المستوى بحيث يتمكن اساتذته وباحثوه وطلابه من قيامهم بالبحوث في المجالات الهندسية الاساسية والتطبيقية والاستكشافية ونشر وتطبيق المعرفة المتوافرة والجديدة بما يخدم المجتمع والمنطقة ويتفاعل مع العالم.
٤. خدمة المجتمع: التفاعل مع المجتمع والانخراط في مجال تطوير صناعة البلاد والمؤسسات الهندسية الذي يؤدي الى التطوير الاجتماعي والاقتصادي للبلد من خلال الاستشارات والتعليم المستمر والتزام المشاكل الصناعية كبحوث لتقديم الحلول لها.

## ٣. اهداف البرنامج

أ. تخريج مهندسين مختصين في مجال علوم هندسة الإلكترونيك وتطبيقاتها وبمواصفات عالية ويمتلكون القدرة على العمل في القطاع العام والخاص

ب. المساهمة الفعالة في نهضة وتقدم المجتمع من خلال عقد الندوات والمؤتمرات والتعليم المستمر
ج. انتاج بحوث علمية رصينة تطبيقية في تخصص الهندسة الالكترونية لغرض حل المشاكل الصناعية والخدمية في المجتمع
د. تعزيز جانب القيادة لدى المنتسبين والخريجين وبث روح التعاون بينهم
هـ- منح شهادات عليا في تخصصات القسم المختلفة وبمواصفات عالية
و.- اعتماد منهج التحديث في المناهج الدراس وتحسين الاداء في الفعاليات والانشطة لضمان تحقيق الاهداف المنشودة للقسم وحسب معايير الجودة (معايير ABET)

<b>٤ . الاعتماد البرامجي</b>				
لا يوجد				
<b>٥ . المؤثرات الخارجية الأخرى</b>				
لا يوجد				
<b>٦ . هيكلية البرنامج</b>				
هيكل البرنامج	عدد المقررات	وحدة دراسية	النسبة المئوية	ملاحظات *
متطلبات المؤسسة	٤	١٢	٧.٨%	مقرر اساسي
متطلبات الكلية	٨	٣٦	٢٣.٤%	مقرر أساسي
متطلبات القسم	٢٥	١٠٦	٦٨.٨%	مقرر أساسي
التدريب الصيفي	في المرحلة الثالثة			أساسي
أخرى				

\* ممكن ان تتضمن الملاحظات فيما اذا كان المقرر أساسي او اختياري.

<b>٧ . وصف البرنامج</b>				
الساعات المعتمدة		اسم المقرر أو المساق	رمز المقرر أو المساق	المرحلة الدراسية
عملي	نظري			
-	٣	Mathematics I	NVEE206	الاول / الفصل الدراسي ١

٣	٣	DC Circuits Analysis	NVEE215	الاول / الفصل الدراسي ١
-	٢	Physical Electronics	NVEE218	الاول / الفصل الدراسي ١
٢	2	Computer science	NVEEEL114	الاول / الفصل الدراسي ١
-	٢	Mechanical engineering principles	NVEE203	الاول / الفصل الدراسي ١
-	٢	Democracy and Human Rights	NV12	الاول / الفصل الدراسي ١
٣	٣	AC Circuits Analysis	NVEE216	الاول / الفصل الدراسي ٢
	٣	Mathematics II	NVEE207	الاول / الفصل الدراسي ٢
-	٢	Physics Of Semiconductor	NVEE219	الاول / الفصل الدراسي ٢
٢	٢	Digital Techniques	NVEE217	الاول / الفصل الدراسي ٢
٣	-	Engineering Drawing	NVEE201	الاول / الفصل الدراسي ٢
	٢	English	NVU11	الاول / الفصل الدراسي ٢
				الثاني /الكترونيات الاجهزة الطبيه
-	٢	Engineering Analysis I	NVEE208	الثاني / الفصل الدراسي ١
٢	٢	Signal Analysis	NVEEELM211	الثاني / الفصل الدراسي ١
٣	٢	Electronic I	NVEEELM212	الثاني / الفصل الدراسي ١
-	٣	Digital design	NVEE223	الثاني / الفصل الدراسي ١
٢	٢	Electromagnetic fields I	NVEE215	الثاني / الفصل الدراسي ١
-	٢	Human Physiology	NVEEELM 213	الثاني / الفصل الدراسي ١
٢	٢	signals and systems	NVEE210	الثاني / الفصل الدراسي ٢
-	٢	Engineering Analysis II	NVEE209	الثاني / الفصل الدراسي ٢
-	٢	Electronic II	NVEEELM221	الثاني / الفصل الدراسي ٢
٢	٢	programming	NVEEELM222	الثاني / الفصل الدراسي ٢
	٢	Electromagnetics FieldsII	NVEE221	الثاني / الفصل الدراسي ٢
-	٢	The Crimes of the Defunt Baath Party	NVU13	الثاني / الفصل الدراسي ٢
				الثاني /صناعي
-	٢	Engineering Analysis I	NVEE208	الثاني / الفصل الدراسي ١
٣	٢	Electronic I	NVEEELI212	الثاني / الفصل الدراسي ١
٢	٢	DC Machines	NVEEELI213	الثاني / الفصل الدراسي ١
٢	٢	Computer Programming	NVEEELI214	الثاني / الفصل الدراسي ١
-	٢	The crimes of the defunct Baath Party	NVU13	الثاني / الفصل الدراسي ١
-	٢	Fundamentals of Electromagnetics	NVEE221	الثاني / الفصل الدراسي ١
-	٢	Engineering Analysis II	NVEE209	الثاني / الفصل الدراسي ٢
٢	٢	Electronics II	NVEEELI222	الثاني / الفصل الدراسي ٢
٢	٢	AC Machines	NVEEELI223	الثاني / الفصل الدراسي ٢
٢	٢	Computer Languages	NVEEELI224	الثاني / الفصل الدراسي ٢
-	٣	Digital Design	NVEE223	الثاني / الفصل الدراسي ٢
٢	٢	Signals and Systems	NVEE210	الثاني / الفصل الدراسي ٢
-	3	Electronic II	EE3301	الثالث

-	٣	Digital Signal Processing	EE3201	الثالث
-	3	Control Engineering	EE3302	الثالث
-	٣	Microprocessors	EE3303	الثالث
		Digital System Design I	EE3304A	
-	٣	Digital System Design II	EE3304B	الثالث
-	٣	Communications	EE3305	الثالث
-	٣	ELECTRONIC INSTRUMENTATION	EE3306	الثالث
٦	-	Laboratory	EE3307	الثالث
-	٣	Industrial Electronic	EE4301	الرابع
-	٣	DATA TRANSMISSION & COMPUTER NETWORKS	EE4302	الرابع
-	٣	Microprocessor & Micro Controller	EE4303	الرابع
-	٣	Microelectronics	EE4304	الرابع
-	٣	Radiation	EE4305	الرابع
		Antenna and Propagation	EE430	
-	٣	Computer aided design	EE4306	الرابع
٣	1	Engineering Project	EE4307	الرابع
٦	-	Laboratory	EE408	الرابع

## ٨. مخرجات التعلم المتوقعه للبرنامج

### أ - الاهداف المعرفية

١. تمكين الطلبة الخريجين من الحصول على المعرفة والفهم والمبادئ والنظريات الأساسية في مجال هندسة الإلكترونيك
٢. تمكين الطلبة الخريجين من فهم واستيعاب المواضيع العلمية الحديثة المتقدمة في مجال الاختصاص الدقيق في هندسة الإلكترونيك.
٣. تمكين الطلبة الخريجين من فهم المبادئ والأساسيات الرياضية لتمثيل الانظمة وتحليلها ودراستها وكيفية تصميم أنظمة الكترونية مختلفة
٤. مساعدة الطالب على الاطلاع على اهم البرمجيات الحاسوبية التي تستخدم في مجال حل المشاكل الهندسية و ان يكون قادرا على فهم اسس عمل الانظمة الالكترونية وكيفية برمجتها ليقوم بمهام عملية معينة.

### ب- الاهداف المهاراتية الخاصة بالبرنامج

- ب ١ القدرة على تصميم وتنفيذ المكونات التجميعية للاتظمة الالكترونيه
- ب ٢ القابلية على تصميم وتنفيذ برامجيات مختلفة ، بالإضافة الى تلك التي تخص نظم التشغيل الأساسية ونظم المعلومات والقدرة على استخدام التقنيات المتقدمه والمتنوعة وتوظيفها في التطبيقات المختلفة.
- ب ٣ التمكن من فهم اسس تصميم وعمل أجهزة الالكترونيه ومواكبة التكنولوجيا الحديثه
- ب ٤ التمكن من وضع المواصفات المناسبة للاجهزة الالكترونيه والبرامج الأساسية اللازمة لتشغيلها ، اضافة الى التجهيزات الفنية اللازمة لتنفيذ عمليات الاتمة والحكومات الالكترونية.

## استراتيجيات التعليم والتعلم

- المتابعه من خلال سير تنفيذ الواجبات والدقة في التعامل معها
- تشكيل مجاميع صغية من الطلبة لحل معضلة معينة وتبادل الآراء مع الزملاء بخصوصها
- فتح باب النقاش في بعض المسائل وبالشكل الذي يضمن مشاركة الجميع والتعود على سماع آراء مختلفه

## طرائق التقييم

١- الامتحانات الفصلية والنهائية

٢- الامتحانات اليومية القصيرة

٣- اجراء التجارب المختبرية وكتابة التقارير ومناقشة النتائج المختبرية

٤- المشاركة في مؤتمرات علمية والنشاطات الصفية التي تتضمن تصميم بعض انظمة الالكترونية

٥- امتحانات الالكترونية وتكليفات ضمن وقت محدد على المنصات التعليمية

## ج- الاهداف الوجدانية والقيمية

- ١ تطوير قدرة الطالب للعمل على أداء الاعمال المناطة له وانجازها في الموعد المحدد بدقة واخلاص.
- ٢ لتفكير التحليلي العلمي المبني على قواعد اساسية علمية ومنطقية.
- ٣ تمكين الطالب من الحوار والمناقشة بالمسائل المتعلقة بتخصصه بشكل مثمر.
- ٤ تبادل الآراء وافساح المجال للاخرين لتوضيح وجهات النظر المختلفة في المسائل المطروحة.

## طرائق التقييم

- ✓ تقييم وتصحيح الاعمال المشتركة للطلبة (اساسية وتطبيقية).
- ✓ تمييز اصحاب الآراء البناءة والمنهج العلمي في حل المشاكل المختلفة.
- ✓ اعتماد آراء الطلبة والتغذية الراجعة من خلال الاستبيانات الالكترونية واخذ الآراء حسب الاكثرية

## د- المهارات العامة والتأهيلية المنقولة (المهارات الأخرى المتعلقة بقابلية التوظيف والتطور الشخصي)

١. القدرة على التحليل العلمي والمنطقي المبني على حقائق أساسية او الخبرة التطبيقية عند التوظيف.
٢. امكانية استخدام التكنولوجيا المتقدمه على اختلاف انواعها للقيام بتطبيقات مهمة في مجال هندسه الالكترونيات المختلفه
٣. القدرة على العمل ضمن فريق واحد والتعاون لانجاز مهمة معينة من خلال المشاركة الفاعلة وتبادل مختلف الآراء للوصول إلى الحل الأمثل.
٤. القابلية على التطوير الذاتي وطرق ابواب التكنولوجيا الحديثة والتطبيقات المتطورة والاستفادة من المعلومات والمهارات الاكاديمي.

### طرائق التعليم والتعلم

- ✓ المتابعة المستمرة لسير البرنامج الكاديمي وبكافة فعالياته.
- ✓ تشكيل مجاميع من الطلبة واشراكهم في حل مشكلة واقعية ومناقشة الحلول المقترحة.
- ✓ فتح باب الحوار لبعض المسائل وسماع آراء مختلفة حول البرنامج وتطويره بشكل مستمر.
- ✓ التركيز على طبيعة المشاكل التي تعالجها مشاريع التخرج للسنة المنتهية والتأكيد على التطبيقية منها والتي تكسب الطالب خبرات اضافية تفيده لاحقاً في مجال العمل عند التوظيف.

### طرائق التقييم

- ✓ تقديرات الطلبة عند التخرج.
- ✓ نسبة المشاركة في الأنشطة والأعمال المختلفة.
- ✓ التقدير الذاتي والمقارنة مع تقدير الزملاء والأساتذة.
- ✓ المقابلات الشخصية مع الطلبة المقبلين على التخرج.
- ✓ المقابلات مع الجهات المستفيدة وشركات التوظيف.

### ٩. طرائق التقييم

- المشاركة في قاعة الدرس الحضورية او الالكترونية.
- تقديم التقارير المختبرية.
- تقييم التنفيذ العملي للتجارب.
- تقديم الانشطة المختلفة.
- اختبارات يومية وفصلية ونهائية حضوريا والكترونيا.

## ١٠. الهيئة التدريسية

ت	الاسم الكامل واللقب	اللقب العلمي	الشهادة	الاختصاص العام	الاختصاص الدقيق
1	خالد خليل محمد جاسم	أستاذ	هندسة كهرباء	الالكترونيك واتصالات	دكتوراه
2	قيس ذنون نجم عبد الله ال احمد جاسم	أستاذ	علوم فيزياء	بلازما	دكتوراه
3	احمد ذنون يونس حسين النقيب	أستاذ مساعد	هندسة كهرباء	الالكترونيات الدقيقة	دكتوراه
4	مجاهد فهمي إبراهيم إسماعيل العزو	أستاذ مساعد	هندسة كهرباء	اتصالات	دكتوراه
5	اوس زهير يونس سليمان	أستاذ مساعد	هندسة حاسوب	اتصالات	دكتوراه
6	حارث احمد محمد احمد البدراني	أستاذ مساعد	هندسة كهرباء	الالكترونيات القدرة	دكتوراه
7	احمد محمد احمد سلامة	أستاذ مساعد	هندسة كهرباء	اتصالات	دكتوراه
٨	هشام سوادى هاشم	أستاذ مساعد	دكتوراه	تاريخ	تاريخ حديث
9	عمر بدر محمد خضر النعيمي	مدرس	هندسة كهرباء	الالكترونيات الدقيقة	دكتوراه
10	إيهاب عصام داؤد سليمان الراوجي	مدرس	هندسة حاسبات	اتصالات	دكتوراه
11	سحر لازم قدوري خضير الدليمي	مدرس	هندسة حاسبات	حاسوب ومعلوماتيه	دكتوراه
12	سرمد فخر الدين إسماعيل جاسم المولى	مدرس	هندسة حاسبات	تحليل ومعالجة الصورة الرقميه	دكتوراه
13	سنان خالد محمد حسن شنشل	مدرس	هندسة كهرباء	الالكترونيك و اتصالات	ماجستير
14	نور طلال محمود عزيز كداوي	مدرس	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
15	خالد فزع محمود محمد	مدرس	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
16	عماد عبد الحلیم عبدو علي ال ملا خضر	مدرس	هندسة الكترونيك	الالكترونيك	ماجستير
17	عبد الحميد محمد جاسم محمد الجبوري	مدرس	هندسة الكترونيك	الالكترونيك	ماجستير
18	همسة فواز ذنون محمد الرحو	مدرس	هندسة كهرباء	حاله صلبه	ماجستير
19	هبة عبد الخالق حمدون عبد الصواف	مدرس	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
20	شوكت محمد يونس مال الله	مدرس مساعد	هندسة كهرباء	الالكترونيات القدرة	ماجستير
21	زهراء صديق يحيى احمد الصانع	مدرس مساعد	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
22	أمنة إدريس كنعان سليمان حيو	مدرس مساعد	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
23	أسماء نبيل خليل عمر	مدرس مساعد	هندسة حاسبات	حاسوب ومعلوماتيه	ماجستير
24	محمد إبراهيم محمد احمد	مدرس مساعد	هندسة كهرباء	الالكترونيات القدرة	ماجستير
25	همام ماهر عبد شاهين الحمداني	مدرس مساعد	هندسة الكترونيك	الالكترونيك	ماجستير
26	يونس صابر عثمان خطاب الرفاعي	مدرس مساعد	هندسة حاسبات	حاسبات	ماجستير
27	حارث حازم ذنون يونس	مدرس مساعد	هندسة الكترونيك	الالكترونيك	ماجستير
28	يعرب عبد المحسن احمد حسين الشلاوي	مدرس مساعد	هندسة كهرباء	الالكترونيك	ماجستير
29	سنان محمود أيوب محمود الرحو	مدرس مساعد	هندسة الكترونيك	الالكترونيك	ماجستير
30	محمد صالح سفر رسول	مدرس مساعد	هندسة ميكانيك	هندسة الحراريات	ماجستير
31	عامر طلال علي أحمد	مدرس مساعد	هندسة الكترونيك	حاسوب ومعلوماتيه	ماجستير
32	هاني محمد صالح سلمان	مدرس مساعد	هندسة ميكانيك	هندسة الحراريات	ماجستير
33	رشا وليد حمد	مدرس مساعد	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
34	عمر نجيب سعدي	مدرس مساعد	هندسة كهرباء	الالكترونيك واتصالات	ماجستير
35	ميسرة عبدالجبار قاسم	مدرس مساعد	هندسة كهرباء	قدرة ومكانن	ماجستير
36	هشام محمد محمود	مدرس مساعد	هندسة كهرباء	الالكترونيات القدرة	ماجستير
37	هاجر خليل ابراهيم أحمد	مدرس مساعد	هندسة الكترونيك	ألكترونيك	ماجستير



38	محمد صالح سفر رسول	مدرس مساعد	هندسة ميكانيك	هندسة الحرارية	ماجستير
39	نجم عبيد ضحوي	لا يوجد	هندسة	هندسة	بكلوريوس
40	طارق حسين خضر	لا يوجد	هندسة	هندسة	بكلوريوس
41	مروه عصام احمد	لا يوجد	هندسة	هندسة	دبلوم عالي
42	عادل غازي شريف	لا يوجد	هندسة	هندسة	بكلوريوس
43	محمد موفق هادي	لا يوجد	هندسة	هندسة	بكلوريوس
44	أسعد عبد الغني صالح	لا يوجد	هندسة	هندسة	بكلوريوس
45	يثراب وليد قاسم خليل	لا يوجد	هندسة	هندسة	بكلوريوس
46	سيف الدين كمال	لا يوجد	هندسة	هندسة	بكلوريوس
47	عمار احمد عبد الله	لا يوجد	هندسة	هندسة	بكلوريوس
48	لؤلؤه حازم فتح الله	لا يوجد	إدارة واقتصاد	إدارة واقتصاد	بكلوريوس
49	ادريس محمد يونس احمد	لا يوجد	معهد	معهد	دبلوم

### ١١. معيار القبول

ان خطة القبول المعتمدة للطلبة الجدد في برامج القسم تتبع بطبيعة الحال خطة القبول المركزي لوزارة التعليم العالي والبحث العلمي وتنفذ من قبل الجامعة والكلية. يمكن القول بان الطلبة الملتحقين ببرامج القسم يمثلون المستويات العليا بمعدلاتهم من المتقدمين الى كلية هندسة الالكترونيات، حيث يعتمد مبدأ المفاضلة على المعدل للدراسة الاعدادية ورغبة الطالب في تحديد البرنامج الدراسي ضمن برامج كلية هندسة الالكترونيات. لذا فان طبيعة من يقبل من الطلبة في برامج القسم هم متميزون بمستوياتهم الدراسية والفكرية وعطائهم طوال فترة البرنامج .

### ١٢. أهم مصادر المعلومات عن البرنامج

يمكن الحصول على بيانات وافية عن برامج القسم من خلال زيارة الموقع الالكتروني الرسمي لجامعة نينوى وتصفح موقع كلية هندسة الالكترونيات:

[www.uoninevah.edu.iq](http://www.uoninevah.edu.iq)

تقرير التقييم الذاتي ودليل القسم السنوي ضمن دليل الجامعة والكلية..

### ١٣. خطة تطوير البرنامج

تحديث المقرر سنويا حسب احدث الكتب العلمية ذات الصلة ومتطلبات سوق العمل بالإضافة الى استخدام شبكة المعلومات الدولية للاطلاع على مفردات مناهج المادة في الجامعات العالمية الأخرى والرائدة في هذا المجال.

## مخطط مهارات المنهج

يرجى وضع اشارة في المربعات المقابلة لمخرجات التعلم الفردية من البرنامج الخاضعة للتقييم

مخرجات التعلم المطلوبة من البرنامج

المهارات العامة والتأهيلية المنقولة (المهارات الأخرى المتعلقة بقبالية التوظيف والتطور الشخصي)	الاهداف الوجدانية والقيمية				الاهداف المهاراتية الخاصة بالبرنامج				الاهداف المعرفية				أساسي أم اختياري	اسم المقرر	رمز المقرر	السنة / المستوى		
	د	د	د	د	ج	ج	ج	ج	ب	ب	ب	ب					أ	أ
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Mathematics I	NVEE206	الاول
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	DC Circuits Analysis	NVEE215	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Physical Electronics	NVEE218	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Computer science	NVEEELM111	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	Mechanical engineering principles	NVEE203	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	Democracy and Human Rights	NVU12	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	AC Circuits Analysis	NVEE216	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Mathematics II	NVEE207	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Physics Of Semiconductor	NVEE219	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital Techniques	NVEE217	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	Engineering Drawing	NVEE201	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	English	NVU11	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Analysis I	NVEE208	الثاني / طبي
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Signal Analysis	NVEEELM211	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic I	NVEEELM212	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital design	NVEE223	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electromagnetic fields I	NVEE215	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Human Physiology	NVEEELM 213	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	signals and systems	NVEE210	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Analysis II	NVEE209	

*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic II	NVEEELM221	الثاني صناعي
	*	*			*		*	*	*	*	*	*	*	*	*	أساسي	programming	NVEEELM222	
	*	*			*		*	*	*	*	*	*	*	*	*	أساسي	Electromagnetics FieldsII	NVEE221	
	*	*			*		*	*	*	*	*	*	*	*	*	ثانوي	The Crimes of the Defunct Baath Part	NVU13	
*	*	*			*		*	*	*	*	*	*	*	*	*	أساسي	Engineering Analysis I	NVEE208	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic I	NVEEELI212	
	*	*			*		*	*	*	*	*	*	*	*	*	أساسي	DC Machines	NVEEELI213	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Computer Programming	NVEEELI214	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	The crimes of the defunct Baath Party	NVU13	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Fundamentals of Electromagnetics	NVEE221	
	*	*	*		*	*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Analysis II	NVEE209	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronics II	NVEEELI222	
	*	*			*	*	*	*	*	*	*	*	*	*	*	أساسي	AC Machines	NVEEELI223	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Computer Languages	NVEEELI224	
	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital Design	NVEE223	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Signals and Systems	NVEE210	
	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic II	EE3301	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital Signal Processing	EE3201	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Control Engineering	EE3302	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microprocessors	EE3303	
*			*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	DIGITAL SYSTEM DESIGN	EE3304	
*		*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Communications	EE3305	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	ELECTRONIC INSTRUMENTATION	EE3306	
	*	*		*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Laboratory	EE3307	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Industrial Electronic	EE4301	الرابع
*	*		*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	DATA TRANSMISSION& COMPUTER ETWORKS	EE4302	
*		*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microprocessor & Micro Controller	EE4303	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microelectronics	EE4304	

*	*	*		*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microwave Engineering	EE405	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Computer aided design	EE4306	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Project	EE4201	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Laboratory	EE4307	

وصف المقررات

## Courses specification

للعام الدراسي ٢٠٢٣ - ٢٠٢٤

جامعة نينوى

كلية هندسة الالكترونيات

قسم هندسة الاكترونيك

## Courses specification for first class (First Course)

Module Information			
<b>Module Title</b>	<b>D.C Circuits Analysis</b>		<b>Module Delivery</b>
<b>Module Type</b>	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEE215		
<b>ECTS Credits</b>	5		
<b>SWL (hr/sem)</b>	125		
<b>Module Level</b>	1	<b>Semester of Delivery</b>	
<b>Administering Department</b>		<b>College</b>	
<b>Module Leader</b>	Zahraa Siddiq Yahya	<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>	Lecturer assistant	<b>Module Leader's Qualification</b>	
<b>Module Tutor</b>	Zahraa Siddiq Yahya	<b>e-mail</b>	E-mail
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>	01/06/2023	<b>Version Number</b>	1.0

Relation with other Modules			
<b>Prerequisite module</b>		<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	1. To identify the basic concepts of DC Electrical Eng. circuits.

	<ol style="list-style-type: none"> <li>2. To understand how is the calculation of current, voltage, and power.</li> <li>3. To understand and cover the basic DC circuit analysis methods and theorems.</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Explain the function of each element in DC Electrical circuits.</li> <li>2. Use the basic circuit analysis methods and theorems to simplified the DC Electrical circuits.</li> <li>3. Explain the different between transformation methods.</li> <li>4. Applying the appropriate analysis method to reach the aim in its simplest form.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A – BASIC CONCEPTS:</u></p> <p>Voltage &amp; current; Power &amp; Energy; Dependent and Independent sources; Ohm’s laws series &amp; parallel connections; Delta- star connections and transformations. [15 hrs]</p> <p><u>Part B - D.C. Network Theorems:</u></p> <p>Source transformation; Linearity &amp; superposition; Thevenin’s &amp; Norton’s Theorems; Source transportation; source superposition; Nodal analysis; Mesh analysis. [35 hrs]</p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	<p>Type something like: The main strategy that will be adopted in delivering this module is to encourage students’ participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering types of simple experiments involving some sampling activities that are interesting to the students.</p>

Student Workload (SWL)			
Structured SWL (h/sem) ↓	30	Structured SWL (h/w)	4
Unstructured SWL (h/sem)	30	Unstructured SWL (h/w)	4
Total SWL (h/sem)	60		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	Assignments	1	10% (10)	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
Summative assessment	Midterm Exam	1.5 hr	10% (10)	10	LO #(1-8)
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		



## Delivery Plan (Weekly Syllabus)

	<b>Material Covered</b>
<b>Week 1</b>	Voltage & current
<b>Week 2</b>	Power & Energy
<b>Week 3</b>	Dependent and Independent sources
<b>Week 4</b>	Ohm's laws
<b>Week 5</b>	series & parallel connections
<b>Week 6</b>	Delta- star connections and transformations
<b>Week 7</b>	Kirchhoff's Current & Voltage Laws (KCL), (KVL)
<b>Week 8</b>	Source transformation
<b>Week 9</b>	Linearity & superposition
<b>Week 10</b>	Nodal analysis
<b>Week 11</b>	Mesh analysis
<b>Week 12</b>	Thevenin's Theorem
<b>Week 13</b>	Norton's Theorem
<b>Week 14</b>	Max. power transfer
<b>Week 15</b>	<b>Preparatory week before the final Exam</b>

## Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	" Engineering Circuit Analysis" By W. Hayt	Yes
<b>Recommended Texts</b>	"Introductory Circuit Analysis" By Boylested	Yes

## Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
Module Title	<b>Computer science</b>		Module Delivery
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab Tutorial Practical <input type="checkbox"/> Seminar
Module Code	<b>NVEEELM114</b>		
ECTS Credits	4		
SWL (hr/sem)	<b>100</b>		
Module Level	1	Semester of Delivery	1
Administering Department	ELM	College	NE
Module Leader	Asmaa Nabeel	e-mail	asmaa.khaleel@uoninevah.edu.iq
Module Leader's Acad. Title	Lecturer Assist	Module Leader's Qualification	M.Sc.
Module Tutor		e-mail	
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	4/7/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<p>General overview of personal computer architecture</p> <p>Computer peripherals, keyboard, screen, mouse, and storage media</p> <p>Computer busses, ports, interfaces</p> <p>Overview of MSDOS operating system</p> <p>MSDOS internal commands</p> <p>MSDOS external commands</p> <p>Introduction to computer languages</p> <p>Overview of windows operating system</p> <p>Windows desktop, changing settings, starting programs</p> <p>Creating, deleting, copying, moving, searching for files and folders</p> <p>Using my computer, my document, and help facility</p> <p>Using windows control panel</p> <p>Using the windows accessories paint, notepad, word pad, ....etc</p> <p>Setup applications to windows, remove applications from windows</p> <p>Connecting to the internet, using the windows explorer</p> <p>Using the Microsoft Word</p> <p>Using the Microsoft Excel</p> <p>Using the Matlab</p>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Understanding the important components of the computer and its operating system.</li> <li>2. Understanding the meaning of MSDOS operating system and its commands.</li> <li>3. Understanding the windows operating system</li> <li>4. Understanding the Microsoft office (word, power point, excel).</li> <li>5. Understanding the high and low level languages</li> <li>6. Learn about how the strings represented in C language.</li> <li>7. introduction to matlab</li> </ol>
<b>Indicative Contents</b>	<ol style="list-style-type: none"> <li>1. explain the components of computer hardware and software</li> <li>2. introduction to the types of computers</li> <li>3. storage media</li> <li>4. computer ports</li> <li>5. computer networks and the types of it</li> <li>6. the internal and external MSDOS commands</li> <li>7. windows operating system</li> <li>8. word office program</li> <li>9. power point office program</li> <li>10. Excel program</li> <li>11. Matlab</li> </ol>

## Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	125	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	65	<b>Unstructured SWL (h/w)</b>	2
<b>Total SWL (h/sem)</b>	190		

## Module Evaluation

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	4	10% (10)	۲, 4, 5,6	LO #1, 2, 10 and 11
	<b>Assignments</b>	1	10% (10)	14	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	.	.	.	LO # 3, 4, 6 and 7, 5, 8 and 10
	<b>Report</b>	۱	۲.	۱۴	
<b>Summative assessment</b>	<b>Midterm Exam</b>	۱.5hr	30% (20)	10	LO # 1-4
	<b>Final Exam</b>	3hr	50% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

Material Covered	
<b>Week 1</b>	<b>Introduction</b> to the part of computers in hardware and software ,computer types, storage media
<b>Week 2</b>	<b>Explain the computer ports , computer networks</b>
<b>Week 3</b>	<b>Introduction to MSDOS operating system and the internal commands of it</b>

<b>Week 4</b>	<b>External Ms DOS command , file and folder related commands and the editor</b>
<b>Week 5</b>	Windows operating system
<b>Week 6</b>	Windows orders(change the background, screen saver, resolution), change the status of files, printing files, copy and save of files, backups, Recycle bin ,compressing files, viruses
<b>Week 7</b>	Microsoft office word (creating new word file, bars , types and styles of fonts, copy and select of texts ,save of word file)
<b>Week 8</b>	MS WORD: spell checking, inserting symbols, add borders, change the document setup , insert table, page numbering, insert equations and effects)
<b>Week 9</b>	MS Power point:(how to design professional presentation, change the layout of presentation and background of it, numbering slides, insert charts , insert table and audio )
<b>Week 10</b>	MS Power point(insert an effect to the object in slide, transition between slides , grouping of objects, insert equation, copy ,save and printing the slides then how to start the presentation )
<b>Week 11</b>	MS EXCEL (getting started with excel, how to create a spreadsheet, copy and rename the work book, entering and deleting of data in sheet, insert and delete of rows& columns, selecting cells, adding border to sheet)
<b>Week 12</b>	MS EXCEL:how to write a formule in sheet, functions, summation of data in row or column ,average function, max& min functions, count& counta, round function, save and print the spread sheet
<b>Week 13</b>	Overview of High &Low level languages
<b>Week 14</b>	<b>Matlab</b>
<b>Week 15</b>	
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

### Delivery Plan (Weekly Lab. Syllabus)

<b>Delivery Plan (Weekly Lab. Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1-15</b>	The application of each part of the covered drawing subject theoretically and according to the weekly sequence of the curriculum in the AutoCAD laboratory  <b>Note: By two hours a week</b>

## Learning and Teaching Resources

	Text	Available in the Library?
Required Texts	1. "Computer Science"	No
Recommended Texts	2. "MATLAB Handbook"	No
Websites	<a href="https://www.tutorialsmate.com/2021/12/parts-of-computer">https://www.tutorialsmate.com/2021/12/parts-of-computer</a> <a href="https://www.koenig-solutions.com/matlab-programming">https://www.koenig-solutions.com/matlab-programming</a>	

## Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information				
معلومات المادة الدراسية				
Module Title	Mathmatics I		Module Delivery	
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE206			
ECTS Credits	٦			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery		1
Administering Department	Electronic Eng. Dep.	College	Electronics Engineering	
Module Leader	Hani M. S. Salman		e-mail	hani.mohamed@uoninevah.edu.iq
Module Leader's Acad. Title	Assistant Lecturer		Module Leader's Qualification	MSc
Module Tutor	Name (if available)		e-mail	E-mail
Peer Reviewer Name	Name		e-mail	E-mail
Scientific Committee Approval Date		Version Number	1.0	

Relation with other Modules				
العلاقة مع المواد الدراسية الأخرى				
Prerequisite module	None		Semester	
Co-requisites module	None		Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	<ol style="list-style-type: none"> <li>4. Gain proficiency in differentiating trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.</li> <li>5. Master differentiation techniques for various types of functions.</li> <li>6. To learn how to sketch curves and to deal with the transcendental functions.</li> <li>7. To increase the skills related to differentiation applications.</li> <li>8. Develop a strong foundation in Integration of trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.</li> </ol>



	<ol style="list-style-type: none"> <li>9. Understand the concept of Application of the definite integral, including finding volumes of revolution, lengths of curves, and surface areas of revolution.</li> <li>10. To learn the methods of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions.</li> <li>11. Apply calculus principles to solve real-world engineering problems, developing problem-solving skills and the ability to apply calculus concepts to practical situations.</li> </ol>
<p><b>Module Learning Outcomes</b></p> <p>مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"> <li>1. Understand the concept of differentiation as a rate of change and slope of the curve.</li> <li>2. Understand the basic differentiation rules, chain rule, implicit differentiation, higher order differentiation, partial differentiation, Differentiation of trigonometric functions and Hyperbolic Functions.</li> <li>3. Learn the applications of differentiation.</li> <li>4. Solve Maximum and Minimum problems.</li> <li>5. Learn how to Plot the Curve.</li> <li>6. Learn Transcendental functions: graphs, and derivative.</li> <li>7. Understand the concept of integration: types of integrals. definite integrals, infinite Integrals. Integration of trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.</li> <li>8. Apply definite integration to as areas between curves, volumes of revolution, length of the curve and surface area of revolution.</li> <li>9. Learn Methods of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions.</li> <li>10. Develop critical thinking and problem-solving skills by applying calculus.</li> </ol>
<p><b>Indicative Contents</b></p> <p>المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p><u>Part A – Differentiation:</u>  Definitions and notations, basic differentiation rules, chain rule, implicit differentiation, higher order differentiation, partial differentiation, Differentiation of trigonometric functions and Hyperbolic Functions: .  Applications of differentiation – slope tangents and normal, rate of change, velocity and acceleration, maxima and minima and inflexion points, and Curve plotting. [16 hrs]</p> <p>Transcendental Functions – definitions, properties, graphs, derivative. [4 hrs]</p> <p><u>Part B – Integration:</u>  Definitions and notations, types of integrals: definite integrals, infinite Integrals. Integration of trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function. [12 hrs]</p>

	Application of the definite integral – areas between curves, volumes of revolution, length of the curve and surface area of revolution. [12 hrs]
	Methods Of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions. [16 hrs]

Learning and Teaching Strategies	
استراتيجيات التعلم والتعليم	
<b>Strategies</b>	This module's major aim is to foster student engagement, improve critical thinking abilities, and promote collaborative learning. Interactive seminars, interesting tutorials, and exercises encourage active participation, allowing students to hone their critical thinking skills and apply engineering mathematics principles to problem solving. Furthermore, students collaborate on engineering mathematics issues, examine real-world scenarios, and explore the practical applications of the principles acquired through group activities, projects, and conversations. This method not only increases students' comprehension of engineering mathematical concepts, but it also fosters cooperation, communication, and key interpersonal skills that will be useful in their future engineering activities.

Student Workload (SWL)			
الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	62	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	88	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	5.9
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>150</b>		

Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	30% (30)	5 and 10	LO #1 #2 #3 and #4, #7, #8
	<b>Assignments</b>	1	10% (10)	12	LO #7 - #9
	<b>Projects / Lab.</b>	-	-	-	-
	<b>Report</b>	-	-	-	-
	<b>Midterm Exam</b>	2hr	10% (10)	7	LO #1 - #6

<b>Summative assessment</b>	<b>Final Exam</b>	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

#### المنهاج الاسبوعي النظري

	<b>Material Covered</b>
<b>Week 1</b>	Introduction – Differentiation definitions and notations, review of basic differentiation rules, chain rule, and Implicit differentiation.
<b>Week 2</b>	Partial differentiation and higher order differentiation.
<b>Week 3</b>	Differentiation of trigonometric functions and Hyperbolic Functions. Applications of differentiation; slope, tangents and normal.
<b>Week 4</b>	Rate of change, velocity and acceleration, maxima and minima, inflexion points and Curve plotting
<b>Week 5</b>	Transcendental Functions – definitions, properties, and graphs, derivative.
<b>Week 6</b>	Definitions and notations of integration, Types of integrals: definite integrals and infinite integrals. Integration of trigonometric function.
<b>Week 7</b>	Integration of inverse trigonometric function, hyperbolic function. <a href="#">Mid-term Exam</a>
<b>Week 8</b>	Integration of inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.
<b>Week 9</b>	Application of the definite integral – areas between curves, volumes of revolution, length of the curve and surface area of revolution.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	Methods Of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions.
<b>Week 13</b>	
<b>Week 14</b>	
<b>Week 15</b>	

Learning and Teaching Resources		
مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	G. B. Thomas Jr., M. D. Weir, J. Hass, and F. R. Giordano, "Thomas' Calculus," 12th ed., Pearson, 2019.	Yes
Recommended Texts		
Websites	<a href="https://www.coursera.org/learn/introduction-to-calculus#syllabus">https://www.coursera.org/learn/introduction-to-calculus#syllabus</a> <a href="https://www.edx.org/learn/calculus">https://www.edx.org/learn/calculus</a> <a href="https://www.khanacademy.org/math/calculus-1">https://www.khanacademy.org/math/calculus-1</a>	

Grading Scheme				
مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

Module Information				
Module Title	Physical Electronics		Module Delivery	
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE218			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery		1
Administering Department	Electronic	College	Ninevah university	
Module Leader	Hamsa Fawaz Thanoon		e-mail	hamsa.thanoon@uoninevah.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc	
Module Tutor			e-mail	E-mail
Peer Reviewer Name			e-mail	E-mail
Scientific Committee Approval Date	04/07/2023	Version Number	1.0	

Relation with other Modules				
Prerequisite module			Semester	
Co-requisites module	None		Semester	

Module Aims, Learning Outcomes and Indicative Contents	
Module Aims	1. To develop problem solving skills and understanding of Atomic Structure 2. To understand Energy band structure of metal, insulator, and semiconductor.

	<ol style="list-style-type: none"> <li>3. To understand Properties of intrinsic P and N type semiconductors.</li> <li>4. To understand Electrical conduction in intrinsic semiconductor.</li> <li>5. To understand Properties of extrinsic semiconductors.</li> <li>6. To understand Electrical conduction in extrinsic semiconductor</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Recognize how semiconductors works in electronics circuits.</li> <li>2. List the various terms associated with electronics circuits.</li> <li>3. Summarize what is meant by a basic of semiconductors.</li> <li>4. Discuss the reaction and involvement of semiconductors in generate the currents.</li> <li>5. Describe mobility of electrons and conductivity in metals.</li> <li>6. Define Ohm's law.</li> <li>7. Identify the pure semiconductors.</li> <li>8. Identify the impure semiconductors</li> <li>9. Discuss the impure semiconductors N and P types</li> <li>10. Explain the type of electronic emission.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A - Energy Bands in Solids</u>  <u>Describe the structure of an atom ♦ Discuss insulators, conductors, and semiconductors and how they differ. [9 hrs]</u></p> <p><u>Revision problem classes [3 hrs]</u></p> <p><u>Part B - Transport Phenomena in Semiconductor</u>  <u>Describe how current is produced in a semiconductor ♦ Describe the properties of n-type and p-type semiconductors. [30 hrs]</u></p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills.

## Student Workload (SWL)

Structured SWL (h/sem)	111	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	65	Unstructured SWL (h/w)	2
Total SWL (h/sem)	176		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	Quizzes	4	10	[2,4,5,6]	LO (#1- #12)
	Assignments	2	10	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
<b>Summative assessment</b>	Midterm Exam	1.5 hr	20% (20)	10	LO #(1-8)
	Final Exam	3 hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

	<b>Material Covered</b>
<b>Week 1</b>	Energy Bands in Solids
<b>Week 2</b>	Fermi-Dirac distribution function
<b>Week 3</b>	Properties of intrinsic P and N type semiconductors
<b>Week 4</b>	Mobility and conductivity
<b>Week 5</b>	Electrical conduction in intrinsic semiconductor
<b>Week 6</b>	Hall Effect
<b>Week 7</b>	Generation and recombination of charges
<b>Week 8</b>	Diffusion current continuity equation
<b>Week 9</b>	Injection minority carrier charges
<b>Week 10</b>	N-type semiconductor
<b>Week 11</b>	Solved problems
<b>Week 12</b>	P-type semiconductor
<b>Week 13</b>	Solved problems
<b>Week 14</b>	Photo-conductivity
<b>Week 15</b>	Preparatory week before the final Exam



## Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	1. "INTEGRATED ELECTRONICS" By MILLMAN & HALKIET 2. "SEMICONDUCTOR DEVICES & CIRCUITS", JOHN WILEY & SONS	Yes
<b>Recommended Texts</b>	1. (Floyd) 2. ثیراجا فصل ۵۱	Yes

## Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX - Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F - Fail</b>	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Module Information

<b>Module Title</b>	<b>Mechanical Engineering Principle</b>		<b>Module Delivery</b>		
<b>Module Type</b>	<u>Base</u>		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
<b>Module Code</b>	NVEE203				
<b>ECTS Credits</b>	<u>6</u>				
<b>SWL (hr/sem)</b>	<u>150</u>				
<b>Module Level</b>		1	<b>Semester of Delivery</b>		1
<b>Administering Department</b>		Type Dept. Code	<b>College</b>	Type College Code	
<b>Module Leader</b>			<b>e-mail</b>		
<b>Module Leader's Acad. Title</b>		Lecturer	<b>Module Leader's Qualification</b>		M.Sc.
<b>Module Tutor</b>			<b>e-mail</b>		
<b>Peer Reviewer Name</b>		Name	<b>e-mail</b>	E-mail	
<b>Scientific Committee Approval Date</b>		2/07/2023	<b>Version Number</b>	1.0	

## Relation with other Modules

<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<p style="text-align: right;">Students will be able to:</p> <ol style="list-style-type: none"> <li>1. Students will be able to:</li> <li>2. Knowing the different methods of making calculations related to forces and their effects on two- and three-dimensional systems</li> <li>3. Clarify that the subject represents a very important introduction to other subjects for the later stages of the student's study and building a scientific base for the student to ensure the possibility of understanding the relevant subjects in the later stages.</li> <li>4. The student will learn different applications of commonly used Mechanical machinery.</li> <li>5. The student will learn strong basics of Mechanical Engineering fundamentals.</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Have understood and overcome any misconceptions about basic concepts in physics (force, energy, work etc).</li> <li>2. Restate existing problem solving skills in a form more suitable for engineering applications.</li> <li>3. Interpret basic engineering applications of mechanics in more detail.</li> <li>4. Acquire four basic thinking skills:             <ol style="list-style-type: none"> <li>1. Perceive, or resolve, contradictions involving their preconceptions about mechanics.</li> <li>2. Organize the basic ideas of mechanics in a form suitable for problem solving.</li> <li>3. Apply basic principles in mechanics to realistic engineering situations.</li> <li>4. Solve realistic engineering problems.</li> </ol> </li> </ol>
<b>Indicative Contents</b>	<p style="text-align: right;"><b>Indicative content includes the following:-</b></p> <p style="text-align: right;"><b>Statics – Introduction [25 hrs]</b></p> <ul style="list-style-type: none"> <li>○ Vectors</li> <li>○ Newton's Laws</li> <li>○ Fundamental Units</li> <li>○ Types of force</li> <li>○ Parallelogram law</li> <li>○ Resultant forces</li> <li>○ Moments and couples</li> <li>○ Moment of couples</li> <li>○ Equilibriums</li> <li>○ Free body diagram</li> <li>○ Coplanar system</li> <li>○ Friction: Nature of friction; Theory of friction; Coefficient of friction</li> </ul> <p style="text-align: right;"><b>Dynamics – Introduction [20 hrs]</b></p> <ul style="list-style-type: none"> <li>○ Basic concepts</li> <li>○ Newton's Laws</li> <li>○ Formulation and solution of problems</li> <li>○ Kinematics of Particles</li> <li>○ Rectilinear motion</li> </ul>

	<ul style="list-style-type: none"> <li>○ Curvilinear motion</li> <li>○ Relative motion</li> <li>○ Kinetics of Particles</li> <li>○ Newton's second Law</li> <li>○ Work and energy</li> <li>-</li> </ul>
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## Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	25	<b>Structured SWL (h/w)</b>	2
<b>Unstructured SWL (h/sem)</b>	20	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	45		

## Module Evaluation

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	6	5% (5)	, 9, 12, 13, 15 <sup>a</sup>	LO #1, 2, 10 and 11
	<b>Assignments</b>	6	5% (5)	, 9, 12, 13, 15 <sup>a</sup>	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	0	0%		
	<b>Report</b>	0	0%	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	3hr	30% (30)	10	LO # 1-7
	<b>Final Exam</b>	3hr	60% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

Week	Material Covered
Week 1	Force system; Units system;

Week 2	Parallelogram law; Forces + components
Week 3	Resultant of coplanar forces
Week 4	Components of force in space
Week 5	Moment of a force
Week 6	Moment of a force
Week 7	Moment of a force
Week 8	Free body diagram; Coplanar system
Week 9	Friction: Nature of friction; Theory of friction
Week 10	Coefficient of friction
Week 11	Coefficient of friction
Week 12	Coefficient of friction
Week 13	Normal and tangential components of acceleration
Week 14	Normal and tangential components of acceleration
Week 15	
Week 16	Normal and tangential components of acceleration

### Learning and Teaching Resources

	Text	Available in the Library?
Required Texts	1. Engineering Mechanics (statics) By: J.L. MERIAM 2. Engineering Mechanics (Dynamics) By: J.L. MERIAM	Yes
Recommended Texts	➤	No
Websites		

### Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.

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## Module Information

### معلومات المادة الدراسية

<b>Module Title</b>	<b><u>Democracy and Human Rights</u></b>		<b>Module Delivery</b>	
<b>Module Type</b>	<b><u>Basic</u></b>		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	<b><u>NV12</u></b>			
<b>ECTS Credits</b>	<b><u>2</u></b>			
<b>SWL (hr/sem)</b>	<b><u>50</u></b>			
<b>Module Level</b>	1	<b>Semester of Delivery</b>		
<b>Administering Department</b>	Dept. of Electronic	<b>College</b>	EE	
<b>Module Leader</b>	Husham swadi hashim	<b>e-mail</b>	<a href="mailto:Husham.hashim@uoninevah.edu.iq">Husham.hashim@uoninevah.edu.iq</a>	
<b>Module Leader's Acad. Title</b>	Assistant Professor	<b>Module Leader's Qualification</b>	PHD	
<b>Module Tutor</b>		<b>e-mail</b>		
<b>Peer Reviewer Name</b>		<b>e-mail</b>		
<b>Scientific Committee Approval Date</b>	01/06/2023	<b>Version Number</b>	1.0	

## Relation with other Modules

### العلاقة مع المواد الدراسية الأخرى

<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

### أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<b>Module Aims</b> أهداف المادة الدراسية	<p>١ - شرح مفهومي حقوق الانسان والديمقراطية</p> <p>٢ - بيان اهمية حقوق الانسان في حياتنا العامة وعلى جميع الصعد ( الدراسية و الوظيفية و الاجتماعية .. الخ )</p> <p>٣ - بيان اهمية ايجاد مفهوم واعى لمصطلح الديمقراطية ضمن انظمة الحكم وتأثيرها على الاستقرار السياسي</p> <p>٤ - ضرورة فهم الترابط الوثيق ما بين حقوق و بناء مجتمع ديمقراطي يضمن حرية افرادة وضمان مصالحهم</p> <p>٥ - ضرورة التركيز على ان بناء مفهوم حقيقي لحقوق الانسان ومجتمع ديمقراطي لا يكون الا من خلال بين قوانين تضمن ذلك واهمية هذه القوانين في بناء مجتمع مستقر يضمن لجميع افرادة حقوقهم ضمن نظام سياسي ديمقراطي</p>
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	<p>١ - ترسيخ قيم الحرية والمساواة في اسس المشاركة الفعلية في بناء المجتمع</p> <p>٢ - العمل على بناء بيئة حقيقية مستقرة من خلال تطبيق القوانين ضمن مجتمع ديمقراطي</p> <p>٣ - والسعي لتوفير اسس لحماية الافراد ضمن المجتمعات الديمقراطية</p>
<b>Indicative Contents</b> المحتويات الإرشادية	<p>-القسم الأول:- التطور التاريخي لحقوق الإنسان</p> <p>أولاً:- المجتمعات البدائية</p> <p>- مرحلة ما قبل التاريخ</p>

- الحضارات الشرقية (بلاد وادي الرافدين والحضارة الفرعونية نموذجاً)

نموذجاً - الحضارات الغربية (اليونانية

والرومانية

ثانياً:- الشرائع السماوية

- الديانة اليهودية

- الديانة المسيحية

- الديانة الإسلامية(بصوره أكثر تفصيلاً)

ثالثاً:- تطور حقوق الانسان في القوانين الوضعية

نظرية العقد

الاجتماعي

-

-الحروب العالمية وأثرها في حقوق

الانسان

- التنظيم الدولي

القسم الثاني :- حقوق الإنسان التعريف بها وأنواعها

أولاً- التحديد والتعريف

- الحق في الفقه الإسلامي

- الحق في الفقه

القانوني

-

تعريف حقوق

الإنسان

ثانياً- تقسيمات حقوق الإنسان (وتتم بدراسة مفصلة ومقارنة بين القانون والشرعية الإسلامية)

الحقوق الجماعية(حق تقرير المصير, حق التنمية, الحق في بيئة مناسبة, حق الإنسان في العيش بسلام)-

الحقوق الفردية (الحقوق الاقتصادية والثقافية, الحقوق المدنية والسياسية الحقوق الصبغة بالشخصية)-

القسم الثالث:- ضمانات احترام وحماية حقوق

الإنسان

أولاً - الضمانات في الشرعية الإسلامية

ثانياً:- الضمانات على الصعيد الوطني

ثالثاً:- الضمانات على الصعيد الدولي

مفردات ماده الديمقراطية

الكورس الأول:- يتضمن ماده الحريات العامة بين الشرعية والقانون

الكورس الثاني:- يتضمن ماده نظم إدارة الدولة بين الشرعية والقانون

الحريات العامة (بين الشرعية والقانون )

أولاً:- المقدمة

ثانياً:- التعريف بالحريات العامة

- الأصل اللغوي

	<ul style="list-style-type: none"> <li>- الأصل التاريخي</li> <li>- الأساس القانوني</li> <li>- الأساس الشرعي</li> <li>ثالثاً:- أسس الحريات العامة</li> <li>- العدالة</li> <li>- المساواة</li> <li>- الحرية</li> <li>رابعاً:- الحريات العامة الو صفية</li> <li>- حرية الرأي</li> <li>- حرية الفكر</li> <li>- حرية الأعلام</li> <li>- المساواه</li> <li>خامساً:- الشريعة الإسلامية والحريات العامة</li> <li>- موقف الإسلام من المرأة (الميراث, الزواج, تولي الوظائف)</li> <li>- موقف الإسلام من حرية العقيدة</li> <li>نظم إدارة الدولة</li> <li>أولاً:- في تحديد النظم السياسية</li> <li>- فكره النظام السياسي</li> <li>- شرعية النظم السياسية</li> <li>- أنواع النظم السياسية</li> <li>ثانياً:- في النظام الديمقراطي</li> <li>- مقدمة تأصيلية</li> <li>- تعريف الديمقراطية</li> <li>-أركان ومرتكزات النظام الديمقراطي</li> <li>ثالثاً:- نماذج الديمقراطية</li> <li>- الديمقراطية المباشرة</li> </ul>
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<b>Learning and Teaching Strategies</b> <b>استراتيجيات التعلم والتعليم</b>	
<b>Strategies</b>	<p>اتباع طريقة التعليم المباشر من خلال عرض المادة وشرحها والاستعانة بالادوات التعليمية لشرحها من خلال توضيح اليات المفهوم العلمي لمصطلحي الديمقراطية و حقوق الانسان</p>



<b>Student Workload (SWL)</b> الحمل الدراسي للطالب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	16	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعياً	1
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	9	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعياً	0.5
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	25		

<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Report	1	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
	Material Covered
Week 1	التطور التاريخي لحقوق الانسان
Week 2	الشرائع السماوية
Week 3	تطور حقوق الانسان في القوانين الوضعية
Week 4	حقوق الانسان التعريف بما وانواعها
Week 5	ضمانات احترام وحماية حقوق الانسان
Week 6	الضمانات في الشريعة وعلى الصعيدين الوطني والدولي
Week 7	Mid-term Exam
Week 8	مفهوم الديمقراطية
Week 9	الحريات العامة بين الشريعة و القانون
Week 10	التعريف بالحريات العامة و اسس الحريات
Week 11	الشريعة الاسلامية والحريات العامة
Week 12	نظم ادارة الدولة
Week 13	الديمقراطية مقدمة تأصيلية
Week 14	اركان ومرتكبات النظام الديمقراطية

Week 15	نماذج الديمقراطية
Week 16	Preparatory week before the final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts		Yes
Recommended Texts		No
Websites		

Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	راسب (فيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

## Courses specification for first class (Second Course)

Module Information			
<b>Module Title</b>	<b>Digital Techniques</b>		<b>Module Delivery</b>
<b>Module Type</b>	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEE217		
<b>ECTS Credits</b>	5		
<b>SWL (hr/sem)</b>	125		
<b>Module Level</b>	1	<b>Semester of Delivery</b>	
<b>Administering Department</b>		<b>College</b>	Type College Code
<b>Module Leader</b>	(Younis Saber Othman), (Noor Alhuda Saad Abbas)	<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>	Lecturer Assistant	<b>Module Leader's Qualification</b>	M.Sc.
<b>Module Tutor</b>		<b>e-mail</b>	

<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>	4/7/2023	<b>Version Number</b>	1.0

<b>Relation with other Modules</b>			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ol style="list-style-type: none"> <li>1. To learn new number systems and how to convert between them</li> <li>2. To identify and learn the logic gates and Boolean algebra</li> <li>3. How to minimize the Boolean functions using Boolean algebra and Karnaugh maps</li> <li>4. To understand, draw, and identify the combinational logic circuits using the discrete logic</li> <li>5. To understand, draw, and identify the combinational logic circuits using the MSI integrated circuits</li> <li>6. To use the 3-variables and 4-variables Karnaugh map for Boolean minimization</li> </ol>
<b>Module Learning Outcomes</b>	<p>Students will be able to:</p> <ol style="list-style-type: none"> <li>1. Learning new number systems and how to convert between them</li> <li>2. Identify the logic gates and learn the Boolean algebra</li> <li>3. Minimize the Boolean functions</li> <li>4. Understand, draw, and identify the combinational logic circuits using the discrete logic and MSI integrated circuits</li> <li>5. Identify and use the 3-variables and 4-variables Karnaugh map</li> </ol>
<b>Indicative Contents</b>	<p><b>Indicative content includes the following:-</b></p> <p><b>NUMBER SYSTEMS:- [10 Hrs]</b>            Decimal number system; Binary; Octal and hexadecimal number systems; Conversion from one number to another number system; Addition; Subtraction; Multiplication and division using different number system; Representation of binary number insignia-magnitude; Sign 1's Complement and align 2's complement notation; Rules for addition and subtraction with complement Representation; BCD; EBCDIC; ASCII; Extended ASCII; Gray and other codes.</p> <p><b>LOGIC GATES AND BOOLEAN ALGEBRA:- [10 Hrs]</b>            AND; OR; NOT; NAND; NOR; Ex-OR logic gates; Positive and negative logic; Fundamental concepts of Boolean algebra; De-murrage's laws; Principles of duality; Simplification of Boolean expressions; Canonical and standard forms for Boolean function; SOP and POS, forms; Realization of Boolean functions using only NAND and NOR gates.</p> <p><b>BOOLEAN FUNCTION MINIMIZATION:- [10 Hrs]</b>            Objectives of the minimization procedures; Karnaugh map method; The 3-Variable Karnaugh Map; The 4-Variable Karnaugh Map; Karnaugh Map SOP Minimization; Don't care conditions; Karnaugh Map POS Minimization; Converting Between POS and SOP Using the Karnaugh Map.</p> <p><b>COMBINATIONAL LOGIC CIRCUITS USING DISCRETE LOGIC GATES:- [5 Hrs]</b>            Parity generator and checker; Code converters; Majority circuits; magnitude comparator.</p>

	<b>COMBINATIONAL LOGIC CIRCUIT USING MSI INTEGRATED CIRCUITS:- [10 Hrs]</b> Encoder; priority encoder; decoder; Multiplexer and demultiplexer circuits; Implementation of Boolean functions using decoder and Multiplexer; BCD to 7-segment decoder; Common anode and common cathode 7-segment displays; Random access memory; Read only memory and erasable programmable ROMS
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<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>	45	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	45	<b>Unstructured SWL (h/w)</b>	4
<b>Total SWL (h/sem)</b>	90		

<b>Module Evaluation</b>					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	10% (10)	1-14	LO #1-14
	<b>Assignments</b>	1	5% (5)	6	LO # 1-6
	<b>Projects / Lab.</b>	10 Lab	10% (10)	5-14	LO # 5-14
	<b>Report</b>	3	5% (5)	5-14	LO # 5-14
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5hr	20% (20)	10	LO # 1-10
	<b>Final Exam</b>	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>	
	Material Covered

<b>Week 1</b>	<b>NUMBER SYSTEMS:-</b> Decimal number system; Binary; Octal and hexadecimal number systems; Conversion from one number to another number system; Addition; Subtraction; Multiplication and division using different number system; Representation of binary number insignia-magnitude; Sign 1's Complement and align 2's complement notation; Rules for addition and subtraction with complement Representation; BCD; EBCDIC; ASCII; Extended ASCII; Gray and other codes.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	<b>LOGIC GATES AND BOOLEAN ALGEBRA:-</b> AND; OR; NOT; NAND; NOR; Ex-OR logic gates; Positive and negative logic; Fundamental concepts of Boolean algebra; De-murrage's laws; Principles of duality; Simplification of Boolean expressions; Canonical and standard forms for Boolean function; SOP and POS, forms; Realization of Boolean functions using only NAND and NOR gates.
<b>Week 5</b>	
<b>Week 6</b>	
<b>Week 7</b>	<b>BOOLEAN FUNCTION MINIMIZATION:-</b> Objectives of the minimization procedures; Karnaugh map method; The 3-Variable Karnaugh Map; The 4-Variable Karnaugh Map; Karnaugh Map SOP Minimization; Don't care conditions; Karnaugh Map POS Minimization; Converting Between POS and SOP Using the Karnaugh Map.
<b>Week 8</b>	
<b>Week 9</b>	
<b>Week 10</b>	
<b>Week 11</b>	<b>COMBINATIONAL LOGIC CIRCUITS USING DISCRETE LOGIC GATES:-</b> Parity generator and checker; Code converters; Majority circuits; magnitude comparator.
<b>Week 12</b>	
<b>Week 13</b>	<b>COMBINATIONAL LOGIC CIRCUIT USING MSI INTEGRATED CIRCUITS:-</b> Encoder; priority encoder; decoder; Multiplexer and demultiplexer circuits; Implementation of Boolean functions using decoder and Multiplexer; BCD to 7-segment decoder; Common anode and common cathode 7-segment displays; Random access memory; Read only memory and erasable programmable ROMS
<b>Week 14</b>	
<b>Week 15</b>	
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

### Delivery Plan (Weekly Lab. Syllabus)

	Material Covered
<b>Week 5-14</b>	Introduction to KL-31001 DIGITAL LOGIC LAB Exp. 1: Logic Gates Exp. 2: NAND, NOR, XOR Gates Exp. 3: AND-OR-INVERTER(A-O-I) Circuits Exp. 4: Bit Parity Generator Circuits Exp. 5: Comparator Circuits Exp. 6: Decoder Exp. 7: Encoder Exp. 8: Multiplexer Exp. 9: Demultiplexer

## Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	<b>Digital Fundamentals</b>   Eleventh Edition Global Edition   by Thomas L. Floyd   Pearson Education 2015	PDF
<b>Recommended Texts</b>	<b>Logic and Computer Design Fundamentals</b>   Fifth Edition Global Edition   by Morris Mano • Charles R. Kime • Tom Martin   Pearson Education 2016	PDF
<b>Websites</b>	(Telegram Group and Google classroom)	

## Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Information			
Module Title	<b>A.C circuits Analysis</b>		Module Delivery
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE216		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	1	Semester of Delivery	
Administering Department	Type Dept. Code	College	Type College Code
Module Leader	Zahraa Siddiq Yahya		e-mail
Module Leader's Acad. Title	Lecturer assistant	Module Leader's Qualification	
Module Tutor	Zahraa Siddiq Yahya		e-mail E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	01/06/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module		Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
Module Aims	12. To identify the basic concepts of energy storage elements. 13. To identify the basic of Alternating Current AC. 14. To understand and cover the basic AC circuit analysis methods and theorems.

<b>Module Learning Outcomes</b>	<p>11. Explain the function of each element in AC Electrical circuits.</p> <p>12. Use the basic circuit analysis methods to simplified the AC Electrical circuits.</p> <p>13. Applying the appropriate analysis method to reach the aim in its simplest form.</p>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A – energy storage elements:</u></p> <p>The capacitor; The Inductor; Analysis of RC-transient circuits; Analysis of RL-transient circuits; RLC transient circuits. [15 hrs]</p> <p><u>Part B - A.C. circuit analysis:</u></p> <p>the basic of Alternating Current AC; The Phasor equivalent circuit; series &amp; parallel connections and equivalent impedance; Methods of Ac-circuit Analysis; superposition; Nodal &amp; Mesh analysis; Thevenin’s Theorem; Norton’s Theorem; Power factor and average power in the sinusoidal Ac-circuits; Complex power; Series &amp; parallel resonance. [35 hrs]</p>

### Learning and Teaching Strategies

<b>Strategies</b>	<p>Type something like: The main strategy that will be adopted in delivering this module is to encourage students’ participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering types of simple experiments involving some sampling activities that are interesting to the students.</p>
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### Student Workload (SWL)

Structured SWL (h/sem)	30	Structured SWL (h/w)	4
Unstructured SWL (h/sem)	30	Unstructured SWL (h/w)	4
<b>Total SWL (h/sem)</b>	<b>60</b>		

<b>Module Evaluation</b>					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	<b>Assignments</b>	1	10% (10)	14	LO #4, #7, #(10-13)
	<b>Projects / Lab.</b>	0	0% (0)		
	<b>Report</b>	1	10% (10)	12	LO #11
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5 hr	10% (10)	10	LO #(1-8)
	<b>Final Exam</b>	3 hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>

<b>Week 1</b>	The capacitor & The inductor
<b>Week 2</b>	Analysis of RC & RL -transient circuits
<b>Week 3</b>	Analysis of RLC transient circuits
<b>Week 4</b>	The basic of Alternating Current AC
<b>Week 5</b>	The Phasor equivalent circuit
<b>Week 6</b>	series & parallel connections and equivalent impedance
<b>Week 7</b>	Methods of Ac-circuit Analysis
<b>Week 8</b>	superposition
<b>Week 9</b>	Nodal & Mesh analysis
<b>Week 10</b>	Thevenin's Theorem
<b>Week 11</b>	Norton's Theorem
<b>Week 12</b>	Power factor and average power in the sinusoidal Ac-circuits
<b>Week 13</b>	Complex power
<b>Week 14</b>	Series & parallel resonance
<b>Week 15</b>	<b>Preparatory week before the final Exam</b>

### Learning and Teaching Resources

	Text	Available in the Library?
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<b>Required Texts</b>	" Engineering Circuit Analysis" By W. Hayt	Yes
<b>Recommended Texts</b>	"Introductory Circuit Analysis" By Boylested	Yes

### Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
معلومات المادة الدراسية			
Module Title	Engineering Drawing		Module Delivery
Module Type	Core		<input type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE201		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	1	Semester of Delivery	
Administering Department		College	
Module Leader	Noor Yassar	e-mail	
Module Leader's Acad. Title		Module Leader's Qualification	
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	01/06/2023	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	Students will be able to: 6. Drawing engineering shapes manually and clearly, including the effective use of the computer-aided drawing program (AutoCAD). 7. Develop a solid understanding of the basic principles of engineering drawing, Included the ability to work with concepts, analytically, and

	<p>visualize them and a functional understanding of how these ideas will manifest in the real world.</p> <ol style="list-style-type: none"> <li>8. Determine the strategies to be used and the assumptions to be made.</li> <li>9. Use both manual and computer approaches in drawing figures.</li> <li>10. Develop the ability to use engineering tools flexibly and creatively.</li> <li>11. Develop an integrated understanding of the AutoCAD module.</li> <li>12. Developing their ability to communicate scientific ideas.</li> <li>13. Develop expertise in experimental methodologies.</li> </ol>
<p><b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"> <li>14. Understand and apply the basics of drawing types of lines.</li> <li>15. Define, explain and apply engineering drawing operations.</li> <li>16. Understand the basics of drawing an ogee curves</li> <li>17. Understand and apply the basic idea of central projection theory.</li> <li>18. Explanation of the central and parallel projection theory to understand the projection process.</li> <li>19. Explain Different Views are Front View (FV), Top View (TV) and Side View (SV) FV is a view projected on VP. TV is a view projected on HP. SV is a view projected on PP.</li> <li>20. Ability to draw using AutoCAD.</li> </ol>
<p><b>Indicative Contents</b> المحتويات الإرشادية</p>	<p><b>Introduction to engineering drawing and its tools</b> Introduction and introducing students to the subject of engineering drawing, which includes Identification of engineering tools and how to use them.</p> <p><b>Engineering shapes and the arcs , lamina. , Dimensions:</b></p> <ul style="list-style-type: none"> <li>- Various engineering operations: -</li> <li>- Drawing a straight line parallel to a known straight line</li> <li>- The division of the rectum into two halves</li> <li>- Angle division is known.</li> <li>- Drawing a straight line parallel to a known straight line from a point that does not belong to the known straight line.</li> <li>- Draw a tangent to a circle from a point that does not belong to it.</li> <li>- Draw a tangent to two contiguous circles from the outside.</li> <li>- Draw a tangent to two contiguous circles from the inside</li> </ul> <p><b>Multi view projection</b></p> <ul style="list-style-type: none"> <li>- Perpendicular Projection Theory of Objects:</li> <li>- Types of projections resulting from vertical projection and approved in the projection of various engineering objects</li> <li>- Front view</li> <li>- Side view.</li> <li>- Top view</li> </ul> <p><b>Using AutoCAD</b></p> <ul style="list-style-type: none"> <li>- Apply everything that has been explained in the manual engineering drawing on the AutoCAD program and drawing the three-dimensional models</li> </ul>

## Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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<b>Student Workload (SWL)</b> الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	45	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	3
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	55	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	3.7
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>100</b>		

<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	6	20% (20)	5 and 10	LO #1, 2, 10 and 11
	<b>Assignments</b>	3	10% (10)	2 and 12	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	3	10% (10)	Continuous	All
	<b>Report</b>	0	0% (0)	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	2hr	10% (10)	7	LO # 1-4
	<b>Final Exam</b>	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
	<b>Material Covered</b>
<b>Week 1</b>	Introduction and introducing students to the subject of engineering drawing, which includes identification of engineering tools and how to use them.
<b>Week 2</b>	Teach students how to apply and draw the following engineering operations:



	Drawing a straight line parallel to a known straight line, the division of the rectum into two halves, angle division and drawing a straight line parallel to a known straight line.
<b>Week 3</b>	Teach students how to draw a tangent to two contiguous circles from the outside, Draw a tangent to two contiguous circles from the inside
<b>Week 4</b>	Draw a tangent to one circle from the inside and the other from the outside and draw a tangent to a circle passing through a straight line.
<b>Week 5</b>	Multi view projection Perpendicular Projection Theory of Objects: • Types of projection in drawing and its practical importance
<b>Week 6</b>	Types of projections resulting from vertical projection and approved in the projection of various engineering objects: Front view, Side view ,Top view
<b>Week 7</b>	Mid-term Exam + Introduction to AutoCAD
<b>Week 8</b>	Apply everything that has been explained in the manual engineering drawing on the AutoCAD program and drawing the three-dimensional models
<b>Week 9</b>	
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	
<b>Week 14</b>	
<b>Week 15</b>	<b>Preparatory week before the final Exam</b>

### Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	Material Covered
<b>Week 1-15</b>	The application of each part of the covered drawing subject theoretically and according to the weekly sequence of the curriculum in the AutoCAD laboratory

### Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	ENGINEERING DRAWING AND GRAPHIC TECHNOLOGY", Fourteenth Edition, By: THOMAS E.FRENCH, CHARLES .VIERCK, ROBERT J.FOSTER,McGRAW-HILL	Yes

<b>Recommended Texts</b>	➤ William D.CallisterJr.&David D.Rethwisch.(2010)"Material Science and Engineering An introduction", eighthEdition.	No
<b>Websites</b>	ENGINEERING DRAWING Any edition	

<b>Grading Scheme</b> مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

<b>Module Information</b> معلومات المادة الدراسية			
<b>Module Title</b>	<b>MathematicsII</b>		<b>Module Delivery</b>
<b>Module Type</b>	<b>Base</b>		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	<b>NVEE 207</b>		
<b>ECTS Credits</b>	<b>٦</b>		
<b>SWL (hr/sem)</b>	<b>١٥٠</b>		
<b>Module Level</b>	1	<b>Semester of Delivery</b>	
<b>Administering Department</b>	Electronic Eng. Dep.	<b>College</b>	Electronics Engineering
<b>Module Leader</b>	Hani M. S. Salman	<b>e-mail</b>	hani.mohamed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Assistant Lecturer	<b>Module Leader's Qualification</b>	MSc
<b>Module Tutor</b>	Name (if available)	<b>e-mail</b>	E-mail
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	1.0

<b>Relation with other Modules</b> العلاقة مع المواد الدراسية الأخرى			
<b>Prerequisite module</b>	NEEM1211	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

### أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<p><b>Module Objectives</b> أهداف المادة الدراسية</p>	<ol style="list-style-type: none"><li>1. To obtain a good knowledge of dealing with complex numbers.</li><li>2. Establish a strong foundation in matrices and their operations, determinants, and inverse matrices. This includes covering definitions, notations, properties, types, and basic operations on matrices, enabling effective application in problem-solving.</li><li>3. enhancing students' proficiency in matrix-based solutions for linear systems of equations using Cramer's rule, the inverse method, and the Gauss elimination method</li><li>4. To provide the students with the knowledge to deal with vectors and their mathematical operations.</li><li>5. To Learn about the polar coordinates, and the graphs of polar equations.</li><li>6. Apply calculus principles to solve real-world engineering problems, developing problem-solving skills and the ability to apply calculus concepts to practical situations.</li></ol>
<p><b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"><li>21. Comprehend and utilize complex numbers within the Argand diagram, and</li><li>22. master complex number operations (Addition, subtraction, product, quotient, power, and roots) and De Moivre's Theorem.</li><li>23. Understand the concept of linear algebra and matrices.</li><li>24. Identify the types of matrices such as square matrices, zero matrix and identity.</li><li>25. Perform the common matrix operations such as addition, subtraction, scalar multiplication, and multiplication.</li><li>26. Find the transpose of a matrix.</li><li>27. Compute the determinants.</li><li>28. Compute the inverse of the matrix.</li><li>29. Identify whether the matrix is invertible or singular.</li><li>30. Relate a matrix to a homogenous system of linear equation.</li><li>31. Solve a system of linear equations by matrices: using Cramer's rule.</li><li>32. Solve a system of linear equations by matrices: using the inverse method.</li><li>33. Solve a system of linear equations by matrices: using Gauss Elimination Method.</li><li>34. Identify the rank of the matrix and its relation to the solution of linear equations.</li><li>35. Find the eigenvalues and eigenvectors of a matrix.</li><li>36. Represent a vector in space.</li><li>37. Compute dot and cross products in vectors.</li><li>38. Understand the meaning of del operator, gradient, divergence, and curl and to compute the del operation, gradient, divergence, and curl.</li><li>39. Learn about the vector functions.</li></ol>

	<p>40. Convert from Cartesian to Polar coordinates and vice versa.</p> <p>41. Sketch in polar system.</p> <p>42. Utilize mathematical reasoning and critical thinking skills to analyze and interpret mathematical concepts and their applications in Electronics engineering.</p> <p>43. Develop proficiency in mathematical problem-solving, both independently and collaboratively, and communicate solutions effectively.</p>
<p><b>Indicative Contents</b> المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p><u>Part A – Review of Complex Numbers:</u> The Argand diagram, Addition, Subtraction; Product, Quotient, power and roots, and Demoiver’s Theorem. [4hrs]</p> <p><u>Part B – Matrices and Determinants:</u> Matrices and Determinants: Definitions and notations, Properties, types of matrices, basic operations on matrices, computation of the determinants of matrices, properties of determinants. [8 hrs] Inverse of the Matrices. [4 hrs] Solution of the system of linear equations-solution of the system of linear equation using Cramer’s rule, solution of the system of linear equation using the inverse method. [12 hrs] Revision problem classes [4 hrs] solution of the system of linear equation using Gauss Elimination Method. [4 hrs] Eigenvalues and eigenvector. [4 hrs]</p> <p><u>Part C – Review of Vectors:</u> Representation of vectors in space (i;j;k), unit vectors, Scalar product, and Vector product. [8 hrs]</p> <p><u>Part D – Vector Calculus:</u> Vectors – del operator, Parametric Equations of Lines in Space, the distance from a Point to a line in Space, plane equation in space, the Distance from the Point to a Plane, Angles Between Planes, vector function versus Scalar function, del operator, Gradient, Divergence and Curl. [12 hrs]</p> <p><u>Part E – Polar Coordinates:</u> Polar coordinates – polar coordinate system, transformation between polar and Cartesian coordinates, graphs of polar equations. [4 hrs]</p>

## Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

<b>Strategies</b>	This module's major aim is to foster student engagement, improve critical thinking abilities, and promote collaborative learning. Interactive seminars, interesting tutorials, and exercises encourage active participation, allowing students to hone their critical thinking skills and apply engineering mathematics principles to problem solving. Furthermore, students collaborate on engineering mathematics issues, examine real-world scenarios, and explore the practical applications of the principles acquired through group activities, projects, and conversations. This method not only increases students' comprehension of engineering mathematical concepts, but it also fosters cooperation, communication, and key interpersonal skills that will be useful in their future engineering activities.
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<b>Student Workload (SWL)</b> الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	62	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	88	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	5.9
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>150</b>		

<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	Quizzes	2	30% (30)	6 and 14	LO #1 - #11, #16- #19
	Assignments	1	10% (10)	13	LO #12-#15
	Projects / Lab.	-	-	-	-
	Report	-	-	-	-
<b>Summative assessment</b>	Midterm Exam	2hr	10% (10)	7	LO #1 - #11
	Final Exam	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
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	Material Covered
Week 1	The Argand diagram, Addition, Subtraction; Product, Quotient, power and roots, and Demoiver's Theorem.
Week 2	Matrices and Determinants: Definitions and notations, Properties, types of matrices, basic operations on matrices, computation of the determinants of matrices, properties of determinants.
Week 3	
Week 4	Inverse of the Matrices.
Week 5	Solution of the system of linear equations-solution of the system of linear equation using Cramer's rule.
Week 6	solution of the system of linear equation using the inverse method.
Week 7	solution of the system of linear equation using Gauss Elimination Method.
Week 8	Revision problem classes, Mid-term Exam
Week 9	Eigenvalues and eigenvector. [4 hrs]
Week 10	Representation of vectors in space (i;j;k), unit vectors, Scalar product, and Vector product.
Week 11	
Week 12	Vectors – del operator, Parametric Equations of Lines in Space, the distance from a Point to a line in Space, plane equation in space, the Distance from the Point to a Plane, Angles Between Planes, vector function versus Scalar function, del operator, Gradient, Divergence and Curl.
Week 13	
Week 14	
Week 15	Polar coordinates – polar coordinate system, transformation between polar and Cartesian coordinates, graphs of polar equations.

### Learning and Teaching Resources

#### مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	"Higher Engineering Mathematics", 7 <sup>th</sup> edition by John Bird	No
	G. B. Thomas Jr., M. D. Weir, J. Hass, and F. R. Giordano, "Thomas' Calculus," 12th ed., Pearson, 2019.	Yes
Recommended Texts	"Introduction to Linear Algebra". 4th edition by Strang, Gilbert "Linear Algebra for Everyone". 2020 by Strang, Gilbert	No

	Zill, D. G., Wright, W. S., & Cullen, M. R. (2011). Advanced Engineering Mathematics. Jones & Bartlett Publishers.	
Websites	<a href="https://ocw.mit.edu/courses/18-06-linear-algebra-spring-2010">https://ocw.mit.edu/courses/18-06-linear-algebra-spring-2010</a> <a href="https://www.khanacademy.org/math/linear-algebra">https://www.khanacademy.org/math/linear-algebra</a> <a href="https://www.ohio.edu/mechanical-faculty/williams/html/PDF/MatricesLinearAlgebra.pdf">https://www.ohio.edu/mechanical-faculty/williams/html/PDF/MatricesLinearAlgebra.pdf</a>	

Grading Scheme				
مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				



Module Information				
Module Title	Physical of semiconductors		Module Delivery	
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE219			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery		2
Administering Department	EI	College	NE	
Module Leader	Hamsa Fawaz Thanoon		e-mail	hamsa.thanoon@uoninevah.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc	
Module Tutor		e-mail	E-mail	
Peer Reviewer Name		e-mail	E-mail	
Scientific Committee Approval Date	04/07/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module		Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
Module Aims	1. To develop problem solving skills and understanding of Atomic Structure

	<ol style="list-style-type: none"> <li>2. To understand Energy band structure of metal, insulator, and semiconductor.</li> <li>3. To understand Properties of intrinsic P and N type semiconductors.</li> <li>4. To understand Electrical conduction in intrinsic semiconductor.</li> <li>5. To understand Properties of extrinsic semiconductors.</li> <li>6. To understand Electrical conduction in extrinsic semiconductor</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Recognize how semiconductors works in electronics circuits.</li> <li>2. List the various terms associated with electronics circuits.</li> <li>3. Summarize what is meant by a basic of semiconductors.</li> <li>4. Discuss the reaction and involvement of semiconductors in generate the currents.</li> <li>5. Describe mobility of electrons and conductivity in metals.</li> <li>6. Define Ohm's law.</li> <li>7. Identify the pure semiconductors.</li> <li>8. Identify the impure semiconductors</li> <li>9. Discuss the impure semiconductors N and P types</li> <li>10. Explain the type of electronic emission.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A - Energy Bands in Solids</u>  <u>Describe the structure of an atom ♦ Discuss insulators, conductors, and semiconductors and how they differ. [9 hrs]</u></p> <p><u>Revision problem classes [3 hrs]</u></p> <p><u>Part B - Transport Phenomena in Semiconductor</u>  <u>Describe how current is produced in a semiconductor ♦ Describe the properties of n-type and p-type semiconductors. [30 hrs]</u></p>

### Learning and Teaching Strategies

<b>Strategies</b>	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills.
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### Student Workload (SWL)

Structured SWL (h/sem)	111	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	65	Unstructured SWL (h/w)	2
Total SWL (h/sem)	176		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10	[2,4,5,6]	LO (#1- #12)
	Assignments	2	10	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
Summative assessment	Midterm Exam	1.5 hr	20% (20)	10	LO #(1-8)
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

	Material Covered
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<b>Week 1</b>	PN junction in equilibrium
<b>Week 2</b>	Volt Ampere characteristics; Temperature dependence
<b>Week 3</b>	diffusion capacitance
<b>Week 4</b>	Non-linear properties; Ideal diode; Basic theory and analysis of simple diode circuit; DC load line; Small signal analysis and concept of dynamic resistance; AC load line
<b>Week 5</b>	Diode capacitance ;Temperature effects of diode
<b>Week 6</b>	Different types of diodes (Zener; schottckey);
<b>Week 7</b>	(Varactor diode; Tunnel and negative resistance diodes).
<b>Week 8</b>	Circuit analysis of half wave and full wave rectifiers
<b>Week 9</b>	Bridge rectifier; Ripple and form factor calculations
<b>Week 10</b>	Types of filters; C filters , L filter ,L .C. filter, PIE filter; Analysis of filter and calculation of ripple and regulation.
<b>Week 11</b>	Solved problems
<b>Week 12</b>	Clipping and Clam Ping Circuit:
<b>Week 13</b>	Transistors: PNP; NPN
<b>Week 14</b>	The BJT as an Amplifier
<b>Week 15</b>	Preparatory week before the final Exam

## Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	1: " SOLID STATE DIVICES" ،PHI; 4TH EDITION ،1995.By STREETMAN ، 2: "SEMICONDUCTOR DEVICES & CIRCUITS" ،JOHN WILEY & SONS ،1992.By : M.S. TYAGI 3: " ELECTRONICS DEVICES & CIRCUITS THEORY" ،HI; By BOYLSTED & NASHELSKY	Yes
<b>Recommended Texts</b>	3. (Floyed) 4. ثیراجا فصل ۵۱	Yes

## Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتیاز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

<b>Module Information</b> معلومات المادة الدراسية			
Module Title	<b><u>English</u></b>		Module Delivery
Module Type	<b><u>Basic</u></b>		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	<b><u>NVU11</u></b>		
ECTS Credits	<b><u>2</u></b>		
SWL (hr/sem)	<b><u>50</u></b>		
Module Level	1	Semester of Delivery	
Administering Department	Dept. of Computer and Information	College	College of Electronics Engineering
Module Leader	Noor Mothafar Hamid	e-mail	<a href="mailto:noorm.hame@duoninevah.edu.iq">noorm.hame@duoninevah.edu.iq</a>
Module Leader's Acad. Title		Module Leader's Qualification	MA
Module Tutor		e-mail	
Peer Reviewer Name		e-mail	
Scientific Committee Approval Date	01/06/2023	Version Number	1.0

<b>Relation with other Modules</b> العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

<b>Module Aims, Learning Outcomes and Indicative Contents</b> أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Aims</b> أهداف المادة الدراسية	<ol style="list-style-type: none"> <li>7. To develop skills, reading, writing and understanding of English language through the application of teaching techniques.</li> <li>8. To understand scientific subjects and technical terms through reading and comprehension.</li> <li>9. This course deals with the basic concepts of scientific subjects.</li> <li>10. This course handles how to write simple research and how to make a successful presentation.</li> <li>11. To understand the scientific language in English.</li> </ol>

<p><b>Module Learning Outcomes</b></p> <p>مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"> <li>5. Recognize parts of speech and tenses in English language.</li> <li>6. List the various terms associated with scientific texts.</li> <li>7. Summarize what is meant by a basic electric circuit.</li> <li>8. Discuss Electric currents, series and parallel circuits.</li> <li>9. Describe electrical power, charge, and current.</li> <li>10. Discuss computers, communication and the future of computers..</li> <li>11. Identify the basic circuit elements and their applications.</li> <li>12. Explain energy types and forms.</li> <li>13. Discuss the various properties of radio waves and vacuum tubes.</li> <li>14. Explain modulation.</li> <li>15. Discuss Electromagnetism.</li> </ol>
<p><b>Indicative Contents</b></p> <p>المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <ol style="list-style-type: none"> <li>1. parts of speech <ul style="list-style-type: none"> <li>_verb</li> <li>_ noun</li> <li>_ pronoun</li> </ul> </li> <li>2. Tenses <ul style="list-style-type: none"> <li>_Past</li> <li>_Present</li> <li>_future</li> </ul> </li> <li>3. Electric currents and circuit <ul style="list-style-type: none"> <li>_AC/DC</li> <li>_parallel, series</li> <li>_Grounding, fuse, short circuit</li> </ul> </li> <li>4. Radio waves and vacuum tubes</li> <li>5. Electromagnetism.</li> <li>6. The future of computers, communication applications. <ul style="list-style-type: none"> <li>_fiber optics.</li> </ul> </li> <li>7. Induction. <ul style="list-style-type: none"> <li>_Electric generator</li> <li>_Electric transformer</li> <li>_self-induction</li> <li>_servomechanism</li> </ul> </li> <li>8. Incandescent lamp.</li> <li>9. Energy.    _types of energy        _forms of energy</li> <li>10. Introduction to electron and electricity.</li> <li>11. Electricity and electronics.</li> </ol>
<p><b>Learning and Teaching Strategies</b></p> <p>استراتيجيات التعلم والتعليم</p>	
<p><b>Strategies</b></p>	<p>The main strategy that will be adopted in delivering this module is to encourage students' participation by reading, writing and comprehension in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, presentation, interactive tutorials, by considering type of simple experiments involving some sampling activities that are interesting to the students.</p>

<b>Student Workload (SWL)</b> الحمل الدراسي للطالب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	33	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعياً	2
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	17	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعياً	1.4
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	50		

<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	4,6	LO #1, 2, 3,4 ,5and 6
	<b>Assignments</b>	2	5% (5)	9, 12	LO # 7,8,9,10,and 11
	<b>Presentation</b>	1	10% (10)	Continuous	
	<b>Report</b>	1	5% (5)	13	LO # 6,10
<b>Summative assessment</b>	<b>Midterm Exam</b>	2 hr	10% (10)	7	LO # 1-8
	<b>Final Exam</b>	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
	Material Covered
<b>Week 1</b>	Parts of speech
<b>Week 2</b>	Tenses
<b>Week 3</b>	Electric currents and circuit
<b>Week 4</b>	Radio waves and vacuum tubes
<b>Week 5</b>	The future of computers, communication applications.
<b>Week 6</b>	Induction -Electric generator -Electric transformer
<b>Week 7</b>	Mid-term Exam
<b>Week 8</b>	Induction -Self-induction -Servomechanism
<b>Week 9</b>	Incandescent lamp.



<b>Week 10</b>	Energy. -types of energy -forms of energy
<b>Week 11</b>	Introduction to electron and electricity.
<b>Week 12</b>	Electricity and electronics
<b>Week 13</b>	The cathode ray tube
<b>Week 14</b>	Propagation
<b>Week 15</b>	Modulation
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

<b>Learning and Teaching Resources</b> مصادر التعلم والتدريس		
	Text	Available in the Library?
<b>Required Texts</b>	English in electrical engineering and electronics. The language of electrical and electronic engineering in English.	Yes
<b>Recommended Texts</b>	English for electrical engineering and computing.	No
<b>Websites</b>	<a href="https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk">https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk</a>	

<b>Grading Scheme</b> مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group</b> (50 - 100)	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group</b> (0 - 49)	<b>FX - Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F - Fail</b>	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

## Courses specification for Second class Medical Engineering (First Course)

Module Information			
<b>Module Title</b>	Engineering analysisI	<b>Module Delivery</b>	
<b>Module Type</b>	Base	<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	NVEE208		
<b>ECTS Credits</b>	6		
<b>SWL (hr/sem)</b>	150		
<b>Module Level</b>	2		
<b>Administering Department</b>	Electronics dept	<b>College</b>	Electronics engineering college
<b>Module Leader</b>	Dr. Omar B Mohammed	<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Lecturer	<b>Module Leader's Qualification</b>	Ph.D.
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>		<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	

Relation with other Modules			
<b>Prerequisite module</b>	Mathematics II	<b>Semester</b>	1
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims, Learning Outcomes and Indicative Contents</b>	
<b>Module Aims</b>	This course covers the following topics: Multiple Integrals, Vectors Functions, Numerical Analysis, Statistics and Probability. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	<p>Upon successful completion, students will:</p> <ol style="list-style-type: none"> <li>1. Improve their problem-solving skills.</li> <li>2. Apply that knowledge toward practical problems in different areas of science.</li> <li>3. Utilize the computer capabilities to solve such problems using proper methods.</li> <li>4. Learn how to deal with geometry in 3D; find areas and volumes.</li> <li>5. Solve ordinary and differential equations numerically.</li> <li>6. Learn the importance of probability and statistics in everyday use.</li> </ol>
<b>Indicative Contents</b>	<p>Vectors Functions                      Multiple Integrals                      Numerical Analysis                      Statistics                      Probability</p>

## Learning and Teaching Strategies

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills.

## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>		<b>Structured SWL (h/w)</b>	
<b>Unstructured SWL (h/sem)</b>		<b>Unstructured SWL (h/w)</b>	
<b>Total SWL (h/sem)</b>			

## Module Evaluation

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	Quizzes				
	Assignments				
	Projects / Lab.				
	Report				
<b>Summative assessment</b>	Midterm Exam				
	Final Exam				
<b>Total assessment</b>					

	This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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## Delivery Plan (Weekly Syllabus)

	Material Covered
<b>Week 1</b>	Vectors: Vector in space, dot and cross product. Lines and planes in space. Vector valued functions and motion in space: position, velocity and acceleration, tangential vectors, curvature and normal vector.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	
<b>Week 5</b>	Multiple Integrals: Double Integral in rectangular coordinates, areas and volumes. Double Integral in Polar Coordinates, areas and volumes. Triple Integrals in rectangular, cylindrical, and spherical coordinates, volumes.
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	Numerical Analysis: Solution of non-linear equations by iteration; bisection and Newton-Raphson. Numerical Integration; trapezoidal rule.
<b>Week 10</b>	
<b>Week 11</b>	

<b>Week 12</b>	Numerical solution of 1st order ordinary differential equations; Euler's method.
<b>Week 13</b>	Statistics and Probability: Definitions, mutually exclusive and conditional probability, permutations and combinations
<b>Week 14</b>	
<b>Week 15</b>	
<b>Week 16</b>	Preparatory week before the final Exam

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney & Thomas	Yes
<b>Websites</b>		

### Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX</b> - Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> - Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information					
Module Title	Signal Analysis			Module Delivery	
Module Type	Core			Theory <input checked="" type="checkbox"/> Lecture Lab <input type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEEELM211				
ECTS Credits	6				
SWL (hr/sem)	150				
Module Level	1	Semester of Delivery			
Administering Department	Type Dept. Code	College	Type College Code		
Module Leader			e-mail		
Module Leader's Acad. Title	Assistant Professor	Module Leader's Qualification		Ph.D.	
Module Tutor	Name (if available)	e-mail	E-mail		
Peer Reviewer Name	Name	e-mail	E-mail		
Scientific Committee Approval Date	25/06/2023	Version Number	1.0		

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
Module Aims	Student will be able to: 15. identify signals concepts . 16. understand the classification of signals . 17. understand the different operations on signals.

	18. perform Fourier and Laplace transformations of signals.
<b>Module Learning Outcomes</b>	<p>44. Definition of the signal concept.</p> <p>45. Introduction of mathematical models.</p> <p>46. Explain Continuous time signals. Discrete time signals.</p> <p>47. Categorize the signals.</p> <p>48. Achieve operations on signals.</p> <p>49. Introduction of basic signals.</p> <p>50. Define convolution operation between two signals.</p> <p>51. Introduction of frequency domain and Fourier analysis.</p> <p>52. Laplace Transformation.</p>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p>Introduction to signals:</p> <ul style="list-style-type: none"> <li>- Definition and mathematical models.</li> <li>- Categorization of signals.</li> <li>- Operation on signals.</li> <li>- Basic types of signals.</li> </ul> <p>Convolution operation:</p> <ul style="list-style-type: none"> <li>- Introduction of convolution.</li> <li>- Convolution properties.</li> </ul> <p>Signal transformation:</p> <ul style="list-style-type: none"> <li>- Fourier series and transform.</li> <li>- Laplace Transform.</li> </ul>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	To make students interesting with both types of signals: continuous and discrete. Also with classifications of signals and operations on them. To make them familiar with time and frequency domain and analysis of a signal . Also to make them familiar with different types of transforms of signals. Also to make them have an experience with solving different problems and examples .

## Student Workload (SWL)

Structured SWL (h/sem)	64	Structured SWL (h/w)	4
Unstructured SWL (h/sem)	86	Unstructured SWL (h/w)	1
<b>Total SWL (h/sem)</b>	<b>150</b>		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	Quizzes	6	10% (10)	2, 9, 12, 13, 15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	2, 9, 12, 13, 15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	2, 9, 12, 13, 15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
<b>Summative assessment</b>	Midterm Exam	1.5hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
<b>Week 1</b>	Basic definitions. Mathematical models.
<b>Week 2</b>	Continuous time signals
<b>Week 3</b>	Discrete time signals
<b>Week 4</b>	Signal classifications
<b>Week 5</b>	Energy and power of signals
<b>Week 6</b>	Basic operations on continuous signals.



<b>Week 7</b>	Basic operations on discrete signals.
<b>Week 8</b>	Time domain representation of continuous signals; sinusoidal and complex exponential signals
<b>Week 9</b>	singularity function signals
<b>Week 10</b>	Convolution definition and operation
<b>Week 11</b>	Convolution properties
<b>Week 12</b>	Frequency domain representation of continuous signals. Spectra and bandwidth of the signal
<b>Week 13</b>	Fourier series representations of periodic signals.
<b>Week 14</b>	Fourier transform representations of non periodic signals.
<b>Week 15</b>	Laplace Transform of continuous signals. Laplace properties.

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	Signals and Systems. Simon S. Haykin	Yes
<b>Recommended Texts</b>	Signals and linear Systems. G. E. Carlson	

### Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
Module Title	Electronic I		Module Delivery
Module Type	core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEEELM212		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	1	Semester of Delivery	1
Administering Department	Electronics	College	Electronic Engineering college
Module Leader		e-mail	
Module Leader's Acad. Title	Assistant Prof.	Module Leader's Qualification	PhD
Module Tutor		e-mail	
Peer Reviewer Name	Name	e-mail	Ahmad.younis@uoninevah.edu.iq
Scientific Committee Approval Date	12/06/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module	NEEI2212	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ul style="list-style-type: none"><li>12. To understand the basic analysis of bipolar transistor amplifier</li><li>13. To be familiar with the dc and ac analysis of transistor amplifier</li><li>14. To understand the dc and ac analysis of FET amplifier</li><li>15. To illustrate and to understand the frequency response of amplifier</li><li>16. To understand the basic concept of feedback concept</li><li>17. To be able to deal with different feedback amplifier topologies</li><li>18. To study the advantages of negative feedback on amplifier performance</li><li>19. To be familiar with feedback amplifier ac analysis</li><li>20. To understand the construction and ideal characteristic of operational amplifier</li><li>21. To study and analyze op-amp equivalent circuit</li><li>22. To be familiar with basic op-amp applications</li><li>23. To start with studying power electronic devices</li></ul>
<b>Module Learning Outcomes</b>	<ul style="list-style-type: none"><li>16. Understand and apply the basic theory and operation of transistor amplifiers</li><li>17. Define and explain the frequency response of bipolar transistor amplifier</li><li>18. Understand the basic concept of negative feedback</li><li>19. Understand and analyze the feedback amplifier</li><li>20. Understanding the operation of ideal operational amplifier</li><li>21. Dealing with dc and ac op-amp equivalent circuit</li><li>22. Understanding the basic application of op-amp</li><li>23. Power electronic devices principle overview</li></ul>

<p><b>Indicative Contents</b></p>	<p><b>Transistor and FET amplifier analysis:</b>  Small signal model analysis, low frequency and high frequency analysis, hybrid model, hybrid -Pi model analysis.</p> <p><b>Amplifier with negative feedback:</b>  Basic concept, feedback analysis, feedback configurations, Feedback effects on gain , bandwidth, input and output resistances</p> <p><b>Operational amplifier:</b>  Ideal Op-amp equivalent circuit; Operational Amplifier Specification; Circuit analysis of an Op-amp;  Closed loop Op-amp Circuit ( Inverting and Non-Inverting Circuit ).</p> <p><b>Op-amp Applications:</b> Summation &amp; subtraction Circuit, Differential circuit Buffer circuit  Ideal and practical Integrator circuits, ideal and practical Differentiator circuits, Examples.</p> <p><b>Power electronic devices:</b>  UJT Construction, Operation and characteristics;  Thyristor Equivalent Circuit ; Thyristor Characteristics and operation ;  Application of the devices.</p>

<p style="text-align: center;"><b>Learning and Teaching Strategies</b></p>	
<p><b>Strategies</b></p>	<p>The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.</p>

Student Workload (SWL)			
Structured SWL (h/sem)	74	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	101	Unstructured SWL (h/w)	1
Total SWL (h/sem)	175		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	2, 5, 9,12,13,15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1:30hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Small signal model analysis
Week 2	Low and high frequency response of transistor amplifier
Week 3	Hybrid and hybrid-Pie equivalent circuit analysis
Week 4	Negative feedback concept and analysis
Week 5	Advantages of negative feedback on amplifier
Week 6	Amplifier feedback topologies
Week 7	Feedback effect on amplifier gain, bandwidth, and on input-output resistances
Week 8	operational amplifier construction and operation
Week 9	ideal and practical op-amp equivalent circuit
Week 10	Inverting and non inverting closed loop amplifier
Week 11	Integration and differentiation active circuits

<b>Week 12</b>	Summation and subtraction op-amp circuits
<b>Week 13</b>	UJT transistor construction
<b>Week 14</b>	Thyristor equivalent circuit and characteristics
<b>Week 15</b>	Subject review
<b>Week 16</b>	<b>Subject review</b>

<b>Delivery Plan (Weekly Lab. Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1-15</b>	<p><b>Practical experiments in transistor amplifier frequency response at lo and high frequency</b></p> <p><b>To measure the effect of feedback on amplifier performance</b></p> <p><b>To measure the performance of different op-amp circuits.</b></p>

<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Electronic Devices By Millmann Electronic Devices By Floyd	yes
<b>Recommended Texts</b>	<b>SOLID STATE DIVICES", PHI; 4TH EDITION, 1995.By STREETMAN,</b> <b>SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992.By : M.S. TYAGI</b>	Yes
<b>Websites</b>	Electronic circuits	

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.

	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
معلومات المادة الدراسية			
Module Title	Digital Design		Module Delivery
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE223		
ECTS Credits	4		
SWL (hr/sem)	100		
Module Level	2	Semester of Delivery	1
Administering Department	Electronic Eng. Dep.	College	Electronics Engineering
Module Leader	Amer Talal Ali	e-mail	
Module Leader's Acad. Title	Lecturer assistant	Module Leader's Qualification	
Module Tutor	Amer Talal Ali	e-mail	
Peer Reviewer Name	Name	e-mail	
Scientific Committee Approval Date	01/06/2023	Version Number	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module		Semester	
Co-requisites module		Semester	



## Module Aims, Learning Outcomes and Indicative Contents

### أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<p><b>Module Objectives</b></p> <p>أهداف المادة الدراسية</p>	<p>19. To understand Advanced Minimization techniques for large number of bits to simplify the large designs.</p> <p>20. Understand how to Design an Arithmetic and Logic unit.</p> <p>21. Understand how to Design using programmable logic device.</p> <p>22. To understand the sequential Logic Circuits.</p> <p>23. To understand how to Design synchronous and asynchronous counters.</p> <p>24. To understand the Design of Registers.</p>
<p><b>Module Learning Outcomes</b></p> <p>مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"><li>1. Using Advanced Minimization techniques for large number of bits to simplify the large designs.</li><li>2. Design an Arithmetic and Logic unit.</li><li>3. Design using programmable logic device.</li><li>4. Design sequential Logic Circuits synchronous and asynchronous.</li><li>5. Design Registers.</li><li>6. Design synchronous and asynchronous counters.</li></ol>
<p><b>Indicative Contents</b></p> <p>المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p><u>Part A</u> – minimization techniques for large number of bits [14 hrs]</p> <p><u>Part B</u> – Initialization to design and Design an Arithmetic and Logic unit. [14 hrs]</p> <p><u>Part C</u> – Design using programmable logic device. [6 hrs]</p> <p><u>Part D</u> – sequential Logic Circuits. [18 hrs]</p>

## Learning and Teaching Strategies

### استراتيجيات التعلم والتعليم

<b>Strategies</b>	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking and digital designing skills. This will be achieved through classes and interactive tutorials.
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## Student Workload (SWL)

### الحمل الدراسي للطلاب محسوب ل ١٥ اسبوعا

<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطلاب خلال الفصل	60	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطلاب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطلاب خلال الفصل	60	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطلاب أسبوعيا	4
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطلاب خلال الفصل	<b>120</b>		

## Module Evaluation

### تقييم المادة الدراسية

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	<b>Assignments</b>	1	10% (10)	14	LO #4, #7, #(10-13)
	<b>Projects / Lab.</b>	0	0% (0)		
	<b>Report</b>	1	10% (10)	12	LO #11
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5 hr	10% (10)	10	LO #(1-8)
	<b>Final Exam</b>	2 hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

### المنهاج الاسبوعي النظري

	Material Covered
Week 1	Introduction to Digital Design
Week 2	The 5-Variable Karnaugh Map; The 5-Variable Karnaugh Map with don't care conditions
Week 3	Map Entered variable Karnaugh Map
Week 4	ADDITIONAL MINIMIZATION TECHNIQUES: Tabular method; Quine-McCluskey
Week 5	Design using multiplexer: - Shannon Expansion
Week 6	top-down design of combinational CIRCUITS: - Gate Level: Adders; Subtractor
Week 7	Design an Arithmetic and Logic unit
Week 8	memory and type of memories
Week 9	Design using programmable logic device (PLD): - PROM; PAL; PLA;
Week 10	sequential LOGIC: - Type of flip-flops; Timing Diagram; Basic concepts of counters; Binary counters; BCD counters; Up down counter
Week 11	sequential LOGIC: -Design of counters using state diagrams and tables;
Week 12	sequential LOGIC: -Mealy and Moore Circuits;
Week 13	synchronous CIRCUITS: Shift left and right register; Registers with parallel load; Serial –in parallel-out (SIPO) and parallel-in-serial-out (PISO).
Week 14	synchronous CIRCUITS: Shift Registers; Twisted Ring Counter; Maximum Length Shift Counter.
Week 15	Preparatory week before the final Exam

## Learning and Teaching Resources

### مصادر التعلم والتدريس

	Text	Available in the Library?
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<b>Required Texts</b>	"Digital and analog communication" 2001 By L. W. Couch Sixth Edition	Yes
<b>Recommended Texts</b>	- Digital Communications Fifth Edition, 2008, John G. Proakis, and Masoud Salehi.  Introduction to Communication Systems" 1992 By F. Stremler.  -ELEMENTS OF INFORMATION THEORY" 2006 By THOMAS M. COVER and JOY A. THOMAS  -Digital Communication, 2004 by Abbas Kattoush.	Yes
<b>Websites</b>		

<b>Grading Scheme</b> مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
<b>Success Group</b> <b>(50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group</b> <b>(0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information			
<b>Module Title</b>	Electromagnetic Fields I		<b>Module Delivery</b>
<b>Module Type</b>	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEE215		
<b>ECTS Credits</b>	4		
<b>SWL (hr/sem)</b>	100		
<b>Module Level</b>	2	<b>Semester of Delivery</b>	1
<b>Administering Department</b>	Type Dept. Code	<b>College</b>	Type College Code
<b>Module Leader</b>	SINAN KHALID SHANSHAL		<b>e-mail</b> sinan.mohammed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Lecturer	<b>Module Leader's Qualification</b>	M.Sc.
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>	02/07/2023	<b>Version Number</b>	1.0

Relation with other Modules			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	To develop knowledge of the laws governing the behavior of electric fields, and to relate the laws governing the fields to applications in a range of electrical and electronic engineering application.
<b>Module Learning Outcomes</b>	<p>On completion of the course the students should be able:</p> <ul style="list-style-type: none"> <li>• to have detailed knowledge of the physical background and terminology of the electrostatic field theory for electrical engineering problems</li> <li>• to understand the electrostatic field behavior</li> <li>• to select and use appropriate theoretical models for analysis, problem solving and finding solutions related to the electrostatic fields</li> <li>• to understand how laws of electrostatic can be applied to problems arising in engineering.</li> </ul>
<b>Indicative Contents</b>	Electric charge and the electric field Electric flux density and Gauss's Law Electric potential Electric field in matter and boundary conditions Capacitance

## Learning and Teaching Strategies

<b>Strategies</b>	Through the presentation of a theoretical explanation with the aid of white board and 'Data Show', to illustrate syllabus (examples and exercises) and using text books.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	45	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	?	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	?		

## Module Evaluation

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	15% (10)	5,8,10,12	LO #1-5, 9 and 11
	Assignments	4	15% (10)	6,9,11,13	LO # 1-5, 6, 10 and 12
	Projects	0	0% (0)		
	Report	0	0% (0)		
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-8
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

	Material Covered
Week 1	Review of Vector Calculus
Week 2	Review of Vector Calculus
Week 3	Experimental law of coulomb; Electric field intensity;
Week 4	Field of a continuous and volume charge distributions; line charge and sheet charge;
Week 5	Field of a continuous and volume charge distributions; line charge and sheet charge;
Week 6	Electric flux density; Gauss's law;
Week 7	Application of Gauss's law; some symmetrical charge distributions.
Week 8	Energy expended in moving a point charge in an electric field;
Week 9	Definition of potential difference and potential;
Week 10	Potential field of a point charge and system of charges; Potential gradient.
Week 11	Conductor Properties and boundary conditions;
Week 12	Nature of Dielectric Materials; Boundary Conditions for Perfect dielectric Materials;
Week 13	Capacitance; Several Capacitance Examples.
Week 14	Poisson and Laplace 's equations; Examples of the solution of Laplace equation
Week 15	Examples of the solution of Laplace equation; Examples of the solution of Poisson's equation.
Week 16	Preparatory week before the final Exam

### Delivery Plan (Weekly Lab. Syllabus)

	Material Covered
Week 1-15	

## Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	1-ENGINEERING ELECTROSTATICES, Mc- Graw Hill, By WILLAIM H. HAYT. 2-Elements of engineering electrostatic, Prentice Hall, By Matthew N. O. SADIKU	No
<b>Recommended Texts</b>	1-Electrostatics (Schaum's Outlines), McGraw-Hill Education By Edminister, Joseph_ Nahvi, Mahmood.	No
<b>Websites</b>		

## Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Information			
<b>Module Title</b>	Human Physiology		<b>Module Delivery</b>
<b>Module Type</b>	Support		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEEELM 213		
<b>ECTS Credits</b>	5		
<b>SWL (hr/sem)</b>	125		
<b>Module Level</b>	2	<b>Semester of Delivery</b>	
<b>Administering Department</b>	Electronics dept	<b>College</b>	Electronics engineering college
<b>Module Leader</b>		<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>		<b>Module Leader's Qualification</b>	
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>		<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	

Relation with other Modules			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

**Module Aims, Learning Outcomes and Indicative Contents**

<p><b>Module Aims</b></p>	<p>This subject introduces engineering students to human anatomy and physiology, with direct application of the knowledge to considerations for designing and manufacturing medical devices and equipment to assist in overcoming physical disabilities.</p>
<p><b>Module Learning Outcomes</b></p>	<p>Upon completion this unit, the student should be able to:</p> <ol style="list-style-type: none"> <li>1. Demonstrate correct usage of the terminology used to describe anatomical structures.</li> <li>2. Describe the organization of cells and tissues.</li> <li>3. Describe the principles relating to the structure of connective tissues, skeletal muscle, bones and joints.</li> <li>4. Describe the principles of excitable tissues.</li> <li>5. Describe the structure and function of the human eye and ear and the mechanisms of vision and hearing.</li> <li>6. Describe the principles of sensorimotor control.</li> <li>7. Describe cardiac mechanics and cardiac biophysics.</li> <li>8. Describe the application of technologies and techniques for investigating the structure and function of the body.</li> </ol>
<p><b>Indicative Contents</b></p>	<p>Anatomical terminology.  The structure and appearance of cells and tissues.  The appearance of bone and cartilage, the organization of dense connective tissues.  Skeletal muscle structure and function.  Principles of excitable tissues.  The structure and function of sensory systems, including the eye and vision and the ear and hearing.  Principles of sensory motor control.  Cardiac mechanics and cardiac biophysics.  Technologies, quantitative measurements and experimental techniques used to investigate the structure and function of different tissues, organs and organ systems.</p>

## Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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<b>Student Workload (SWL)</b>				
<b>Structured SWL (h/sem)</b>		<b>Structured SWL (h/w)</b>		
<b>Unstructured SWL (h/sem)</b>		<b>Unstructured SWL (h/w)</b>		
<b>Total SWL (h/sem)</b>				
<b>Module Evaluation</b>				
	<b>Time/Number</b>	<b>Weight (Marks)</b>	<b>Week Due</b>	<b>Relevant Learning Outcome</b>
<b>Formative assessment</b>	<b>Quizzes</b>			
	<b>Assignments</b>			
	<b>Projects / Lab.</b>			
	<b>Report</b>			
<b>Summative assessment</b>	<b>Midterm Exam</b>			
	<b>Final Exam</b>			
<b>Total assessment</b>				

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Cells and their function
<b>Week 2</b>	Tissues, glands & membranes
<b>Week 3</b>	Muscle tissue
<b>Week 4</b>	The skeleton
<b>Week 5</b>	Nervous system

<b>Week 6</b>	Sensory
<b>Week 7</b>	Respiration
<b>Week 8</b>	The eye
<b>Week 9</b>	The joints
<b>Week 10</b>	The skin
<b>Week 11</b>	Digestive system
<b>Week 12</b>	The urinary system and body fluids
<b>Week 13</b>	The heart
<b>Week 14</b>	Blood
<b>Week 15</b>	Blood vessels Blood clotting
<b>Week 16</b>	Preparatory week before the final exam

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>		
<b>Recommended Texts</b>		
<b>Websites</b>		

### Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54). The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Courses specification for Second class Medical Engineering (Second Course)

Module Information			
<b>Module Title</b>	Signals and Systems	<b>Module Delivery</b>	
<b>Module Type</b>	Core	<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	NEEM210		
<b>ECTS Credits</b>	6		
<b>SWL (hr/sem)</b>	150		
<b>Module Level</b>	1	<b>Semester of Delivery</b>	4
<b>Administering Department</b>	Type Dept. Code	<b>College</b>	Type College Code
<b>Module Leader</b>		<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>	Assistant Professor	<b>Module Leader's Qualification</b>	Ph.D.
<b>Module Tutor</b>	Name (if available)	<b>e-mail</b>	E-mail
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>	25/06/2023	<b>Version Number</b>	1.0

Relation with other Modules			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<p>Student will be able to:</p> <ol style="list-style-type: none"> <li>25. identify systems concepts .</li> <li>26. understand the properties of systems .</li> <li>27. understand the mathematical relation between input and output of a system.</li> <li>28. deal with Fourier and Laplace analysis of systems.</li> <li>29. perform z-transform of discrete signals .</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>53. Definition of the system concept.</li> <li>54. Introduction of mathematical models.</li> <li>55. Explain Continuous time systems. Discrete time systems.</li> <li>56. Introduction of frequency response of systems.</li> <li>57. Definition of filters.</li> <li>58. Explain Ideal filters, Non ideal filters, and Butterworth filter design.</li> <li>59. Define Z-transform of discrete signals.</li> <li>60. Analyze of continuous system using Laplace Transform. System transfer function.</li> <li>61. Definition of transfer function of a discrete system.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p>Introduction to systems:</p> <ul style="list-style-type: none"> <li>- Definition and mathematical models.</li> <li>- Properties of systems.</li> </ul> <p>Transformation used with continuous systems</p> <ul style="list-style-type: none"> <li>- Fourier transforms.</li> <li>- Filters.</li> <li>- Laplace transform.</li> </ul> <p>Z-transform:</p> <ul style="list-style-type: none"> <li>- Introduction of z- transform of discrete time signal.</li> <li>- Z-transform used with discrete systems.</li> </ul>

	Convolution used for <ul style="list-style-type: none"> <li>- Continuous systems.</li> <li>- Discrete systems</li> </ul>
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## Learning and Teaching Strategies

<b>Strategies</b>	To make students interesting with both types of systems: continuous and discrete. Also with properties of systems and operations . To make them familiar with time and frequency domain and analysis of a system. Also to make them familiar with different types of transforms of systems. Also to make them have an experience with solving different problems and examples.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	62	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	88	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	<b>150</b>		

## Module Evaluation

	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
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Formative assessment	Quizzes	6	10% (10)	γ, ρ, 9,12,13,15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	γ, ρ, 9,12,13,15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	γ, ρ, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

	Material Covered
<b>Week 1</b>	Basic definitions. Mathematical models.
<b>Week 2</b>	Continuous time systems.
<b>Week 3</b>	Discrete time systems.
<b>Week 4</b>	System properties.
<b>Weeks 5</b>	Mathematical system representation in time domain: Convolution representation.
<b>Week 6</b>	Convolution properties.
<b>Week 7</b>	System description by linear constant coefficient differential equations.
<b>Week 8</b>	Frequency domain analysis of continuous system.
<b>Week 9</b>	Frequency response of a system.
<b>Week 10</b>	Frequency response of electrical circuits.
<b>Week 11</b>	Filters. Distortion less transmission.
<b>Week 12</b>	Ideal filters. Non ideal filters. Butterworth filter design.



<b>Week 13</b>	Analysis of continuous system using Laplace Transform.
<b>Week 14</b>	System transfer function.
<b>Week 15</b>	Analysis of discrete system using z-Transform. System transfer function.

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	Signals and Systems. Simon S. Haykin	Yes
<b>Recommended Texts</b>	Signals and linear Systems. G. E. Carlson	

### Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
<b>Module Title</b>	Engineering Analysis II		<b>Module Delivery</b>
<b>Module Type</b>	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEE209		
<b>ECTS Credits</b>	3		
<b>SWL (hr/sem)</b>	75		
<b>Module Level</b>	2	<b>Semester of Delivery</b>	2
<b>Administering Department</b>	Electronics dept	<b>College</b>	Electronics engineering college
<b>Module Leader</b>	Dr. Omar B Mohammed	<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Lecturer	<b>Module Leader's Qualification</b>	Ph.D.
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>		<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	

Relation with other Modules			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

**Module Aims, Learning Outcomes and Indicative Contents**

<b>Module Aims</b>	This course covers the following topics: ordinary differential equations, sequences and series, solution of differential equations by power series, and matrix analysis. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	Upon successful completion, students will: 7. Improve their problem-solving skills. 8. Apply that knowledge toward practical problems in different areas of science. 9. Utilize the computer capabilities to solve such problems using proper methods. 10. Learn how to represent any function as a power series, then use computer to solve it. 11. Learn the importance of differential equations for modeling almost any system, and how to solve it to find the properties of that system. 12. Learn the linear algebra and its importance in science.
<b>Indicative Contents</b>	Ordinary Differential Equations. Sequences and Series. Solution of Differential Equations by Power Series. Matrix Analysis.

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>		<b>Structured SWL (h/w)</b>	
<b>Unstructured SWL (h/sem)</b>		<b>Unstructured SWL (h/w)</b>	

<b>Total SWL (h/sem)</b>				
<b>Module Evaluation</b>				
		<b>Time/Number</b>	<b>Weight (Marks)</b>	<b>Week Due</b>
	<b>Relevant Learning Outcome</b>			
<b>Formative assessment</b>	<b>Quizzes</b>			
	<b>Assignments</b>			
	<b>Projects / Lab.</b>			
	<b>Report</b>			
<b>Summative assessment</b>	<b>Midterm Exam</b>			
	<b>Final Exam</b>			
<b>Total assessment</b>				

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Ordinary Differential Equations: <ol style="list-style-type: none"> <li>1. First order (variables separable, homogeneous, linear and exact).</li> <li>2. Second order homogeneous.</li> <li>3. Second order nonhomogeneous; indeterminate coefficients, variation of parameters.</li> </ol>
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	
<b>Week 5</b>	Infinite Sequences and Series: <ol style="list-style-type: none"> <li>1. Limit laws, indeterminate forms and L'hospital rule.</li> <li>2. Infinite series; convergence test.</li> <li>3. Power series; Taylor and Maclaurin series.</li> </ol>
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	Solution of Differential Equations by Power Series: Power series method, Legendre's equation; Legendre s polynomials.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	Matrix Analysis: <ol style="list-style-type: none"> <li>1. Review of matrix theory, solving system of equations; Cramer's rule, inverse of the matrix method, Gauss elimination.</li> <li>2. Eigen values and eigen vectors.</li> <li>3. Diagonalization of matrices</li> </ol>
<b>Week 14</b>	
<b>Week 15</b>	

	4. Application of matrices to electric circuits.
<b>Week 16</b>	Preparatory week before the final Exam

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney & Thomas	Yes
<b>Websites</b>		

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
Module Title	Electronic II		Module Delivery
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEEELM221		
ECTS Credits	6		
SWL (hr/sem)	150		
Module Level	1	Semester of Delivery	2
Administering Department	Electronics	College	Electronic Engineering college
Module Leader		e-mail	
Module Leader's Acad. Title	Assistant Prof.	Module Leader's Qualification	PhD
Module Tutor		e-mail	
Peer Reviewer Name	Name	e-mail	Ahmad.younis@uoninevah.edu,iq
Scientific Committee Approval Date	12/06/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module	Electronic I	Semester	1
Co-requisites module	None	Semester	

### Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ul style="list-style-type: none"> <li>24. To understand the basic theory and operation of bipolar transistor</li> <li>25. To be familiar with current mechanism in an npn and pnp transistors</li> <li>26. To concentrate transistor physical and electrical characteristics</li> <li>27. To illustrate and design different dc biasing circuits</li> <li>28. To understand the biasing stability conditions</li> <li>29. To be able to deal with the mathematical behavior of transistor model</li> <li>30. To understand small signal analysis of transistor amplifier</li> <li>31. To deal with different transistor amplifier configuration</li> <li>32. To be able to deal with the frequency response of transistor amplifier</li> <li>33. To understand the basic operation of field effect transistor and MOS device</li> <li>34. To understand the dc and ac behavior of FET and MOS amplifiers</li> </ul>
<b>Module Learning Outcomes</b>	<ul style="list-style-type: none"> <li>24. Understand and apply the basic theory and operation of transistor amplifiers</li> <li>25. Define and explain the electrical characteristic of bipolar transistor</li> <li>26. Understand the basic structure of npn and pnp transistors</li> <li>27. Understand and analyze the electrical transistor model</li> <li>28. Understanding the dc and ac analysis of transistor amplifier</li> <li>29. Dealing with dc biasing and ac amplifiers</li> <li>30. Understanding the effect of frequency on amplifier response</li> <li>31. Familiar with other FET and MOS circuits</li> </ul>

<b>Indicative Contents</b>	<p><b>Bipolar junction transistors,</b>  Transistor construction, transistor operation,  NPN &amp; PNP Bipolar Transistor; Current Flow Mechanism in Transistor Junctions; Transistor configurations; Current Gain Calculation [Alpha] and [Beta]; Transistor input/ output characteristics; DC Load line ; Operating point; Different DC circuit biasing. Bias circuit, voltage divider circuit bias with feedback</p> <p><b>DC biasing,</b>  Operating point, fixed bias circuit, emitter bias circuit, voltage divider circuit, dc bias with feedback</p> <p><b>Biasing stability</b>  Stability factor analysis due to temperature variation ( Effect of <math>I_{co}</math>, <math>V_{be}</math> and <math>\beta</math> ); Temperature compensation using diode biasing.</p> <p><b>Small signal analysis,</b>  Small signal equivalent circuit for CB, CE and CC configuration; Input/Output resistance; Calculation of current and voltage Gain in small signal amplifier; Graphical Analysis for voltage gain; Hybrid parameters to analyze transistor circuits.</p> <p><b>Field Effect Transistor (FET) and MOS transistor :</b>  <b>FET biasing configurations, Depletion and Enhanced mode operation,</b>  Introduction to the theory and operations of JFET &amp; MOSFET; FET Transistor configurations; Transistors transfer characteristics; Amplifier Circuit Biasing; transistor Equivalent circuit; signal analysis of FET transistor.</p> <p><b>FREQUENCY RESPONSE:</b>  Definition and Concepts; Gain in decibel; Bode plot for the gain; The effect of the Coupling capacitor; Low frequency analysis due to the R-C Coupled amplifier in BJTs; the Effect of emitter bypass capacitor; Calculation of the Low cut-off frequency. Transistor amplifier at high frequency; Hybrid <math>\pi</math> equivalent circuit at high frequency; High frequency behavior of CB &amp; CE amplifier; High cut-off frequency; Gain Band-Width products for the above circuits; FET at high frequency; CD and CS amplifier at high frequency;</p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	<p>The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.</p>



Student Workload (SWL)			
Structured SWL (h/sem)	88	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	76	Unstructured SWL (h/w)	1
Total SWL (h/sem)	164		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	Υ, ϕ, ρ,12,13,15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	Υ, ϕ, ρ,12,13,15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	Υ, ϕ, ρ,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1:30hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Transistor construction and operation
Week 2	Bipolar transistor current flow mechanism
Week 3	Transistor configurations, current gain calculation, and input and output resistances
Week 4	Dc biasing circuits, operating point calculation
Week 5	Biasing stability, stability factor calculation
Week 6	Temperature compensation using diode biasing
Week 7	Small signal equivalent circuit for CB, CC, CE configurations
Week 8	Calculation of voltage and current gains
Week 9	Hybrid model ac analysis of transistor amplifier
Week 10	FET and MOS transistors operation

<b>Week 11</b>	FET biasing configurations
<b>Week 12</b>	Depletion and enhancement mode operation
<b>Week 13</b>	Definition and analysis of amplifier frequency response
<b>Week 14</b>	Low frequency and high frequency analysis
<b>Week 15</b>	Hybrid-Pie equivalent circuit at high frequency
<b>Week 16</b>	<b>Subject review</b>

### Delivery Plan (Weekly Lab. Syllabus)

	Material Covered
<b>Week 1-15</b>	<p><b>Practical experiments in transistor amplifiers to measure the current and voltage gains.</b></p> <p><b>To measure the input and output amplifier resistances</b></p> <p><b>To measure the amplifier frequency response.</b></p>

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	<p><b>Textbook1:INTEGRATED ELECTRONICS" MCGRAWHILL;9TH EDITION,1995.ByMILLMAN&amp;HALKIES</b></p> <p><b>2: " ELECTRONICS DEVICES AND COMPONENTS", PITMAN, 1995 By MOTTERSLED,</b></p>	yes
<b>Recommended Texts</b>	<p><b>3: " SOLID STATE DEVICES", PHI; 4TH EDITION, 1995.By STREETMAN,</b></p> <p><b>4" SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992.By : M.S. TYAGI</b></p>	Yes
<b>Websites</b>	Electronic circuits	

## Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54). The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
معلومات المادة الدراسية			
Module Title	Programming		Module Delivery
Module Type	Core		<input checked="" type="checkbox"/> Theory
Module Code	NVEEELM222		<input checked="" type="checkbox"/> Lecture
ECTS Credits	6		<input checked="" type="checkbox"/> Lab
SWL (hr/sem)	150		<input type="checkbox"/> Tutorial
			<input type="checkbox"/> Practical
			<input type="checkbox"/> Seminar
Module Level	UGx11 2	Semester of Delivery	2
Administering Department	Dept. of Electronic Eng. (Med. Ele)	College	College of Electronic Engineering
Module Leader	Qais Thanon	e-mail	Qais.najim@uoninevah.edu.iq
Module Leader's Acad. Title	Prof.	Module Leader's Qualification	Ph. D.
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	20/06/2023	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	<ol style="list-style-type: none"> <li>1. Learning about the algorithms types and how building the algorithms.</li> <li>2. Learning how to command computers to perform tasks using C++ language (Programming/coding).</li> <li>3. Become acquainted with the designed programming including sequencing, condition and iteration.</li> <li>4. Learn about the 1d and 2d arrays in C++ language.</li> <li>5. Learn about the functions in C++ language.</li> </ol>

	6. Learn about the strings in C++ language.
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	<ol style="list-style-type: none"> <li>Understanding the meaning of the algorithms in programming languages.</li> <li>Understanding the basics concepts of C language programming such as variables, data types, operators, control</li> <li>Understanding the utilities of each one of sequencing, condition, and loops, and basic input/output operations.</li> <li>Understanding how represent the data in 1d arrays and 2d arrays.</li> <li>Learn about how the strings represented in C language.</li> <li>Learn about divide any problem in sub-program and execute this problem by using function.</li> <li>In advance practical experience by working on programming exercises and projects.</li> </ol>
<b>Indicative Contents</b> المحتويات الإرشادية	<p>Indicative content includes the following.</p> <ul style="list-style-type: none"> <li>Visualization via flowchart and Pseudocode [4 hrs]</li> <li>Keywords, identifier, format specifier, and naming variables and constants [8 hrs]</li> <li>Use standard libraries to take input and display output [8 hrs]</li> <li>Operators in C++ programming [10 hrs]</li> <li>Priorities in C++ programming [4 hrs]</li> <li>Math functions [4 hrs]</li> <li>Conditional operations [8 hrs]</li> <li>Iterations (Loop operators) [10 hrs]</li> <li>Arrays [10 hrs]</li> <li>Functions [8 hours]</li> <li>Review classes and problem solving [8 hrs]</li> </ul>

<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم			
<b>Strategies</b>	The main strategy being focused on is developing conceptual programming thinking, meanwhile refining and expanding their mathematical thinking skills. This will be achieved through classes, online lectures, interactive tutorials. Additionally, working on complex projects that challenge students' skills and require to apply advanced concepts. Such projects would help students exploring various aspects of C++ programming and gain hands-on experience in solving complex problems. some sampling activities that are interesting to the students.		
<b>Student Workload (SWL)</b> الحمل الدراسي للطلاب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطلاب خلال الفصل	77	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطلاب أسبوعيا	5.1
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطلاب خلال الفصل	73	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطلاب أسبوعيا	4.8
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطلاب خلال الفصل	<b>150</b>		

<b>Module Evaluation</b> تقييم المادة الدراسية				
As	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome

Formative assessment	Quizzes	2	10% (10)	5 and 10	LO #1, #2 and #10, #11
	Assignments	2	10% (10)	2 and 12	LO #3, #4 and #6, #7
	Projects / Lab.	1	10% (10)	Continuous	All
	Report	1	10% (10)	13	LO #5, #8 and #10
Summative assessment	Midterm Exam	2hr	10% (10)	7	LO #1 - #7
	Final Exam	3hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

#### المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Introduction to computer languages and the structure of C program
Week 2	Flowchart and Pseudo-code
Week 3	Introduction to C++ programming: Declare variables and constants
Week 4	Take input and print output
Week 5	Assignment and Increment ,Decrement, Arithmetic, Logical, and Bitwise operators
Week 6	Standard math functions in math header <math.h>
Week 7	Priorities of operators in C++ programming
Week 8	Relational and conditional operators
Week 9	Mid-term Exam
Week 10	If statement versus switch case statement
Week 11	Examples of structured programming (sequencing and condition)
Week 12	Loop operators (For, while, do-while)
Week 13	Arrays
Week 14	Functions
Week 15	String of characters
Week 16	Preparatory week before the final Exam

### Delivery Plan (Weekly Lab. Syllabus)

#### المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1-2	Learn the C++ language program compiler.
Week 3-4	Declare variables and constants and <iostream.h> including standard functions
Week 5-6	Arithmetic, logical, and bitwise operators
Week 7-8	Math header for math functions <math.h> and Assignment and increment & decrement operators
Week 9-10	Relational and conditional operators and Loop operators
Week 11-12	Examples about the Arrays
Week 13-14	Examples about Functions and string

## Learning and Teaching Resources

### مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	C Programming Absolute Beginner's Guide, 3rd Edition 2014. BY: Greg Perry and Dean Miller.	Yes
<b>Recommended Texts</b>	C How to Program with an introduction to C++, 8 <sup>th</sup> Edition 2016. BY: Paul Deitel and Harvey Deitel. Global Edition contribution by Piyali Sengupta	No
<b>Websites</b>	1- <a href="https://www.programiz.com/c-programming">https://www.programiz.com/c-programming</a> 2- <a href="https://www.coursera.org/specializations/c-programming">https://www.coursera.org/specializations/c-programming</a>	

## Grading Scheme

### مخطط الدرجات

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
Module Title	Electromagnetic Fields II		Module Delivery
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE221		
ECTS Credits	6		
SWL (hr/sem)	150		
Module Level	2	Semester of Delivery	2
Administering Department	Type Dept. Code	College	Type College Code
Module Leader	SINAN KHALID SHANSHAL		e-mail: <a href="mailto:sinan.mohammed@uoninevah.edu.iq">sinan.mohammed@uoninevah.edu.iq</a>
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc.
Module Tutor		e-mail	
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	02/07/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	



## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	To develop knowledge of the laws governing the behavior of magnetic and electromagnetic fields, and to relate the laws governing the fields to applications in a range of electrical and electronic engineering application.
<b>Module Learning Outcomes</b>	<p>On completion of the course the students should be able:</p> <ul style="list-style-type: none"> <li>• to have detailed knowledge of the physical background and terminology of the electromagnetic field theory for electrical engineering problems</li> <li>• to understand the electromagnetic field behavior</li> <li>• to select and use appropriate theoretical models for analysis, problem solving and finding solutions related to the electrostatic, magnetostatic and electromagnetic fields</li> <li>• to understand how laws of electromagnetism can be applied to problems arising in engineering.</li> </ul>
<b>Indicative Contents</b>	Magnetic field and Ampere's Law Magnetic flux and Gauss's Law for magnetic fields Faraday's Law Inductance Maxwell's equations Applications of Electromagnetics

## Learning and Teaching Strategies

<b>Strategies</b>	Through the presentation of a theoretical explanation with the aid of white board and 'Data Show', to illustrate syllabus (examples and exercises) and using text books.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	45	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	?	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	?		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	15% (10)	5,8,10,12	LO #1-5,6-7, 9 and 11
	Assignments	4	15% (10)	6,9,11,13	LO # 1-5, 6, 10 and 12
	Projects	0	0% (0)		
	Report	0	0% (0)		
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-8
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Review of Vector Calculus
Week 2	Review of Vector Calculus
Week 3	Boit – Savart law
Week 4	Amperes law; Magnetic Flux & Magnetic Flux Density
Week 5	Inductance
Week 6	Force on Moving Charge; Force on Differential Current. Elements
Week 7	Force and Torque on a Closed Circuit
Week 8	Magnetization and Permeability; Magnetic Boundary Conditions; Magnetic Circuit.
Week 9	Faraday's Law;
Week 10	Maxwell's Equations
Week 11	Example of Maxwell's Equations
Week 12	Wave Equations; Wave Propagation in Lossy Dielectrics
Week 13	Plane Waves in Lossless Dielectrics; Plane Waves in Free Space
Week 14	Plane Waves in Good Conductors;
Week 15	Power and the Poynting Vector.
Week 16	Preparatory week before the final Exam

Delivery Plan (Weekly Lab. Syllabus)
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	<b>Material Covered</b>
<b>Week 1-15</b>	

<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	1-ENGINEERING ELECTROMAGNETICES, Mc- Graw Hill, By WILLAIM H. HAYT.  2-Elements of engineering electromagnetic, Prentice Hall, By Matthew N. O. SADIKU	No
<b>Recommended Texts</b>	1-Electromagnetics (Schaum's Outlines), McGraw-Hill Education, By Edminister, Joseph_ Nahvi, Mahmood.	No
<b>Websites</b>		

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information			
معلومات المادة الدراسية			
Module Title	<u>English</u>		Module Delivery
Module Type	<u>Support</u>		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	<u>NVU11</u>		
ECTS Credits	<u>3</u>		
SWL (hr/sem)	<u>75</u>		
Module Level	UGx11 1	Semester of Delivery	1
Administering Department		College	NV
Module Leader		e-mail	
Module Leader's Acad. Title	Noor Mothafar Hamid	Module Leader's Qualification	MS.D.
Module Tutor	Name (if available)	e-mail	noorm.hame@duoninevah.edu.iq
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	01/06/2023	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Aims</b> أهداف المادة الدراسية	35. To develop skills, reading, writing and understanding of English language through the application of teaching techniques. 36. To understand scientific subjects and technical terms through reading and comprehension. 37. This course deals with the basic concepts of scientific subjects. 38. This course handles how to write simple research and how to make a successful presentation. 39. To understand the scientific language in English.

<p><b>Module Learning Outcomes</b></p> <p>مخرجات التعلم للمادة الدراسية</p>	<p>32. Recognize parts of speech and tenses in English language.</p> <p>33. List the various terms associated with scientific texts.</p> <p>34. Summarize what is meant by a basic electric circuit.</p> <p>35. Discuss Electric currents, series and parallel circuits.</p> <p>36. Describe electrical power, charge, and current.</p> <p>37. Discuss computers, communication and the future of computers..</p> <p>38. Identify the basic circuit elements and their applications.</p> <p>39. Explain energy types and forms.</p> <p>40. Discuss the various properties of radio waves and vacuum tubes.</p> <p>41. Explain modulation.</p> <p>42. Discuss Electromagnetism.</p>
<p><b>Indicative Contents</b></p> <p>المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p>1.parts of speech            _verb            _noun            _pronoun        2.Tenses            _Past            _Present            _future        3.Electric currents and circuit            _AC/DC            _parallel, serious            _Grounding, fuse, short circuit        4.Radio waves and vacuum tubes        5. Electromagnetism.        6. The future of computers, communication applications.            _fiber optics.        7. Induction.            _Electric generator            _Electric transformer            _self-induction            _servomechanism        8. Incandescent lamp.        9. Energy.            _types of energy            _forms of energy        10. Introduction to electron and electricity.        11.Electricity and electronics.</p>

<p><b>Learning and Teaching Strategies</b></p> <p>استراتيجيات التعلم والتعليم</p>	
<p><b>Strategies</b></p>	

	The main strategy that will be adopted in delivering this module is to encourage students' participation by reading, writing and comprehension in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, presentation, interactive tutorials, by considering type of simple experiments involving some sampling activities that are interesting to the students.
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<b>Student Workload (SWL)</b> الحمل الدراسي للطالب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	30	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعياً	2
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	70	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعياً	5
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	100		

<b>Module Evaluation</b> تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	3	10% (10)	4,6	LO #1, 2, 3,4 ,5and 6
	<b>Assignments</b>	2	10% (10)	9, 12	LO # 7,8,9,10,11and 12
	<b>Projects / Lab.</b>				
	<b>Report</b>	1	10% (10)	13	LO # 13,14
<b>Summative assessment</b>	<b>Midterm Exam</b>	2 hr	10% (10)	7	LO #
	<b>Final Exam</b>	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
Week	Material Covered
<b>Week 1</b>	Parts of speech
<b>Week 2</b>	Tenses
<b>Week 3</b>	Electric currents and circuit

<b>Week 4</b>	Radio waves and vacuum tubes
<b>Week 5</b>	The future of computers, communication applications.
<b>Week 6</b>	Induction -Electric generator -Electric transformer
<b>Week 7</b>	Mid-term Exam
<b>Week 8</b>	Induction -Self-induction -Servomechanism
<b>Week 9</b>	Incandescent lamp.
<b>Week 10</b>	Energy. -types of energy -forms of energy
<b>Week 11</b>	Introduction to electron and electricity.
<b>Week 12</b>	Electricity and electronics
<b>Week 13</b>	The cathode ray tube
<b>Week 14</b>	Propagation
<b>Week 15</b>	Modulation
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

### Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	
Week 2	
Week 3	
Week 4	
Week 5	
Week 6	
Week 7	

### Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	English in electrical engineering and electronics. The language of electrical and electronic engineering in English.	Yes

<b>Recommended Texts</b>	English for electrical engineering and computing.	No
<b>Websites</b>	<a href="https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk">https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk</a>	

## Grading Scheme

### مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX</b> - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> - Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



## Courses specification for Second class Industrial Electronic Engineering (Second Course)

Module Information			
Module Title	<u>DC Machines</u>	Module Delivery	
Module Type	<u>Core</u>	<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	<u>NEEI2313</u>		
ECTS Credits	<u>6</u>		
SWL (hr/sem)	<u>114</u>		
Module Level	2    2	Semester of Delivery	1
Administering Department	Electronic Dept.	College	Electronics collage
Module Leader		e-mail	
Module Leader's Acad. Title		Module Leader's Qualification	
Module Tutor		e-mail	
Peer Reviewer Name		e-mail	
Scientific Committee Approval Date		Version Number	

Relation with other Modules			
Prerequisite module	DC Circuit Analysis	Semester	1
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	40. Understanding DC Machine Principles 41. Analyzing DC Machine Behavior 42. Control Strategies 43. System Integration 44. Practical Applications 45. Problem-Solving Skills 46. Laboratory Skills 47. Teamwork and Communication 48. Professional Development
<b>Module Learning Outcomes</b>	43. Understand how voltage is induced in a rotating loop 44. Understand how curved pole faces contribute to a constant flux, and thus 45. more constant output voltages. 46. Understand how curved pole faces contribute to a constant flux, and thus more constant output voltages. 47. Understand the power flow diagram for de machines 48. Know the types of de motors in general use. 49. Understand the equivalent circuit of a de motor. 50. Understand how to derive the torque-speed characteristics of separately excited, shunt, series, and compounded de motors. 51. Understand how to control the speed of different types of de motors. 52. Understand the special characteristics of series de motors, and the applications. 53. Understand the methods of starting dc motors safely. 54. Understand the equivalent circuit of a dc generator. 55. Understand the purpose of a transformer in a power system. 56. Understand how real transformers approximate the operation of an ideal transformer. 57. Be able to explain how copper losses, leakage flux, hysteresis, and eddy currents are modeled in transformer equivalent circuits.
<b>Indicative Contents</b>	Introduction - A Simple Rotating Loop between Curved Pole Faces. The Voltage Induced in a Rotating Loop / Getting DC Voltage Out of the Rotating Loop / The Induced Torque in the rotating loop.(12 hrs.). Commutation and Armature Construction in Real DC Machine. .(8 hrs.). Power Flow and Losses in DC Machines. .(6 hrs.).

	Introduction to DC Motors. The Equivalent Circuit of a DC Motor. The Magnetization Curve of a DC Machine. Separately Excited and Shunt DC Motors.(10 hrs.).
	Permanent-Magnet DC Motor. The Series DC Motor. The Compounded DC Motor. (6 hrs.).
	Motor Starters. Solid-State Speed Controllers. (12 hrs.).
	DC Motor Efficiency Calculations. (4 hrs.).
	Mid-term Exam. (3 hrs.).
	Introduction to DC Generators. The Separately Excited Generator. (12 hrs.).
	The Shunt DC Generator. The Series DC Generator.(4 hrs.).
	The Cumulatively Compounded DC Generator. The Differentially Compounded DC Generator. (4 hrs.).
	Types and Construction of Transformers. The Ideal Transformer. (10 hrs.).
	Theory of Operation of Real Single-Phase Transformers. The Equivalent Circuit of a Transformer. (18 hrs.).
	Transformer Voltage Regulation and Efficiency. (12 hrs.).
	Instrument Transformers. (4 hrs.).

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	Visual Aids Problem-Solving Exercises Real-World Applications Group Projects  Simulations and Virtual Labs Multimedia Resources Real-Life Examples

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>	74	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	40	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	114		

<b>Module Evaluation</b>					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
	<b>Quizzes</b>	2	10% (10)	5, 10	

<b>Formative assessment</b>	<b>Assignments</b>	2	10% (10)	2, 12	
	<b>Projects / Lab.</b>	1	10% (10)	Continuous	
	<b>Report</b>	1	10% (10)	13	
<b>Summative assessment</b>	<b>Midterm Exam</b>	2 hr	10% (10)	7	
	<b>Final Exam</b>	2hr	50% (50)	16	
<b>Total assessment</b>			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

<b>Week</b>	<b>Material Covered</b>
<b>Week 1</b>	Introduction - A Simple Rotating Loop between Curved Pole Faces. The Voltage Induced in a Rotating Loop / Getting DC Voltage Out of the Rotating Loop / The Induced Torque in the rotating loop.
<b>Week 2</b>	Commutation and Armature Construction in Real DC Machine.
<b>Week 3</b>	Power Flow and Losses in DC Machines.
<b>Week 4</b>	Introduction to DC Motors. The Equivalent Circuit of a DC Motor. The Magnetization Curve of a DC Machine. Separately Excited and Shunt DC Motors
<b>Week 5</b>	Permanent-Magnet DC Motor. The Series DC Motor. The Compounded DC Motor.
<b>Week 6</b>	Motor Starters. Solid-State Speed Controllers.
<b>Week 7</b>	DC Motor Efficiency Calculations.
<b>Week 8</b>	Mid-term Exam.
<b>Week 9</b>	Introduction to DC Generators. The Separately Excited Generator.
<b>Week 10</b>	The Shunt DC Generator. The Series DC Generator
<b>Week 11</b>	The Cumulatively Compounded DC Generator. The Differentially Compounded DC Generator.
<b>Week 12</b>	Types and Construction of Transformers. The Ideal Transformer.
<b>Week 13</b>	Theory of Operation of Real Single-Phase Transformers. The Equivalent Circuit of a Transformer.
<b>Week 14</b>	Transformer Voltage Regulation and Efficiency.
<b>Week 15</b>	Instrument Transformers.
<b>Week 16</b>	Preparatory week before the final Exam

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Electrical Machinery Fundamentals” edited by Stephen J. Chapman.	NO
<b>Recommended Texts</b>	electrical machines and transformer by: Ancieron and Macneil	NO
<b>Websites</b>	<a href="https://www.coursera.org">https://www.coursera.org</a>	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (فيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required
<p><b>Note:</b>Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

### Module Information

<b>Module Title</b>	<b><u>Electronic I</u></b>		<b>Module Delivery</b>	
<b>Module Type</b>	<b><u>core</u></b>		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	<b><u>NVEEELI212</u></b>			
<b>ECTS Credits</b>	<b><u>7</u></b>			
<b>SWL (hr/sem)</b>	<b><u>164</u></b>			
<b>Module Level</b>	1	<b>Semester of Delivery</b>	1	
<b>Administering Department</b>	Electronics	<b>College</b>	Electronic Engineering college	
<b>Module Leader</b>		<b>e-mail</b>		
<b>Module Leader's Acad. Title</b>	Assistant Prof.	<b>Module Leader's Qualification</b>	PhD	
<b>Module Tutor</b>		<b>e-mail</b>		
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	Ahmad.younis@uoninevah.edu,iq	
<b>Scientific Committee Approval Date</b>	12/06/2023	<b>Version Number</b>	1.0	

### Relation with other Modules

<b>Prerequisite module</b>	NEEI1223	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ul style="list-style-type: none"> <li>49. To understand the basic theory and operation of bipolar transistor</li> <li>50. To be familiar with current mechanism in an npn and pnp transistors</li> <li>51. To concentrate transistor physical and electrical characteristics</li> <li>52. To illustrate and design different dc biasing circuits</li> <li>53. To understand the biasing stability conditions</li> <li>54. To be able to deal with the mathematical behavior of transistor model</li> <li>55. To understand small signal analysis of transistor amplifier</li> <li>56. To deal with different transistor amplifier configuration</li> <li>57. To be able to deal with the frequency response of transistor amplifier</li> <li>58. To understand the basic operation of field effect transistor and MOS device</li> <li>59. To understand the dc and ac behavior of FET and MOS amplifiers</li> </ul>
<b>Module Learning Outcomes</b>	<ul style="list-style-type: none"> <li>58. Understand and apply the basic theory and operation of transistor amplifiers</li> <li>59. Define and explain the electrical characteristic of bipolar transistor</li> <li>60. Understand the basic structure of npn and pnp transistors</li> <li>61. Understand and analyze the electrical transistor model</li> <li>62. Understanding the dc and ac analysis of transistor amplifier</li> <li>63. Dealing with dc biasing and ac amplifiers</li> <li>64. Understanding the effect of frequency on amplifier response</li> <li>65. Familiar with other FET and MOS circuits</li> </ul>

<b>Indicative Contents</b>	<p style="text-align: right;"><b>Bipolar junction transistor</b></p> <p>Transistor construction, transistor operation; NPN &amp; PNP Bipolar Transistor; Current Flow Mechanism in Transistor Junctions; Transistor configurations; Current Gain Calculation [<math>\alpha</math>] and [<math>\beta</math>]; Transistor input/ output characteristics; DC Load line ; Operating point; Different DC circuit biasing. Bias circuit, voltage divider circuit, dc bias with feedback</p> <p><b>DC biasing,</b></p> <p>Operating point, fixed bias circuit, emitter bias circuit, voltage divider circuit, dc bias with feedback.</p> <p style="text-align: right;"><b>Biasing stability</b></p> <p>Stability factor analysis due to temperature variation ( Effect of <math>I_{co}</math>, <math>V_{be}</math> and <math>\beta</math> ); Temperature compensation using diode biasing.</p> <p><b>Small signal analysis,</b></p> <p>Small signal equivalent circuit for CB, CE and CC configuration; Input/Output resistance; Calculation of current and voltage Gain in small signal amplifier; Graphical Analysis for voltage gain; Hybrid parameters to analyze transistor circuits.</p> <p style="text-align: right;"><b>Field Effect Transistor (FET) and MOS transistor</b></p> <p><b>FET biasing configurations, Depletion and Enhanced mode operation</b></p> <p>Introduction to the theory and operations of JFET &amp; MOSFET; FET Transistor configurations; Transfer characteristics; Amplifier Circuit Biasing; transistor Equivalent circuit; Small signal analysis (transistor)</p> <p style="text-align: right;"><b>FREQUENCY RESPONSE</b></p> <p>Definition and Concepts; Gain in decibel; Bode plot for the gain; The effect of the Coupling capacitor; Low frequency analysis due to the R-C Coupled amplifier in BJTs; the Effect of emitter bypass capacitor; Calculation of the Low cut-off frequency. Transistor amplifier at high frequencies; Hybrid <math>\pi</math> equivalent circuit at high frequency; High frequency behavior of CB &amp; CE amplifier; High cut-off frequency; Gain Bandwidth products for the above circuits; FET at high frequencies; CD and CS amplifier at high frequency;</p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
<b>Student Workload (SWL)</b>	



<b>Structured SWL (h/sem)</b>	88	<b>Structured SWL (h/w)</b>	3
<b>Unstructured SWL (h/sem)</b>	76	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	164		

### Module Evaluation

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	, 9, 12, 13, 15 <sup>a</sup>	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	, 9, 12, 13, 15 <sup>a</sup>	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	, 9, 12, 13, 15 <sup>a</sup>	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1:30hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

Week	Material Covered
Week 1	Transistor construction and operation
Week 2	Bipolar transistor current flow mechanism
Week 3	Transistor configurations, current gain calculation, and input and output resistances
Week 4	Dc biasing circuits, operating point calculation
Week 5	Biasing stability, stability factor calculation
Week 6	Temperature compensation using diode biasing
Week 7	Small signal equivalent circuit for CB, CC, CE configurations
Week 8	Calculation of voltage and current gains
Week 9	Hybrid model ac analysis of transistor amplifier
Week 10	FET and MOS transistors operation
Week 11	FET biasing configurations
Week 12	Depletion and enhancement mode operation
Week 13	Definition and analysis of amplifier frequency response
Week 14	Low frequency and high frequency analysis
Week 15	Hybrid-Pie equivalent circuit at high frequency
Week 16	Subject review

### Delivery Plan (Weekly Lab. Syllabus)

Week	Material Covered
Week 1-15	<p><b>Practical experiments in transistor amplifiers to measure the current and voltage gains.</b></p> <p><b>To measure the input and output amplifier resistances</b></p> <p><b>To measure the amplifier frequency response.</b></p>

Learning and Teaching Resources		
	Text	Available in the Library?
Required Texts	<p><b>Textbook1:INTEGRATED ELECTRONICS" MCGRAWHILL;9TH EDITION,1995.ByMILLMAN&amp;HALKIES</b></p> <p><b>2: " ELECTRONICS DEVICES AND COMPONENTS", PITMAN, 1995 By MOTTERSLED,</b></p>	yes
Recommended Texts	<p><b>3: " SOLID STATE DEVICES", PHI; 4TH EDITION, 1995.By STREETMAN,</b></p> <p><b>4" SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992.By : M.S. TYAGI</b></p>	Yes
Websites	Electronic circuits	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

Module Information			
Module Title	<b>Fundamentals of <u>Electromagnetics</u></b>		Module Delivery
Module Type	<b><u>Base</u></b>		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEEELI221		
ECTS Credits	<b><u>4</u></b>		
SWL (hr/sem)	<b><u>45</u></b>		
Module Level	2	Semester of Delivery	1
Administering Department	Type Dept. Code	College	Type College Code
Module Leader	SINAN KHALID SHANSHAL	e-mail	sinan.mohammed@uoninevah.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.S c.
Module Tutor		e-mail	
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	02/07/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	To develop knowledge of the laws governing the behavior of electric and electro-magnetic fields, and to relate the laws governing the fields to applications in a range of electrical and electronic engineering application.
<b>Module Learning Outcomes</b>	<p>On completion of the course the students should be able:</p> <ul style="list-style-type: none"> <li>• to have detailed knowledge of the physical background and terminology of the electromagnetic field theory for electrical engineering problems</li> <li>• to understand the electromagnetic field behavior</li> <li>• to select and use appropriate theoretical models for analysis, problem solving and finding solutions related to the electrostatic, magnetostatic and electromagnetic fields</li> <li>• to understand how laws of electromagnetism can be applied to problems arising in engineering and biomedical sciences.</li> </ul>
<b>Indicative Contents</b>	Electric charge and the electric field Electric flux density and Gauss's Law Electric scalar potential Electric field in matter and boundary conditions Capacitance Magnetic field and Ampere's Law Magnetic flux and Gauss's Law for magnetic fields Faraday's Law Inductance Maxwell's equations Applications of Electromagnetics

## Learning and Teaching Strategies

<b>Strategies</b>	Through the presentation of a theoretical explanation with the aid of white board and 'Data Show', to illustrate syllabus (examples and exercises) and using text books.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	45	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	?	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	?		

## Module Evaluation

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	15% (10)	5,8,10,12	LO #1-5, 9 and 11
	Assignments	4	15% (10)	6,9,11,13	LO # 1-5, 6, 10 and 12
	Projects	0	0% (0)		
	Report	0	0% (0)		
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-8
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

Week	Material Covered
Week 1	Review of Vector Calculus
Week 2	Review of Vector Calculus
Week 3	Experimental law of coulomb; Electric field intensity;
Week 4	Field of a continuous and volume charge distributions; line charge and sheet charge;
Week 5	Electric flux law density; Gauss's law; Application of Gauss's law; Some symmetrical charge distributions.
Week 6	Energy expended in moving a point charge in an electric field
Week 7	Definition of potential difference and potential
Week 8	Potential field of a point charge and system of charges; Potential gradient;
Week 9	Boit – Savart law
Week 10	Amperes law
Week 11	Magnetic Flux and Magnetic Flux Density
Week 12	Force on Differential Current Elements; Force and Torque on a Closed Circuit;
Week 13	Faraday's Law; Maxwell's Equations
Week 14	Example of Maxwell's Equations
Week 15	Wave Equations.
Week 16	Preparatory week before the final Exam

## Delivery Plan (Weekly Lab. Syllabus)

Week	Material Covered
Week 1-15	

## Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	1-ENGINEERING ELECTROMAGNETICES, Mc- Graw Hill, By WILLAIM H. HAYT. 2-Elements of engineering electromagnetic, Prentice Hall, By Matthew N. O. SADIKU	No
<b>Recommended Texts</b>	1-Electromagnetics (Schaum's Outlines), McGraw-Hill Education, By Edminister, Joseph_ Nahvi, Mahmood.	No
<b>Websites</b>		

### Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
<b>Module Title</b>	Engineering analysisI		<b>Module Delivery</b>
<b>Module Type</b>	Base		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEE208		
<b>ECTS Credits</b>	6		
<b>SWL (hr/sem)</b>	150		
<b>Module Level</b>	2	<b>Semester of Delivery</b>	1
<b>Administering Department</b>	Electronics dept	<b>College</b>	Electronics engineering college
<b>Module Leader</b>	Dr. Omar B Mohammed	<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Lecturer	<b>Module Leader's Qualification</b>	Ph.D.
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>		<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	

Relation with other Modules			
<b>Prerequisite module</b>	Mathematics II	<b>Semester</b>	1
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	This course covers the following topics: Multiple Integrals, Vectors Functions, Numerical Analysis, Statistics and Probability. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	Upon successful completion, students will: 13. Improve their problem-solving skills. 14. Apply that knowledge toward practical problems in different areas of science. 15. Utilize the computer capabilities to solve such problems using proper methods. 16. Learn how to deal with geometry in 3D; find areas and volumes. 17. Solve ordinary and differential equations numerically. 18. Learn the importance of probability and statistics in everyday use.
<b>Indicative Contents</b>	Vectors Functions Multiple Integrals Numerical Analysis Statistics Probability

## Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the
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	consideration of simple experiments involving sampling activities that students find interesting.
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<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>		<b>Structured SWL (h/w)</b>	
<b>Unstructured SWL (h/sem)</b>		<b>Unstructured SWL (h/w)</b>	
<b>Total SWL (h/sem)</b>			

<b>Module Evaluation</b>					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>				
	<b>Assignments</b>				
	<b>Projects / Lab.</b>				
	<b>Report</b>				
<b>Summative assessment</b>	<b>Midterm Exam</b>				
	<b>Final Exam</b>				
<b>Total assessment</b>					

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Vectors: Vector in space, dot and cross product. Lines and planes in space.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	Vector valued functions and motion in space: position, velocity and acceleration, tangential vectors, curvature and normal vector.
<b>Week 5</b>	Multiple Integrals: Double Integral in rectangular coordinates, areas and volumes. Double Integral in Polar Coordinates, areas and volumes. Triple Integrals in rectangular, cylindrical, and spherical coordinates, volumes.
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	Numerical Analysis: Solution of non-linear equations by iteration; bisection and Newton-Raphson.
<b>Week 10</b>	

<b>Week 11</b>	Numerical Integration; trapezoidal rule.
<b>Week 12</b>	Numerical solution of 1st order ordinary differential equations; Euler's method.
<b>Week 13</b>	Statistics and Probability:
<b>Week 14</b>	Definitions, mutually exclusive and conditional probability, permutations and combinations
<b>Week 15</b>	Probability distribution: binomial, normal and Poisson distributions.
<b>Week 16</b>	Preparatory week before the final Exam

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney & Thomas	Yes
<b>Websites</b>		

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX</b> - Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> - Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

<b>Module Information</b> معلومات المادة الدراسية			
<b>Module Title</b>	Compute Programming		<b>Module Delivery</b>
<b>Module Type</b>	Core		<input checked="" type="checkbox"/> Theory
<b>Module Code</b>	NVEEELI214		<input checked="" type="checkbox"/> Lecture
<b>ECTS Credits</b>	5		<input checked="" type="checkbox"/> Lab
<b>SWL (hr/sem)</b>	150		<input type="checkbox"/> Tutorial
			<input type="checkbox"/> Practical
			<input type="checkbox"/> Seminar
<b>Module Level</b>	UGx11 2	<b>Semester of Delivery</b>	2
<b>Administering Department</b>	Dept. of Electronic Eng. (Med. Ele)	<b>College</b>	College of Electronic Engineering
<b>Module Leader</b>	Qais Thanon	<b>e-mail</b>	Qais.najim@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Porf.	<b>Module Leader's Qualification</b>	Ph. D.
<b>Module Tutor</b>	Name (if available)	<b>e-mail</b>	E-mail
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>	20/06/2023	<b>Version Number</b>	1.0

<b>Relation with other Modules</b> العلاقة مع المواد الدراسية الأخرى			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b> أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	<ol style="list-style-type: none"> <li>1. Learning about the algorithms types and how building the algorithms.</li> <li>2. Learning how to command computers to perform tasks using C++ language (Programming/coding).</li> <li>3. Become acquainted with the designed programming including sequencing, condition and iteration.</li> <li>4. Learn about the 1d and 2d arrays in C++ language.</li> <li>5. Learn about the functions in C++ language.</li> <li>6. Learn about the strings in C++ language.</li> </ol>
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	<ol style="list-style-type: none"> <li>8. Understanding the meaning of the algorithms in programming languages.</li> <li>9. Understanding the basics concepts of C language programming such as variables, data types, operators, control</li> <li>10. Understanding the utilities of each one of sequencing, condition, and loops, and basic input/output operations.</li> <li>11. Understanding how represent the data in 1d arrays and 2d arrays.</li> <li>12. Learn about how the strings represented in C language.</li> <li>13. Learn about divide any problem in sub-program and execute this problem by using function.</li> <li>14. In advance practical experience by working on programming exercises and projects.</li> </ol>
<b>Indicative Contents</b> المحتويات الإرشادية	<p style="text-align: right;">Indicative content includes the following.</p> <ul style="list-style-type: none"> <li>• Visualization via flowchart and Pseudocode [4 hrs]</li> <li>• Keywords, identifier, format specifier, and naming variables and constants [8 hrs]</li> <li>• Use standard libraries to take input and display output [8 hrs]</li> <li>• Operators in C++ programming [10 hrs]</li> <li>• Priorities in C++ programming [4 hrs]</li> <li>• Math functions [4 hrs]</li> <li>• Conditional operations [8 hrs]</li> <li>• Iterations (Loop operators) [10 hrs]</li> <li>• Arrays [10 hrs]</li> <li>• Functions [8 hours]</li> <li>• Review classes and problem solving [8 hrs]</li> </ul>

<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم			
<b>Strategies</b>	The main strategy being focused on is developing conceptual programming thinking, meanwhile refining and expanding their mathematical thinking skills. This will be achieved through classes, online lectures, interactive tutorials. Additionally, working on complex projects that challenge students' skills and require to apply advanced concepts. Such projects would help students exploring various aspects of C++ programming and gain hands-on experience in solving complex problems. some sampling activities that are interesting to the students.		
<b>Student Workload (SWL)</b> الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	77	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	5.1
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	73	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	4.8

<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>150</b>
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<b>Module Evaluation</b> تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	10% (10)	5 and 10	LO #1, #2 and #10, #11
	<b>Assignments</b>	2	10% (10)	2 and 12	LO #3, #4 and #6, #7
	<b>Projects / Lab.</b>	1	10% (10)	Continuous	All
	<b>Report</b>	1	10% (10)	13	LO #5, #8 and #10
<b>Summative assessment</b>	<b>Midterm Exam</b>	2hr	10% (10)	7	LO #1 - #7
	<b>Final Exam</b>	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
Week	Material Covered
<b>Week 1</b>	Introduction to computer languages and the structure of C program
<b>Week 2</b>	Flowchart and Pseudo-code
<b>Week 3</b>	Introduction to C++ programming: Declare variables and constants
<b>Week 4</b>	Take input and print output
<b>Week 5</b>	Assignment and Increment ,Decrement, Arithmetic, Logical, and Bitwise operators
<b>Week 6</b>	Standard math functions in math header <math.h>
<b>Week 7</b>	Priorities of operators in C++ programming
<b>Week 8</b>	Relational and conditional operators
<b>Week 9</b>	Mid-term Exam
<b>Week 10</b>	If statement versus switch case statement
<b>Week 11</b>	Examples of structured programming (sequencing and condition)
<b>Week 12</b>	Loop operators (For, while, do-while)
<b>Week 13</b>	Arrays
<b>Week 14</b>	Functions
<b>Week 15</b>	String of characters
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

## Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1-2	Learn the C++ language program compiler.
Week 3-4	Declare variables and constants and <iostream.h> including standard functions
Week 5-6	Arithmetic, logical, and bitwise operators
Week 7-8	Math header for math functions <math.h> and Assignment and increment & decrement operators
Week 9-10	Relational and conditional operators and Loop operators
Week 11-12	Examples about the Arrays
Week 13-14	Examples about Functions and string

## Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	C Programming Absolute Beginner's Guide, 3rd Edition 2014. BY: Greg Perry and Dean Miller.	Yes
<b>Recommended Texts</b>	C How to Program with an introduction to C++, 8 <sup>th</sup> Edition 2016. BY: Paul Deitel and Harvey Deitel. Global Edition contribution by Piyali Sengupta	No
<b>Websites</b>	1- <a href="https://www.programiz.com/c-programming">https://www.programiz.com/c-programming</a> 2- <a href="https://www.coursera.org/specializations/c-programming">https://www.coursera.org/specializations/c-programming</a>	

## Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
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	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Courses specification for Second class Industrial Engineering (Second Course)

Module Information			
Module Title	AC Machines		Module Delivery
Module Type	Support or related learning activity		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEEELI223		
ECTS Credits	6		
SWL (hr/sem)	175		
Module Level	2	Semester of Delivery	
Administering Department	Electronic Dept.	College	Electronics Collage
Module Leader		e-mail	
Module Leader's Acad. Title		Module Leader's Qualification	
Module Tutor		e-mail	
Peer Reviewer Name		e-mail	
Scientific Committee Approval Date		Version Number	

<b>Relation with other Modules</b>			
<b>Prerequisite module</b>	DC Machine	<b>Semester</b>	1
<b>Co-requisites module</b>	None	<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>	
<b>Module Aims</b>	<ul style="list-style-type: none"> <li>60. Understanding AC Machine Principles</li> <li>61. Analyzing AC Machine Behavior</li> <li>62. Control Strategies</li> <li>63. System Integration</li> <li>64. Practical Applications</li> <li>65. Problem-Solving Skills</li> <li>66. Laboratory Skills</li> <li>67. Teamwork and Communication</li> <li>68. Professional Development</li> </ul>
<b>Module Learning Outcomes</b>	<ul style="list-style-type: none"> <li>66. Understand how voltage is induced in a rotating loop</li> <li>67. Understand how curved pole faces contribute to a constant flux, and thus</li> <li>68. more constant output voltages.</li> <li>69. Understand how curved pole faces contribute to a constant flux, and thus more constant output voltages.</li> <li>70. Understand the power flow diagram for Ac machines</li> </ul>



	<p>71. Know the types of Ac machines in general use.</p> <p>72. Understand the equivalent circuit of a three phase induction motor.</p> <p>73. Understand how to derive the Torque speed characteristic of three phase induction motor.</p> <p>74. Understand how to control the speed of different types of AC motors.</p> <p>75. Understand the starting torque, condition for maximum torque, condition for maximum starting torque of the Ac motors.</p> <p>76. Understand the methods of starting AC motors safely.</p> <p>77. Understand the equivalent circuit of a AC generator.</p> <p>78. Understand of Single phase Induction motor . Construction , theories of operation, torque speed characteristic, Equivalent circuit.</p> <p>79. Understand how Test of single phase induction motor, no load test, blocked rotor test, power flow diagram, applications.</p> <p>80. Understand how Three phase synchronous generator, Construction, Equivalent circuit, applications.</p> <p>81. Understand how Single phase synchronous motors, Reluctance motor, Construction of reluctances motor, applications.</p> <p>82. Understand how Hysteresis motor, Construction of Hysteresis motor, application.</p> <p>83. Be able to explain how copper losses, leakage flux, hysteresis, and eddy currents are modeled in Ac machines circuits.</p>
<b>Indicative Contents</b>	<p>Introduction - The module further develops students' understanding of electrical machines by introducing the operational principles and characteristics of AC machines, three phase circuits and complex power. It introduces the principles, operation and design of common power electronic converter circuits.(12 hrs.)</p> <hr/> <p>Commutation and Armature Construction in Real Tree phase induction motor.(8 hrs.)</p> <hr/> <p>Introduction of The Equivalent Circuit of a Tree phase induction motor. (10 hrs.).</p> <hr/> <p>Power Flow and Losses in Tree phase induction motor. (6 hrs.)</p> <hr/> <p>Torque speed characteristic, starting torque, condition for maximum torque, condition for maximum starting torque.(12 hrs.)</p>

	Test of three phase induction motor, no load test, blocked rotor test, power flow diagram, applications.(12 hrs.)
	Mid-term Exam. .(3 hrs.).
	Single phase Induction motor.(4 hrs.).
	Introduction of Single phase Induction motor . Construction , theories of operation, torque speed characteristic, Equivalent circuit, (12 hrs.).
	Test of single phase induction motor, no load test, blocked rotor test, power flow diagram, applications.(12 hrs.).
	Three phase synchronous generator, Construction, Equivalent circuit, applications. (12 hrs.).
	Single phase synchronous motors, Reluctance motor, Construction of reluctance motor, applications.(10 hrs.).
	Hysteresis motor, Construction of Hysteresis motor , application .(9 hrs.).
	AC Commutator machine,Universal motor.(12 hrs.).

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	Visual Aids Problem-Solving Exercises Real-World Applications Group Projects Simulations and Virtual Labs Multimedia Resources Real-Life Examples

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>	74	<b>Structured SWL (h/w)</b>	5
<b>Unstructured SWL (h/sem)</b>	101	<b>Unstructured SWL (h/w)</b>	4.6 4
<b>Total SWL (h/sem)</b>	175		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	
	Assignments	2	10% (10)	2, 12	
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	13	
Summative assessment	Midterm Exam	2 hr	10% (10)	7	
	Final Exam	2hr	50% (50)	16	
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Introduction - The module further develops students' understanding of electrical machines by introducing the operational principles and characteristics of AC machines, three phase circuits and complex power.
Week 2	Commutation and Armature Construction in AC Machine.
Week 3	Introduction of The Equivalent Circuit of a Three phase induction motor.
Week 4	Power Flow and Losses in Three phase induction motor.
Week 5	Torque speed characteristic, starting torque, condition for maximum torque, condition for maximum starting torque in Three phase induction motor.
Week 6	Test of three phase induction motor, no load test, blocked rotor test. , <b>applications</b>
Week 7	Mid-term Exam.
Week 8	Introduction of Single phase Induction motor. Construction, theories of operation.
Week 9	Torque speed characteristic, Equivalent circuit, of single phase induction motor.
Week 10	power flow diagram of single phase induction motor & applications.
Week 11	Test of single phase induction motor, no load test, blocked rotor test of Single phase Induction motor.
Week 12	Three phase synchronous generator, Construction, Equivalent circuit, applications.
Week 13	Single phase synchronous motors, Reluctance motor, Construction of reluctance motor, applications.

<b>Week 14</b>	Hysteresis motor, Construction of Hysteresis motor , application
<b>Week 15</b>	AC Commutator machine, Universal motor
<b>Week 16</b>	Preparatory week before the final Exam

<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Electrical Machinery Fundamentals” edited by Stephen J. Chapman.	NO
<b>Recommended Texts</b>	electrical machines and transformer by: Ancieron and Macneil	NO
<b>Websites</b>	<a href="https://www.coursera.org">https://www.coursera.org</a>	

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required

**Note:**Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
Module Title	<b>Electronics II</b>		Module Delivery
Module Type	core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	<b>NVEEELI222</b>		
ECTS Credits	7		
SWL (hr/sem)	<b>175</b>		
Module Level	1	Semester of Delivery	
Administering Department	Electronics	College	Electronic Engineering college
Module Leader		e-mail	
Module Leader's Acad. Title	Assistant Prof.	Module Leader's Qualification	PhD

<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	Ahmad.younis@uoninevah.edu.iq
<b>Scientific Committee Approval Date</b>	12/06/2023	<b>Version Number</b>	1.0

<b>Relation with other Modules</b>			
<b>Prerequisite module</b>	NEEI2212	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>	
<b>Module Aims</b>	<p>69. To understand the basic analysis of bipolar transistor amplifier</p> <p>70. To be familiar with the dc and ac analysis of transistor amplifier</p> <p>71. To understand the dc and ac analysis of FET amplifier</p> <p>72. To illustrate and to understand the frequency response of amplifier</p> <p>73. To understand the basic concept of feedback concept</p> <p>74. To be able to deal with different feedback amplifier topologies</p> <p>75. To study the advantages of negative feedback on amplifier performance</p> <p>76. To be familiar with feedback amplifier ac analysis</p> <p>77. To understand the construction and ideal characteristic of operational amplifier</p> <p>78. To study and analyze op-amp equivalent circuit</p> <p>79. To be familiar with basic op-amp applications</p> <p>80. To start with studying power electronic devices</p>

<p><b>Module Learning Outcomes</b></p>	<p>84. Understand and apply the basic theory and operation of transistor amplifiers</p> <p>85. Define and explain the frequency response of bipolar transistor amplifier</p> <p>86. Understand the basic concept of negative feedback</p> <p>87. Understand and analyze the feedback amplifier</p> <p>88. Understanding the operation of ideal operational amplifier</p> <p>89. Dealing with dc and ac op-amp equivalent circuit</p> <p>90. Understanding the basic application of op-amp</p> <p>91. Power electronic devices principle overview</p>
<p><b>Indicative Contents</b></p>	<p><b>Transistor and FET amplifier analysis:</b> Small signal model analysis, low frequency and high frequency analysis, hybrid model, hybrid -Pi model analysis.</p> <p><b>Amplifier with negative feedback:</b> Basic concept, feedback analysis, feedback configurations, Feedback effects on gain , bandwidth, input and output resistances</p> <p><b>Operational amplifier:</b> Ideal Op-amp equivalent circuit; Operational Amplifier Specification; Circuit analysis of an Op-amp; Closed loop Op-amp Circuit ( Inverting and Non-Inverting Circuit ).</p> <p><b>Op-amp Applications:</b> Summation &amp; subtraction Circuit, Differential circuit Buffer circuit Ideal and practical Integrator circuits, ideal and practical Differentiator circuits, Examples.</p> <p><b>Power electronic devices:</b> UJT Construction, Operation and characterises; Thyristor Equivalent Circuit ; Thyristor Characteristics and operation ; Application of the devices.</p>

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<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>	74	<b>Structured SWL (h/w)</b>	3
<b>Unstructured SWL (h/sem)</b>	101	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	175		

<b>Module Evaluation</b>					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	6	10% (10)	γ, ο, 9,12,13,15	LO #1, 2, 10 and 11
	<b>Assignments</b>	6	10% (10)	γ, ο, 9,12,13,15	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	6	20% (20)	γ, ο, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	<b>Report</b>	0	0% (0)	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	1:30hr	20% (20)	10	LO # 1-4
	<b>Final Exam</b>	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>



	Material Covered
Week 1	Small signal model analysis
Week 2	Low and high frequency response of transistor amplifier
Week 3	Hybrid and hybrid-Pie equivalent circuit analysis
Week 4	Negative feedback concept and analysis
Week 5	Advantages of negative feedback on amplifier
Week 6	Amplifier feedback topologies
Week 7	Feedback effect on amplifier gain, bandwidth, and on input-output resistances
Week 8	operational amplifier construction and operation
Week 9	ideal and practical op-amp equivalent circuit
Week 10	Inverting and non inverting closed loop amplifier
Week 11	Integration and differentiation active circuits
Week 12	Summation and subtraction op-amp circuits
Week 13	UJT transistor construction
Week 14	Thyristor equivalent circuit and characteristics
Week 15	Subject review
Week 16	<b>Subject review</b>

### Delivery Plan (Weekly Lab. Syllabus)

	Material Covered
Week 1-15	<p><b>Practical experiments in transistor amplifier frequency response at lo and high frequency</b></p> <p><b>To measure the effect of feedback on amplifier performance</b></p> <p><b>To measure the performance of different op-amp circuits.</b></p>

### Learning and Teaching Resources

	Text	Available in the Library?
Required Texts	Electronic Devices By Millmann Electronic Devices By Floyd	yes
Recommended Texts	<b>SOLID STATE DIVICES", PHI; 4TH EDITION, 1995.By STREETMAN,</b> <b>SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992.By : M.S. TYAGI</b>	Yes

Websites	Electronic circuits	
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Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

### Module Information

معلومات المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Digital Design		Module Delivery
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE223		
ECTS Credits	3		
SWL (hr/sem)	60		
Module Level	2	Semester of Delivery	1
Administering Department	Electronic Eng. Dep.	College	Electronics Engineering
Module Leader	Amer Talal Ali	e-mail	
Module Leader's Acad. Title	Lecturer assistant	Module Leader's Qualification	
Module Tutor	Amer Talal Ali	e-mail	

<b>Peer Reviewer Name</b>	Name	e-mail	
<b>Scientific Committee Approval Date</b>	01/06/2023	<b>Version Number</b>	

<b>Relation with other Modules</b>			
العلاقة مع المواد الدراسية الأخرى			
<b>Prerequisite module</b>		<b>Semester</b>	
<b>Co-requisites module</b>		<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	30. To understand Advanced Minimization techniques for large number of bits to simplify the large designs. 31. Understand how to Design an Arithmetic and Logic unit. 32. Understand how to Design using programmable logic device. 33. To understand the sequential Logic Circuits. 34. To understand how to Design synchronous and asynchronous counters. 35. To understand the Design of Registers.
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	1. Using Advanced Minimization techniques for large number of bits to simplify the large designs. 2. Design an Arithmetic and Logic unit. 3. Design using programmable logic device. 4. Design sequential Logic Circuits synchronous and asynchronous. 5. Design Registers.

	6. Design synchronous and asynchronous counters.
<b>Indicative Contents</b> المحتويات الإرشادية	<p>Indicative content includes the following.</p> <p><u>Part A</u> – minimization techniques for large number of bits [14 hrs]</p> <p><u>Part B</u> – Initialization to design and Design an Arithmetic and Logic unit. [14 hrs]</p> <p><u>Part C</u> – Design using programmable logic device. [6 hrs]</p> <p><u>Part D</u> – sequential Logic Circuits. [18 hrs]</p>

<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم			
<b>Strategies</b>	The main strategy that will be adopted in delivering this module is to encourage students’ participation in the exercises, while at the same time refining and expanding their critical thinking and digital designing skills. This will be achieved through classes and interactive tutorials.		
<b>Student Workload (SWL)</b> الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	60	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	60	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	4
<b>Total SWL (h/sem)</b>	<b>120</b>		

## Module Evaluation

## تقييم المادة الدراسية

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	Assignments	1	10% (10)	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
Summative assessment	Midterm Exam	1.5 hr	10% (10)	10	LO #(1-8)
	Final Exam	2 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

## المنهاج الاسبوعي النظري

	Material Covered
Week 1	Introduction to Digital Design
Week 2	The 5-Variable Karnaugh Map; The 5-Variable Karnaugh Map with don't care conditions
Week 3	Map Entered variable Karnaugh Map
Week 4	ADDITIONAL MINIMAZATION TECNHHNIQUES: Tabular method; Quine-McCluskey
Week 5	Design using multiplexer: - Shannon Expansion
Week 6	top-down design of combainaal CIRCUITS: - Gate Level: Adders; Subtractor

<b>Week 7</b>	Design an Arithmetic and Logic unit
<b>Week 8</b>	memory and type of memories
<b>Week 9</b>	Design using programmable logic device (PLD): - PROM; PAL; PLA;
<b>Week 10</b>	sequential LOGIC: - Type of flip-flops; Timing Diagram; Basic concepts of counters; Binary counters; BCD counters; Up down counter
<b>Week 11</b>	sequential LOGIC: -Design of counters using state diagrams and tables;
<b>Week 12</b>	sequential LOGIC: -Mealy and Moore Circuits;
<b>Week 13</b>	synchronous CIRCUITS: Shift left and right register; Registers with parallel load; Serial –in arallel-out (SIPO) and parallel-in-serial-out (PISO).
<b>Week 14</b>	synchronous CIRCUITS: Shift Registers; Twisted Ring Counter; Maximum Length Shift Counter.
<b>Week 15</b>	Preparatory week before the final Exam

<b>Learning and Teaching Resources</b>		
مصادر التعلم والتدريس		
	Text	Available in the Library?
<b>Required Texts</b>	"Digital and analog communication" 2001 By L. W. Couch Sixth Edition	Yes
<b>Recommended Texts</b>	- Digital Communications Fifth Edition, 2008, John G. Proakis, and Masoud Salehi. Introduction to Communication Systems" 1992 By F. Strempler. -ELEMENTS OF INFORMATION THEORY" 2006 By THOMAS M. COVER and JOY A. THOMAS -Digital Communication, 2004 by Abbas Kattoush.	Yes
<b>Websites</b>		

## Grading Scheme

## مخطط الدرجات

Group	Grade	التقدير	Marks %	Definition
<b>Success Group</b> (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group</b> (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Module Information

<b>Module Title</b>	<u>Signals and Systems</u>		<b>Module Delivery</b>	
<b>Module Type</b>	<u>Core</u>		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	<u>NVEE210</u>			
<b>ECTS Credits</b>	<u>6</u>			
<b>SWL (hr/sem)</b>	<u>150</u>			
<b>Module Level</b>	UGx11 1	<b>Semester of Delivery</b>		4
<b>Administering Department</b>	Type Dept. Code	<b>College</b>	Type College Code	
<b>Module Leader</b>			<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>	Assistant Professor	<b>Module Leader's Qualification</b>		Ph.D.
<b>Module Tutor</b>	Name (if available)	<b>e-mail</b>	E-mail	
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail	
<b>Scientific Committee Approval Date</b>	25/06/2023	<b>Version Number</b>	1.0	



Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	<p style="text-align: right;">Student will be able to:</p> 36. identify systems concepts . 37. understand the properties of systems . 38. understand the mathematical relation between input and output of a system. 39. deal with Fourier and Laplace analysis of systems. 40. perform z-transform of discrete signals .
<b>Module Learning Outcomes</b>	62. Definition of the system concept. 63. Introduction of mathematical models. 64. Explain Continuous time systems. Discrete time systems. 65. Introduction of frequency response of systems. 66. Definition of filters. 67. Explain Ideal filters, Non ideal filters, and Butterworth filter design. 68. Define Z-transform of discrete signals. 69. Analyze of continuous system using Laplace Transform. System transfer function. 70. Definition of transfer function of a discrete system.
<b>Indicative Contents</b>	<p style="text-align: right;">Indicative content includes the following.</p> <p style="text-align: right;">Introduction to systems:</p> <ul style="list-style-type: none"> <li>- Definition and mathematical models.</li> <li>- Properties of systems.</li> </ul> <p style="text-align: right;">Transformation used with continuous systems</p> <ul style="list-style-type: none"> <li>- Fourier transforms.</li> <li>- Filters.</li> <li>- Laplace transform.</li> </ul> <p style="text-align: right;">Z-transform:</p> <ul style="list-style-type: none"> <li>- Introduction of z- transform of discrete time signal.</li> <li>- Z-transform used with discrete systems.</li> </ul> <p style="text-align: right;">Convolution used for</p> <ul style="list-style-type: none"> <li>- Continuous systems.</li> <li>- Discrete systems</li> </ul>

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### Learning and Teaching Strategies

<b>Strategies</b>	To make students interesting with both types of systems: continuous and discrete. Also with properties of systems and operations . To make them familiar with time and frequency domain and analysis of a system. Also to make them familiar with different types of transforms of systems. Also to make them have an experience with solving different problems and examples.
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### Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	62	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	88	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	<b>150</b>		

### Module Evaluation

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	6	10% (10)	, 0, 2 ,12,13,15 <sup>9</sup>	LO #1, 2, 10 and 11
	<b>Assignments</b>	6	10% (10)	, 0, 2 ,12,13,15 <sup>9</sup>	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	6	20% (20)	, 0, 2 ,12,13,15 <sup>9</sup>	LO # 3, 4, 6 and 7, 5, 8 and 10
	<b>Report</b>	0	0% (0)	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5hr	20% (20)	10	LO # 1-4
	<b>Final Exam</b>	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

Week	Material Covered
Week 1	Basic definitions. Mathematical models.
Week 2	Continuous time systems.
Week 3	Discrete time systems.
Week 4	System properties.
Weeks 5	Mathematical system representation in time domain: Convolution representation.
Week 6	Convolution properties.
Week 7	System description by linear constant coefficient differential equations.
Week 8	Frequency domain analysis of continuous system.
Week 9	Frequency response of a system.
Week 10	Frequency response of electrical circuits.
Week 11	Filters. Distortion less transmission.
Week 12	Ideal filters. Non ideal filters. Butterworth filter design.
Week 13	Analysis of continuous system using Laplace Transform.
Week 14	System transfer function.
Week 15	Analysis of discrete system using z-Transform. System transfer function.

## Learning and Teaching Resources

	Text	Available in the Library?
Required Texts	Signals and Systems. Simon S. Haykin	Yes
Recommended Texts	Signals and linear Systems. G. E. Carlson	

## Grading Scheme

Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded

	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				

Module Information			
<b>Module Title</b>	Engineering Analysis II		<b>Module Delivery</b>
<b>Module Type</b>	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
<b>Module Code</b>	NVEE209		
<b>ECTS Credits</b>	3		
<b>SWL (hr/sem)</b>	75		
<b>Module Level</b>	2	<b>Semester of Delivery</b>	2

<b>Administering Department</b>	Electronics dept	<b>College</b>	Electronics engineering college
<b>Module Leader</b>	Dr. Omar B Mohammed	<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Lecturer	<b>Module Leader's Qualification</b>	Ph.D.
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>		<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	

<b>Relation with other Modules</b>			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>
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<b>Module Aims</b>	This course covers the following topics: ordinary differential equations, sequences and series, solution of differential equations by power series, and matrix analysis. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	Upon successful completion, students will: 19. Improve their problem-solving skills. 20. Apply that knowledge toward practical problems in different areas of science. 21. Utilize the computer capabilities to solve such problems using proper methods. 22. Learn how to represent any function as a power series, then use computer to solve it. 23. Learn the importance of differential equations for modeling almost any system, and how to solve it to find the properties of that system. 24. Learn the linear algebra and its importance in science.
<b>Indicative Contents</b>	Ordinary Differential Equations. Sequences and Series. Solution of Differential Equations by Power Series. Matrix Analysis.

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>		<b>Structured SWL (h/w)</b>	
<b>Unstructured SWL (h/sem)</b>		<b>Unstructured SWL (h/w)</b>	

<b>Total SWL (h/sem)</b>				
<b>Module Evaluation</b>				
		<b>Time/Number</b>	<b>Weight (Marks)</b>	<b>Week Due</b>
<b>Formative assessment</b>	<b>Quizzes</b>			
	<b>Assignments</b>			
	<b>Projects / Lab.</b>			
	<b>Report</b>			
<b>Summative assessment</b>	<b>Midterm Exam</b>			
	<b>Final Exam</b>			
<b>Total assessment</b>				

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Ordinary Differential Equations: 4. First order (variables separable, homogeneous, linear and exact). 5. Second order homogeneous. 6. Second order nonhomogeneous; indeterminate coefficients, variation of parameters.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	
<b>Week 5</b>	Infinite Sequences and Series: 4. Limit laws, indeterminate forms and L'hospital rule. 5. Infinite series; convergence test. 6. Power series; Taylor and Maclaurin series.
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	Solution of Differential Equations by Power Series: Power series method, Legendre's equation; Legendre's polynomials.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	Matrix Analysis: 5. Review of matrix theory, solving system of equations; Cramer's rule, inverse of the matrix method, Gauss elimination. 6. Eigen values and eigen vectors. 7. Diagonalization of matrices 8. Application of matrices to electric circuits.
<b>Week 14</b>	
<b>Week 15</b>	

<b>Week 16</b>	Preparatory week before the final Exam
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<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney & Thomas	Yes
<b>Websites</b>		

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

### Courses Table For Third Class



Electronic Engineering Departement								
Undergraduate Third Class								
Code	Subject	Hours/Week						Units
		First Term			Second Term			
		Th	Pr.	Tut	Th	Pr	Tut	
EE3301	Electronic – II	2	-	1	2	-	1	4
EE3201	Digital Signal Processing	2	-	1	2	-	1	£
EE3302	Control engineering	3	-	1	3	-	1	6
EE3303	Microprocessor	2	-	1	2	-	1	4
EE3304A	Digital System Design I	2	-	1				2
EE3304B	Digital System Design II				2	-	1	2
EE3305	Communication	2	-	1	2	-	1	4
EE3306	Electronic Instrumentation	2	-	1	2	-	1	4
EE3307	Laboratory	-	6	-	-	6	-	4
Total		15	6	7	15	6	7	34
		28			28			

**Total Theoretical : 15 Hour/Week**

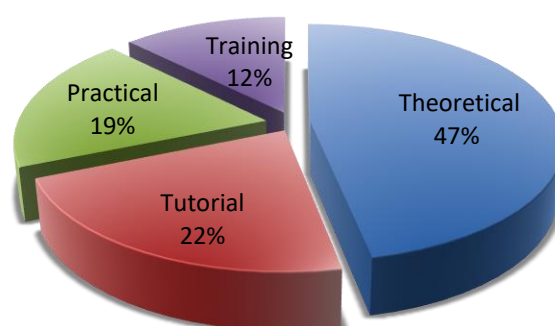
**Total Practical :6 Hour/Week**

**Total Summer Training 4 Hour/Week**

**Total Tutorial :7 Hour/Week**

**Total Units :34**

**Weekly classes categories for the department**



## Third Year

- **Course Number: EE3301**
- **Course Name: Electronics II**
- **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**

**Course Content:** The electronics course covers the area of analog electronic circuit design: Non linear application of op-amp, Filter design theory and approximation, Active filter design, Waveform generator theory and classification, Power amplifier classification.

- **Course Number: EE3201**
- **Course Name: Digital signal processing**
- **Credit Hours: (4,3,0,0) ( Units, Theory, Tutorial, Practical)**

**Course Content:** This course covers the following topics: review of discrete signals and systems, discrete fourier series, discrete fourier transform, convolution and correlation, discrete and fast fourier transform, z- transform, framework for digital filter design, realization of digital filter, finite impulse response digital filter design, infinite impulse response digital filter design

- **Course Number: EE3302**
  - **Course Name: Control Eng.**
  - **Credit Hours: (6,3,0,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:**This course covers the following topics: i-continuous control system (System representation, Time domain analysis, State space analysis, Stability of system, Frequency response analysis, Design of control system) II-DIGITAL CONTROL SYSTEM (Z-transform, Sampled data control system, Time response analysis, Stability of system).
- **Course Number: EE3303**
  - **Course Name: Microprocessor I**
  - **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:**This course covers the following, Studying the 8086 microprocessor from software point of view, Studying the 8086 microprocessor from hardware point of view.
- **Course Number: EE3304A**
  - **Course Name: Digital system design I**
  - **Credit Hours: (2,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** This course covers the following topics: Standard buses, programmable logic devices, PLC. Inductive proximity sensors: ultrasonic, Optical , Basic Elements of PLC, PLC inputs and outputs interfaces, Ladder programming, PLC Instructions: Latching, Comparisons, Timers, Counters, Sequencers, Shift Registers
- **Course Number: EE3304B**
  - **Course Name: Digital system design II**
  - **Credit Hours: (2,2,1,0) ( Units, Theory, Tutorial, Practical)**
  - **Course Content:**Programmable Logic Devices, GAL, SPLD, CPLD, OLMC, ISP, FPGA
  - Introduction to VHDL, Modeling flip-flops using VHDL process, Compilation and simulation of VHDL code, Modeling a sequential machine, Variables, Signals and Constants, Arrays
- **Course Number: EE3305**
  - **Course Name: Communication**
  - **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:**the course cover transmission lines, Analog input analog output schemes, Digital input analog output schemes, analog input digital output schemes, Digital input Digital output schemes.
- **Course Number: EE3306**
  - **Course Name: Electronic Instrumentation**
  - **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** This course covers the following topics: instrumentation errors, transducers, signal conditioning, signal conversion, instrumentation amplifier, analog electronic instruments, digital instruments, and interface buses.

- **Course Number: EE3307**
- **Course Name: Laboratory**
- **Credit Hours: (4,0,0,6) ( Units, Theory, Tutorial, Practical)**

**Course Content:** The principle objective is to ensure that the student have the ability to integrate concepts and achieve the practical works for the different topics he attend in the theoretical classes. Each student should submit a written technical report for each experiment.

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<b>Class</b>	<b>Third</b>			<b>Theory :</b>	<b>2Hrs/wk</b>
<b>Subject</b>	<b>Electronic II</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE3301</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>OP-AMP Applications:</b> inverting, non-inverting amplifier, buffer, summing amplifier, difference amplifier, integrator and differentiator , comparator, sample and hold, zero crossing detector, peak detector, precision diode and fast rectifier, analog computation.	<b>12</b>
<b>Filters:</b> Filter approximations, passive RLC design, active filter design methods (ladder, and cascaded design technique).	<b>9</b>
<b>Oscillators :</b> Barkhausen’s criteria for oscillators ; Satiability concept Three pole amplifier ; Nyquist criteria ; Stabilizing networks ; frequency compensation and sinusoidal oscillator ; phase shift , Wien bridge , Colpitts , Hartley , Crystal and Tune circuit type oscillator (AF &RF Range ).	<b>9</b>
<b>Tuned Amplifier:</b> Introduction to single tuned amplifier ; G.B. response calculations & design ; Cascade amplifier ; Neutralization methods ; Synchronously tuned amplifier ; Elementary treatment of stagger tuned and doubly tuned amplifiers .	<b>9</b>
<b>Audio Frequency Linear Power Amplifiers:</b> Introduction to Class A, B, AB, a ,C operation , Class A – common –emitter power amplifier ; Transformer coupled amplifier ; Class push –pull power amplifier ; Amplifiers using complementary symmetry ; Class C amplifier .	<b>12</b>
<b>Comparators and Converters :</b> Zero crossing detector , Schmitt trigger , Comparator ,Voltage limiters and window detector , Clippers and clampers , Peak detector , introduction to A / D and D /A converters and sample and hold circuit .	<b>9</b>
<b>Multivibrators:</b> Astable, monostable, 555 timer, and bistable	<b>12</b>
<b>Integrated Circuits and Devices :</b> Introduction of IC families ; Fabrication Steps and evolving transistor , Diode and Resistor ; capacitors families.	<b>9</b>
<b>Specialized IC Applications :</b> phase locked loops , ICL 8038 function generator , Voltage Controlled Oscillator , XR 2240 programmable timer / counter .	<b>9</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
1: Integrated electronics by Milmann
2: Microelectronics by Milma

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<b>Class</b>	<b>Third</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Digital Signal Processing</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE3201</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Review of Discrete Signals and Systems</b>	<b>6</b>
<b>Discrete Fourier Series:</b> Spectra of periodic digital signals· Properties of series.	<b>9</b>
<b>Discrete Fourier Transform:</b> Properties· Frequency response of LTI systems.	<b>9</b>
<b>Convolution and Correlation</b>	<b>6</b>
<b>DISCRETE AND FAST FOURIER TRANSFORM</b>	<b>9</b>
<b>Z- Transform:</b> Review· Z-plane poles and zeros.	<b>9</b>
<b>Framework for Digital Filter Design</b>	<b>6</b>
<b>Finite Impulse Response Digital Filter Design:</b> window method· frequency sampling method· realization of FIR.	<b>12</b>
<b>Infinite Impulse Response Digital Filter Design:</b> Pole-zero method· Bilinear Z-transform· Realization of IIR.	<b>12</b>
<b>Applications of Filter Banks in DSP</b>	<b>12</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
1: " Digital Signal Processing", By Emmanuel and Barrie
2: "Digital Signal Processing with Computer Applications", John Wiley & Sons , 1997 By PAUL A. LYNN



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<b>Class</b>	<b>Third</b>			<b>Theory :</b>	<b>3 Hrs/wk</b>
<b>Subject</b>	<b>Control Engineering</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE3201</b>	<b>Unit</b>	<b>6</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Introduction And Basic Definition:</b> Closed Loop And Open Loop, Control Systems	3
<b>Transfer Function:</b> Electrical System; Mechanical System; Servo System.	6
<b>Block Diagram:</b> Block Diagram Reduction Algebra.	3
<b>Signal Flow Graph:</b> Mason Gain Rule.	6
<b>Time Response:</b> Typical Test Signals & Types Of System; Steady State Errors; Transient Response of 1st and 2 <sup>nd</sup> Order System.	9
<b>Stability Of Control System:</b> Routh-Hurwitz Criterion:	3
<b>Root Locus Analysis:</b> Root Locus Plot; General Rules Of Constructing Root Loci; Root Locus Analysis Of Control Systems	9
<b>Frequency Response:</b> Introduction To Frequency Response.	3
<b>Bode Plot:</b> Bode Analysis; Rules For Sketching The Bode Plots; Phase And Gain Margines; Relative Stability.	9
<b>Control System Design By Frequency Response:</b> Proportional Gain Only; Lead Compensation; Lag Compensation.	12
<b>The PID Controller; Definition; Tuning By Ziegler-Nicholes Methods.</b>	6
<b>Digital Control Systems :</b> Z- Trans Form & Inverse Z-Transfrom; Pulse Transfer Function ; Open Loop And Closed Loop Responses Of Discrete-Time Systems; Discretization Methods; Stability Test For Digital Control System (Jury's Test).	15
<b>State-Space Analyses:</b> State Equation; Solution Of State Equation; Controllability and Observability.	6
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: "AUTOMATIC CONTROL SYSTEM" By B. KUO 2001</b>
<b>2: "MODERN CONTROL SYSTEM" By K. OGATA 2001</b>

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<b>Class</b>	<b>Third</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Microprocessors I</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE3303</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Introduction to 16bit Microprocessor:</b> 8086 /8088 Architecture , Machine language, instruction , Internal execution and timing .	<b>6</b>
<b>8086/8088 Family Assembly Language Programming:</b> Data Transfer instructions ;Arithmetic instructions, logical, Shift and rotate instructions ; Branch instructions ; Loop instruction;NOP ; HLT and flag manipulation instructions; Assembler directives.	<b>12</b>
<b>8086 System Connections and Timing :</b> 8086 Hardware overview ; Basic Signal flow on 8086 buses ;Analyzing a minimum mode system; 8086 addressing and address decoding ; 8086 timing parameter .	<b>12</b>
<b>Interrupts and Interrupt Service Procedure :</b> 8086 interrupts and interrupt response ; 8086 interrupt types ; Hardware and software consideration for using interrupt .	<b>12</b>
<b>I/O Programming :</b> Fundamentals I/O consideration ;Programmed and interrupt I/O ; Block transfers and DMA ,I/O design example .	<b>12</b>
<b>Interfacing :</b> Programmable Parallel ports and handshake input/ output ; Interfacing microprocessors to keyboard and display ; D/A converter operation ; Interfacing and applications ;A/D converter ; Specifications and interfacing ; Serial communication interfaces .	<b>12</b>
<b>Parallel I/O and Interfacing Application :</b> Basic interfacing concepts 8255 Program Peripheral Interface ; Interfacing displays ; Keyboards; , 8279 Programmable keyboard interface; , interfacing memory ; Memory ; Mapped I/O .	<b>12</b>
<b>General Purpose Programmable Peripheral Devices:</b> 8253 Programmable Timer 8257 controller , 8259 interrupt controller .	<b>12</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: "The Intel Microprocessor"By BARRY B. BREY,</b>
<b>2: "The 8088 &amp; 8086 mp`s programming , interfacing S/W, H/W &amp; applications", PrenticeHall, 2003ByW. A. Triebel&amp; A. Singh</b>



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<b>Class</b>	<b>Third</b>	<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Digital System Design I</b>	<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE3304A</b>	<b>Unit</b>	<b>2</b>
		<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
Programmable Logic Controller PLC	3
Basic Components & Their symbols	3
Control Transformer switches , relays , time delay relays	1
References Designators: on ,off ,Run ,stop, cycle	3
Inductive proximity sensors: ultrasouic, Optical	1
Analog Input / output	3
Basic Elements of PLC	3
PLC inputs and outputs interfaces	3
Ladder programming	6
PLC Instructions: Latching, Comparisons,	6
Timers, Counters,	6
Sequencers, Shift Registers	2
Math Instructions: ADD, SUB, MUL, DIV, CLV, CLR, SQR	2
Move & Logic Instructions: MOV, MVM, AND, OR, NOR, NOT,CLR	2
Standard Buses	3
Internal, External buses, Serial, Parallel buses	3
	<b>Total</b>
	<b>45</b>

Text book:
1: Digital Fundamental, Floyd
2: PLC Software Manual

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<b>Class</b>	<b>Third</b>	<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Digital System Design II</b>		<b>Tutorial</b>
<b>Code</b>	<b>EE3304B</b>	<b>Unit</b>	<b>2</b>
		<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
Programmable Logic Devices GAL, SPLD, CPLD, OLMC, ISP, FPGA	9
V-Hardware Description Language (VHDL)	3
Introduction to VHDL	3
VHDL description of combinational network	3
Modeling flip-flops using VHDL process	3
Compilation and simulation of VHDL code	3
Modeling a sequential machine	3
Variables, Signals and Constants	3
Arrays	3
VHDL operators, functions, procedures Packages and Libraries	3
Memory Expansion, RAM, ROM	3
System Projects	6
<b>Total</b>	<b>45</b>

Text book:
1: Digital Fundamental, Floyd
3: Digital System Design using VHDL By Charles H

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<b>Class</b>	<b>Third</b>	<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Communication</b>	<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE3305</b>	<b>Unit</b>	<b>4</b>
		<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Transmission lines:</b> Equivalent circuit, characteristic impedance, phase velocity, reflection coefficient, standing waves, quarter – wave transformer, smith chart calculation and stub matching.	<b>15</b>
<b>Analog Input Analog Output Schemes:</b> Amplitude Modulation ; Equation for AM, modulation index, spectrum of AM, DSB transmission with and without carriers, VSB transmission, DSB,C amplitude modulators, Envelope detectors, Balanced Modulator, SSB signal generation and Demodulation schemes.	<b>15</b>
<b>Frequency modulation:</b> Equations for FM, modulation Index, spectrum calculation for sinusoidal waveform and Bessels function table, phase modulation, relationship between FM and PM, NBFM, frequency modulators ( Armstrong method) Types of noise in AM and FM systems.	<b>15</b>
<b>Digital Input Analog Output Schemes :</b> ASK, FSK, QAM, BPSK, QPSK, Transmitter and receiver block diagrams.	<b>15</b>
<b>Analog Input Digital Output Schemes :</b> Various pulse modulation methods, pulse code modulation PCM, Delta modulation DM. Comparison between PCM and DM, Compounding method, Noise in digital systems.	<b>15</b>
<b>Digital Input Digital Output Schemes :</b> Line encoding methods : NRZ, RZ, Manchester, and multilevel encoding methods and comparison of these schemes	<b>15</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: principle of communication engineering by Anokh Singh</b>

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Class	Third	Theory :	2 Hrs/wk
Subject	Electronic Instrumentation	Tutorial	1 Hrs/wk
Code	EE3306	Unit	4
		Practical	Hrs/wk

Article	Hrs
<b>INSTRUMENTATION ERRORS</b>	6
<b>TRANSDUCERS:</b> Resistive, Capacitive, Inductive. Active Transducers.	9
<b>SIGNAL CONDITIONING:</b> Input signal modification, scaling of measuring variables, delay lines, noise, signal averaging, interference, grounding, shielding, signal filtering, signal correlation, current-mode amplifier.	12
<b>SIGNAL CONVERSION:</b> Conversion by transducer bridge, electronic multipliers, signal generator, a.c to d.c signal conversion, logic elements, sample & hold, A/D and D/A signal conversion, isolation amplifier	12
<b>INSTRUMENTATION AMPLIFIER:</b> Circuit design, characteristics, CMMR	9
<b>ANALOG ELECTRONIC INSTRUMENTS:</b> Analog (voltmeter, multi-meter, vector impedance meter, frequency meter, distortion analyzer, spectrum analyzer.	15
<b>DIGITAL INSTRUMENTS:</b> Digital indicator, voltmeter (dual slop, multi-slop, successive approximation, and voltage to frequency converter, ammeters, ohmmeters, multi-meters, counters (frequency, frequency ratio meter, time-interval meter, energy meter), digital multiplexers, microprocessor-based meters	15
<b>INTERFACE BUSES:</b> Parallel port, RS-232, GPIB.	12
<b>Total</b>	<b>90</b>

<b>Text book:</b>
1: "Electronic Instrumentation and Measurement Techniques" By William David Cooper and Albert D. Helfrick.
2: Principles of Measurement systems By John P. Bentley
3: Electrical and Electronic Measurement By Ahmed A. Montaser and Karam A. sharshar

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<b>Class</b>	<b>Third</b>	<b>Theory :</b>	<b>Hrs/wk</b>
<b>Subject</b>	<b>Laboratory</b>	<b>Tutorial</b>	<b>Hrs/wk</b>
<b>Code</b>	<b>EE2307</b>	<b>Unit</b>	<b>4</b>
		<b>Practical</b>	<b>6Hrs/wk</b>

Article	Hrs
<p>The principal objective is to ensure that students have a good quality capstone design &amp; experience to integrate concepts from a range of classes in the core. The students are to apply modern engineering practices and techniques. Each student should submit a written technical report for each experiment. The experiments cover the related topics in electronic circuit analysis, communication system and microprocessor technology.</p>	
<b>Total</b>	<b>180</b>

<b>Text book:</b>
<b>1:</b>
<b>2:</b>
<b>3:</b>

## Courses Table for Fourth Class

Electronic Engineering Departement								
Undergraduate Fourth Class								
Code	Subject	Hours/Week						Units
		First Term			Second Term			
		Th	Pr.	Tut	Th	Pr	Tut	
EE4301	Industrial Electronic	2	-	1	2	-	1	4
EE4302	Data Transmission&ComputerNetwork	2	-	1	2	-	1	4
EE4303	MicroController(*)	2	-	1				2
EE4309	Microprocessor II(*)				2	-	1	2
EE4304	Microelectronics	2	-	-	2	-	-	4
EE4308	Antenna & Propagation(*)				2	-	1	2
EE4305	Radiation(*)	2	-	1				2
EE4306	Computer aided design	2	-	1	2	-	1	4
EE4201	Engineering Project	1	3	-	1	3	-	4
EE4307	Laboratory	-	6	-	-	6	-	4
Total		13	9	5	13	9	5	32
		27			27			

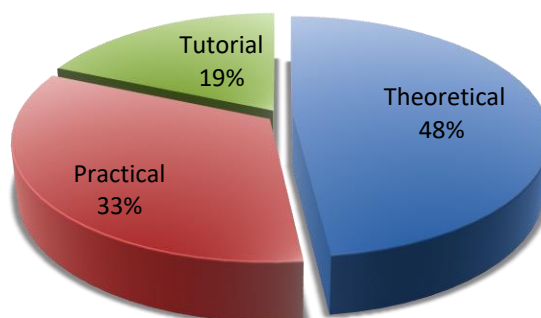
Theoretical : 13 Hour/Week

Total Practical : 9 Hour/Week

Total Tutorial : 5 Hour/Week

Total Units : 32

### Weekly classes categories for the department





## Fourth Year

- **Course Number: EE4301**
  - **Course Name: Industrial Electronic**
  - **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** This course cover the power semiconductor devices, Phase control converters, Thyristor commutation techniques, Inverters, PWM and speed control.
- **Course Number: EE4302**
  - **Course Name: Data Transmission and Computer Networks**
  - **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** Definitions and standards, Transmission media, OSI and TCP/IP models, Connecting devices. Data link control and data link protocols, LAN technologies, WLAN standards and devices, WAN and Wireless WAN.
- **Course Number: EE4309**
  - **Course Name: Microprocessor II**
  - **Credit Hours: (2,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** Introduction to Advanced Microprocessors, The 80386 and 80486 Microprocessor, Assembly language and Programming,
- **Course Number: EE4303**
  - **Course Name: Microcontroller**
  - **Credit Hours: (2,2,1,0) (Units, Theory, Tutorial, Practical)**
- Course Content:** Microprocessors and Microcontrollers, The MCS-51 Architecture, Basic Assembly Language Programming Concept, An MCS-51 Microcontroller Design
- **Course Number: EE4304**
  - **Course Name: Microelectronics**
  - **Credit Hours: (6,3,0,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** The microelectronics course covers the area of integrated circuit design. The fabrication of electronic devices, and design and analysis of analog and digital integrated circuits.
- **Course Number: EE4305**
  - **Course Name: Radiation**
  - **Credit Hours: (2,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** Give the students an overview of microwave technology and introduction to Microwave devices.



- **Course Number: EE4308**
- **Course Name: Antenna and Propagation**
- **Credit Hours: (2,2,1,0) ( Units, Theory, Tutorial, Practical)**
- **Course Content:**Antenna Theory ( Principles of radiation and equivalent circuit)  
, Dipole antenna, Array antenna, Reflector Antenna (Parabolic antenna), Ground wave propagation (Direct and Reflected), Ionspheric Propagation , Radar theory (Circuits and equations)
  
- **Course Number: EE4306**
- **Course Name: Computer aided design**
- **Credit Hours: (4,2,1,0) ( Units, Theory, Tutorial, Practical)**
- Course Content:** This course covers the following topics: Numerical solution for Linear and nonlinear circuit ,DC and AC matrix analysis ,two port analysis ,graph theory , Simulation ,State variable analysis, Sensitivity, Optimization, CAD for integrated circuits, Genetic Algorithm .
  
- **Course Number: EE4201**
- **Course Name: Engineering Project**
- **Credit Hours: (4,1,0,3) ( Units, Theory, Tutorial, Practical)**
- Course Content:** Collaboration team work in research environment is expected including extensive interaction with other students. Each group should submit a written report and should attend the final oral examination.
  
- **Course Number: EE4307**
- **Course Name: Laboratory**
- **Credit Hours: (4,0,0,6) ( Units, Theory, Tutorial, Practical)**
- Course Content:** The principle objective is to ensure that the student have the ability to integrate concepts and achieve the practical works for the different topics he attend in the theoretical classes. Each student should submit a written technical report for each experiment.

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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>2Hrs/wk</b>
<b>Subject</b>	<b>Data Transmission &amp; Computer Networks</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4302</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Introduction and Definitions:</b> Data Communication, Networks, Protocols, Standards, and Standard organizations.	<b>3</b>
<b>BASIC CONCEPTS:-</b> Line configuration, Topology, Categories of networks.	<b>6</b>
<b>Transmission Media:</b> Electromagnetic spectrum., Guided media: Unshielded Twisted Pair (UTP) Cable., Shielded Twisted Pair (STP) Cable., Coaxial Cable., Optical Fiber., Unguided media: Radio Transmission., Microwave Transmission., Satellite Microwave.	<b>9</b>
<b>Interfaces and Modems:</b> Data transmission: parallel, serial, synchronous and asynchronous., DTE-DCE interface and standards., Modems.	<b>6</b>
<b>The OSI and TCP/IP Models</b>	<b>6</b>
<b>Networking and Internetworking Devices:</b> Networking devices: NICs, Hubs, Repeaters, Bridges and Switches., Internetworking devices: Router and Gateways.	<b>6</b>
<b>Data Link Control:</b> Link Discipline, Flow control, Error control.	<b>6</b>
<b>Data Link Protocols:</b> Asynchronous protocols, Synchronous protocols.	<b>3</b>
<b>Local area Network (LAN):</b> Ethernet, Token Bus, project 802, Token Ring, FDDI.	<b>12</b>
<b>TCP/IP Model and Protocols</b>	<b>9</b>
<b>Wireless LAN (WLAN):</b> Introduction and history of (WLANs), Standardization and frequency bands, IEEE 802.11 standard, WIFI, WIMAX, Bluetooth.	<b>9</b>
<b>Wide Area Network (WAN)</b>	<b>6</b>
<b>Wireless WAN</b>	<b>6</b>
<b>Internet Working and Internet</b>	<b>3</b>
<b>Total</b>	<b>90</b>

**Text book:**

1: "Introduction to Data Comm. And Networking", By Pehrouz Forouzan.
2: "Computer Networks and Internets", Douglas E. Comer (4 <sup>th</sup> edition)

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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Industrial Electronic</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4301</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Introduction :</b> Scope of power electronics , power converter specification . <b>Power Semiconductor Devices :</b> Thyristor families , V-I characteristics of SCR , Triac , GTO , Diac , Source of thyristor triggering , turn On \ turn Off characteristic and Gate triggering requirements, series/parallel operation, device ratings.	<b>12</b>
<b>Power Transistor devices:</b> Basic structure and V-I characteristics of power MOSFET,IGBT,SIT. Switching characteristic, Gate/Base drive circuits, Safe operating area, di/dt / dv/dt limitation, series/parallel operation, ratings.	<b>12</b>
<b>Phase Control Converters:</b> Signal phase central taped transformer connection , half controlled and fully controlled Bridge configuration , three phase half controlled Bridge converters , Use of flywheeling diode operation with resistive , inductive and Back EMF load , line commutated inverter , effect of source inductance on converter performance , power factor , ripple factor calculation , firing scheme , linear alpha and cosine angle control , application of D.C motor speed control , regulated power supply , battery charger	<b>18</b>
<b>Thyristor Commutation Techniques:</b> Natural commutation , Force commutation , Voltage / Current commutation , DC chopper , Principle of Voltage control , analysis of Morgan chopper circuit , Johns chopper circuit, regenerative chopper circuit .	<b>15</b>
<b>Inverters :</b> Single phase series and parallel inverters , classification of CSI and VSI inverters , single phase and three phase inverter circuit , methods of voltage controlled inverter circuits , comparison of thyristor and transistor, based inverters, application to speed control of AC motors , uninterrupted power supply , induction melting , heating furnaces .	<b>15</b>
<b>Industrial Applications:</b> DC Motor Control, Induction Motor Control, Pulse width Modulation & Speed Control, Static Relays & Contactors.	<b>12</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
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<b>Class</b>	<b>Fourth</b>	<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Microprocessor II</b>	<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4309</b>	<b>Unit</b>	<b>2</b>
		<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<ul style="list-style-type: none"> <li>• <b>Introduction to Advanced Microprocessors :</b> Overview of 80186,80286 , 80386 , 80486 Architecture , Descriptor table , Privilege levels , paging . Detail study of Pentium , Pentium MMX architecture , Pentium II , Memory and Microprocessor , The Programming Model , Real mode and protected mode Memory addressing , Data formats .</li> </ul>	<b>12</b>
<ul style="list-style-type: none"> <li>• <b>The 80386 and 80486 Microprocessor :</b> Architecture – Real mode and Protected mode , 80386 Memory Management , Memory segmentation , Memory paying Mechanism , On chip cache organization .</li> </ul>	<b>12</b>
<b>Assembly language and Programming concepts :</b> The instruction set , Addressing modes , Data movement instructions , Arithmetic and logic instructions , programming the Microprocessor .	<b>12</b>
<ul style="list-style-type: none"> <li>• <b>Interfacing and Applications :</b> Memory interfacing , Basic I/O interfacing .</li> </ul>	<b>9</b>
	<b>Total</b>
	<b>45</b>

<b>Text book:</b>
<b>1: “The 80386-80486 and Pentium processor” By Walter A. Tribel;</b>
<b>2: “The Intel Microprocessors “ By Barry B. Bery</b>
<b>3: “The 8051 micro-controller” By I. Scott Mackenzie.</b>

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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Microcontroller</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4303</b>	<b>Unit</b>	<b>2</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Microprocessors and Microcontrollers</b> : Comparing Microprocessors and Microcontrollers , The Z80 and MCS 51 , Microcontroller survey .	<b>6</b>
<b>Microprocessor &amp; Micro Controller</b> :Comparing Microprocessors and Microcontrollers, , Micro Controller survey.	<b>9</b>
<b>The MCS-51Architecture</b> :Introduction, MCS-51 family microcontrollers hardware, Input/output pin, ports and circuits, External memory interfacing, counter, timer, serial data input/output, Interrupts.	<b>9</b>
<b>Basic Assembly Language Programming Concept</b> : Addressing mode, External data, move, Code memory read – only data moves, Push and Pop opcodes, Data Exchanges, Logical operations, Arithmetic operations, Branching Instructions, Interrupts and Returns.	<b>12</b>
<b>An MCS-51 Microcontroller Design</b> :Microcontroller Specification, External memory and Memory space Decoding, Expanding I/O , Memory map I/O , Memory address decoding, Testing the Design, Lookup table for the 8051, Serial data Transmission.	<b>9</b>
<b>Total</b>	<b>45</b>

<b>Text book:</b>
<b>1: “The 80386-80486 and Pentium processor” By Walter A. Tribel;</b>
<b>2: “The Intel Microprocessors “ By Barry B. Bery</b>
<b>3: “The 8051 micro-controller” By I. Scott Mackenzie.</b>

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<b>Class</b>	<b>Fourth</b>	<b>Theory :</b>	<b>3 Hrs/wk</b>
<b>Subject</b>	<b>Microelectronics</b>	<b>Tutorial</b>	<b>Hrs/wk</b>
<b>Code</b>	<b>EE4304</b>	<b>Unit</b>	<b>6</b>
		<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Semiconductor Fundamental</b> :Energy band model of solid, intrinsic and extrinsic semiconductor, free carrier density in semiconductor, carrier concentration and Fermi level. Carrier transport and recombination, carrier diffusion, the drift current, Hall effect. MIS, MOS, Schottky barriers, magnetic effect, bipolar junction, p-n junction, FET.	<b>6</b>
<b>IC fabrication processes</b> :Crystal growth, diffusion, doping, evaporations, and photo masking, Ion implementation, Thin and thick film fabrication, sputtering, mounting, package, and hybrid integrated circuits.	<b>12</b>
<b>LSI and VLSI Design and Application</b> :Discrete device design, bipolar transistor fundamental, technology, and miniaturization. Linear I.C's: fabrication, and general consideration. Current sources,. LSI oriented bipolar technology. Logic Families based on bipolar transistor (RTL, DTL, TTL, ECL, TRL, I <sup>2</sup> L). TTL gate circuit analysis. Metal-Semiconductor junction, Metal-Oxide Semiconductor junction. FET theory and analysis.	<b>18</b>
<b>MOS Transistor Fundamentals and MOS I.C Technology</b> :MOS capacitor, static characteristics of the MOS transistor, MOS device fabrication. MOSFET's. Logic circuits based on MOSFET, PMOS, NMOS, CMOS,DMOS, SOS, VMOS. NMOS inverter and gate circuit analysis. CMOS inverter and gate circuit analysis. Charge-coupled devices and non-volatile memory devices, software applications.	<b>18</b>
<b>ASIC Design methodologies and system design consideration</b>	<b>9</b>
<b>LCA, Standard cell, Gate array, Structured array]</b>	<b>9</b>
<b>Full-Custom and Semi-Custom Design</b> :Design motivations; design either discrete component, full-custom and semi-custom design approaches.	<b>9</b>
<b>Field programmable gate arrays FPGA and Field programmable analog arrays FPGA</b>	<b>9</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: Microelectronic By Millmann</b>
<b>2:Pprinciple of CMOS VLSI Design By Neil Weste and KarmanEshrahan</b>

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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Radiation</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4305</b>	<b>Unit</b>	<b>2</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
Various applications of Microwaves, Review of Maxwell's Equations	3
Review of Electromagnetic Theory: (Plane wave incidence on boundaries, Reflection & transmission )	3
Waveguide Theory	3
Rectangular Waveguides	3
Circular Waveguides	3
S-parameters and the scattering Matrix	3
Tee junctions & Magic Tee	3
Attenuators, Directional couplers	3
Propagation into Ferrites, Ferrites Devices	3
Active Microwave Device, Two cavity Klystron	3
Velocity Modulation, Power and Efficiency	3
The Reflex Klystron, Power and frequency characteristics, Magnetron	3
Passive Microwave Devices, Detector Diodes, power sensing diode,	3
Varactor diodes, PIN diodes, BARITE & IMPATT diodes	3
Microwave Transistor circuit	3
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: “Microwave Circuits and devices” by Liao</b>
<b>2: Microwave Engineering” by Pozar</b>

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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Antenna and Propagation</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4308</b>	<b>Unit</b>	<b>2</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
Antenna Theory ( Principles of radiation and equivalent circuit)	<b>3</b>
Antenna Parameters (Gain, Directivity, Bandwidth, Beam width, and Radiation Pattern)	<b>3</b>
Radiation Intensity and Power Density of Antennas	<b>3</b>
Monopole antenna	<b>3</b>
Dipole antenna	<b>3</b>
Array antenna	<b>3</b>
Reflector Antenna (Parabolic antenna)	<b>3</b>
Microstrip antenna	<b>3</b>
Free space propagation	<b>3</b>
Friis Transmission Formula	<b>3</b>
Ground wave propagation (Direct and Reflected)	<b>3</b>
Ionspheric Propagation	<b>3</b>
Radar theory (Circuits and equations)	<b>3</b>
Satellite communication	<b>3</b>
Mobile and 2-Ray model	<b>3</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: “Microwave Circuits and devices” by Liao</b>
<b>2: Microwave Engineering” by Pozar</b>



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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>2 Hrs/wk</b>
<b>Subject</b>	<b>Computer Aided design</b>			<b>Tutorial</b>	<b>1 Hrs/wk</b>
<b>Code</b>	<b>EE4306</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>Hrs/wk</b>

Article	Hrs
<b>Introduction</b> Linear circuits, A.C circuits, A.C circuits matrix analysis, two port analysis, graph theory. Numerical solution for nonlinear network simple search algorithm convergence properties, secant method.	<b>12</b>
<b>Simulation</b> Algorithms , stability and accuracy in Eulers methods , higher-order , Runge-kutto Algorithms.	<b>15</b>
<b>State variable analysis</b> Generation of state equation from topological data , finding a tree, solution of state equations.	<b>18</b>
<b>Sensitivity analysis</b> Sensitivity measures , sensitivity calculation tolerance analysis.	<b>9</b>
<b>Optimization</b> Gradient algorithms , numerical solution of gradient algorithm , stability , search methods.	<b>12</b>
<b>C.A.D for integrated circuits</b> Layout algorithm routing algorithm , testability analysis.	<b>15</b>
<b>Genetic algorithms</b> Application of GA in electronics.	<b>9</b>
<b>Total</b>	<b>90</b>

<b>Text book:</b>
<b>1: Computer Assisted Network and System Analysis by: by Mastacusa</b>

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<b>Class</b>	<b>Fourth</b>	<b>Theory :</b>	<b>1 Hrs/wk</b>
<b>Subject</b>	<b>ENGINEERING PROJECT</b>	<b>Tutorial</b>	<b>Hrs/wk</b>
<b>Code</b>	<b>EE4201</b>	<b>Unit</b>	<b>4</b>
		<b>Practical</b>	<b>3Hrs/wk</b>

Article	Hrs
Collaborative team work of the nature in a research environment is expected, including extensive interaction with other students. Each student should submit a written technical report and should attend the final oral examination . The students apply verbal written and oral technical skills to document the design process .	
<b>Total</b>	<b>120</b>

<b>Text book:</b>
<b>1:</b>

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<b>Class</b>	<b>Fourth</b>			<b>Theory :</b>	<b>Hrs/wk</b>
<b>Subject</b>	<b>Laboratory</b>			<b>Tutorial</b>	<b>Hrs/wk</b>
<b>Code</b>	<b>EE4307</b>	<b>Unit</b>	<b>4</b>	<b>Practical</b>	<b>6Hrs/wk</b>

Article	Hrs
<p>The principle objective is to ensure that students have a good quality capstone design &amp; experience to integrate concepts from a range of classes in the core. The students are to apply modern engineering practices and techniques. Each student should submit a written technical report for each experiment.</p>	
<b>Total</b>	<b>180</b>

<b>Text book:</b>
<b>1:</b>

وصف المقررات للمراحل الاولى والثانية حسب نظام  
بولونيا والثالثة والرابعة سنوي

**Courses specification for second, third,  
and fourth class**

**للعام الدراسي ٢٠٢٣ - ٢٠٢٤**

جامعة نينوى

كلية هندسة الالكترونيات

قسم هندسة الالكترونيات