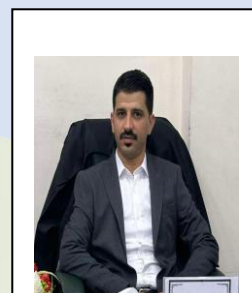




## Faculty Member Academic Biography



### Personal data

<b>Name</b>	Harith Hazim Thannoon		
<b>Current academic rank</b>	Lecturer		
<b>Workplace</b>	College of Electronics Engineering	<b>Academic department</b>	Electronic Engineering
<b>General specialization</b>	Electrical Engineering	<b>Specialization</b>	Electronics and communications engineering
<b>E-mail</b>	harith.thannoon@uoninevah.edu.iq		

### Academic qualifications

Degree	University Name	Country Studied at	Major	Graduation Date
B.Sc.	University of Mosul	Iraq	Electronic Engineering	2012
M.Sc.	Technology University	Iraq	Electronics and communications engineering	2019
Pd.D.	Technology University	Iraq	Electronics and communications engineering	2024

### Academic rank:

Academic rank	Date of Obtaining the rank	Academic rank granting Institution
Assistant Lecturer	6/5/2019	Ninevah University
Lecturer	12/12/2024	Ninevah University

### Practical experiences:

Time period		Employer	Job Title
from	to		
1/2/2016	20/9/2016	Ninevah University	Head of Retirement Division

### Scientific production (published/accepted for publication):

Research Title	Place of publication	Country of publication	Number	Vol	date of publication
Design and Implementation of deception detection system based on reliable facial expression	Journal of engineering and applied sciences	Germany			2019
A Survey on deceptive detection systems and technologies	Engineering and Technology journal	Iraq			2019
Hardware Implementation of a High-Speed Adaptive Filter Using a Combination of Systolic and Convex Architectures	Circuits, Systems, and Signal Processing	USA			2023
A Novel Design of Combined Systolic and Convex Architectures for Efficient High-Speed Adaptive Filter	Journal of Signal Processing Systems	USA			2024

Efficient enhanced Recursive Least Square algorithm adaptive filtering scheme for artifacts removal in ECG signals	e-Prime - Advances in Electrical Engineering, Electronics and Energy	USA			2023
Efficient FPGA Implementation of Recursive Least Square Adaptive Filter Using Non-Restoring Division Algorithm	INTL JOURNAL OF ELECTRONICS AND TELECOMMUNICATIONS	Poland			2023
FPGA Implementation of Efficient Adaptive Filter Incorporating Systolic Architecture	Engineering and Technology Journal	Iraq			2024

NINEVAH UNIVERSITY

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## Conferences, scientific seminars and workshops in which I participated:

Research Title	Name of conference/seminar/workshop	Venue of the conference/seminar/workshop	Date
Detection of deception using facial expressions based on different classification algorithms	Third scientific conference of electrical engineering (SCEE), University of Technology-Iraq	Iraq	2018
FPGA design of a high-speed adaptive filter using a combination of RDLMS and RDZA-LMS	THE SECOND INTERNATIONAL CONFERENCE ON SCIENTIFIC RESEARCH AND INNOVATION 2023 (2ICSRI2023)	USA	2025

## Published books

Book Title	Publishing Institution	Year of Publication	Number of pages

## Awards obtained:

Name of the award	The award granting institution	Date

**Patent:**

Patent title	Registration Institution	Date

**Courses(s) I have taught:**

Course name (subject)	Academic year	University/ Institution
Digital Design	2019-2021,2024	جامعة نينوى
Programmable Logic Control (PLC)	2020-2023	جامعة نينوى
Computer Aided Design (CAD)	2021-2025	جامعة نينوى

**Supervising master's or doctoral theses:**

Title	Degree	Date

**Experience in the field of local community service:**

Post name	Post type	Date

**Note: This CV has been updated until 9 / 4 / 2025**