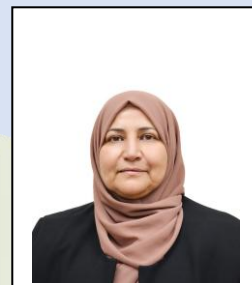




Faculty Member Academic Biography



Personal data

Name	Zahraa siddiq yahya ahmed alsaegh		
Current academic rank	Assistant Lecturer		
Workplace	College of Electronics engineering	Academic department	Electronic
General specialization	Electrical engineering	Specialization	Electronic
E-mail	Zahraa.yahya@uoninevah.edu.iq		

Academic qualifications

Degree	University Name	Country Studied at	Major	Graduation Date
BSc.	Mosul	iraq	Electrical engineering	1988
MSc.	Mosul	iraq	Electrical engineering/Electronic dept.	2016

Academic rank:

Academic rank	Date of Obtaining the rank	Academic rank granting Institution
Assistant Lecturer	24/10/2016	Ninevah university

Practical experiences:

Time period		Employer	Job Title
from	to		
2006	2012	College of electronics engineering/ electronic dept.	engineer
2016	2025	College of electronics engineering/ electronic dept.	lecture

Scientific production (published/accepted for publication):

Research Title	Place of publication	Country of publication	Number	Vol	date of publication
1. Architectural Design and Implementation of Bit Error Rate Tester on FPGA	American Scientific Research Journal for Engineering, Technology, and Sciences (ASRJETS)	USA	1	21	2016-07-05
2. Study of Efficiency-limiting Defects in silicon solar cells	The 11th International Renewable Energy Congress (IREC 2020), IEEE	Hammamet, Tunisia 10.1109/IREC48820.2020.9310427 10/2020			
3. A non -ideal hybridization issue for vertical TFET-based dielectric-modulated biosensor	Open engineering	Germany	1	14	August 2024

Conferences, scientific seminars and workshops in which I participated:

Research Title	Name of conference/seminar/workshop	Venue of the conference/seminar/workshop	Date
1.	Principles of Academic writing	College of electronics engineering/ electronic dept.	-20 22/6/2021
2.	Constructing solar cells using SCAPS simulation software	College of electronics engineering/ electronic dept.	4- 7/7/2021

3.	An introduction to the EndNote reference program	College of electronics engineering/ electronic dept.	5-6/6/2022
4.	Introduction to VHDL	College of electronics engineering/ electronic dept.	1-2/6/2022
5.	Learning Silvaco for semiconductor simulation	College of electronics engineering/ electronic dept.	27-28/5/2024

Courses(s) I have taught:

Course name (subject)	Academic year	University/ Institution
Basics of Electrical Networks	2019-2023	College of electronics engineering/ electronic dept. First Stage
Lab. of Basics of Electrical Networks	2006-2025	College of electronics engineering/ electronic dept. First Stage
Electronic Measurements	2016-2017	College of electronics engineering/ electronic dept. third Stage
Lab. of Electronic	2010-2012 2021-2022	College of electronics engineering/ electronic dept. second Stage
Analysis of DC circuits Analysis of AC circuits	2023-2024 2024-2025	College of electronics engineering/ electronic dept. First Stage
Lab. of Analysis of DC circuits Lab. of Analysis of AC circuits	2023-2024 2024-2025	College of electronics engineering/ electronic dept. First Stage

Experience in the field of local community service:

Post name	Post type	Date
Contribute to a tree planting campaign on campus	Collective	2020,2021,2024,2025
Contribute to the donation campaign for thalassemia patients	Collective	2022,2023,2024
Contribute to the group iftar banquet	Collective	2023,2024,2025
Contribute to supporting the Student Club	Collective	2024

Note: This CV has been updated until / / 2024

