Faculty Member
Academic Biography
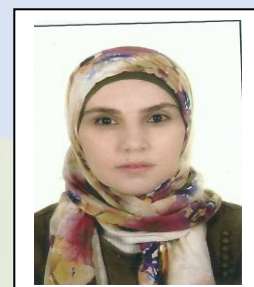
Personal data

Name	Noor Talal Mahmood Gadawe		
Current academic rank	Literature		
Workplace	Electronics Engineering	Academic department	Electronic
General specialization	Electrical Engineering	Specialization	Electronic &Communication
E-mail	noor.gadawe@uoninevah.edu.iq		

Academic qualifications

Degree	University Name	Country Studied at	Major	Graduation Date
BSc	Mosul University	Iraq	Electrical Engineering	2005
MSc	Mosul University	Iraq	Electrical Engineering	2008

Academic rank:

Academic rank	Date of Obtaining the rank	Academic rank granting Institution
Assistant Literature	2008/1/31	Mosul University
Literature	2013/2/25	Mosul University

Practical experiences:

Time period		Employer	Job Title
from	**to**		

Scientific production (published/accepted for publication):

Research Title	Place of publication	Country of publication	Number	Vol	date of publication
"FPGA implementation of a multilayer perceptron (MLP) network",	rafidain Al engineering journal	العراق	1	17	feb 2009
FPGA implementation of OFDM system", vol 6 no.1	in Mediterranean journal of computers and network	UK	1	6	2010 January
a new method for hardware implementation of digital modulator system on fpga"	Mediterranean journal of computers and network	UK	2	6	2010 January
Design and implementation of smart traffic light controller using VHDL language	International Journal of Engineering &Technology,	UAE	4	8	2019
Real Time Traffic Light Controller System based on FPGA and Arduino",	EAI Endorsed Transactions on Pervasive Health and Technology journal				
REAL-TIME PROCESSING STAGES OF ELECTROCARDIOGRAM SIGNAL: A REVIEW"	Journal of Modern Technology and Engineering	العراق	1	9	2024
Realization of IIR Digital Filter Structures for ECG Denoising"	Journal Européen des Systèmes Automatisés		2	57	2024

Conferences, scientific seminars and workshops in which I participated:

Research Title	Name of conference/seminar/workshop	Venue of the conference/seminar/workshop	Date
Optimum hardware implementation of FFT/IFFT on FPGA	the third scientific conference of wassit university	العراق	12/2009
Synthesis and Implementation of IIR Filter using VHDL Language	first International Ninevah Conference on Engineering and Technology (INCET2021).	العراق	2021

Published books

Book Title	Publishing Institution	Year of Publication	Number of pages

Awards obtained:

Name of the award	The award granting institution	Date

Patent:

Patent title	Registration Institution	Date

Courses(s) I have taught:

Course name (subject)	Academic year	University/ Institution
Digital Signal Processing (Third Class)	2009	Electronic
Signal & System (Second Class)	2008 2018	Electronic
Electronic Lab (Forth Class)	2025-2008	Electronic
Electronic Lab (Third Class)	2022-2023	Electronic
Digital Signal Processing (Third Class)	2023-2022	Control& System
Digital Signal Processing (Third Class)	2023-2022	Control& System

Supervising master's or doctoral theses:

Title	Degree	Date

Experience in the field of local community service:

Post name	Post type	Date

Note: This CV has been updated until / / 2024