

وزارة التعليم العالي والبحث العلمي  
جهاز الإشراف والتقييم العلمي  
دائرة ضمان الجودة والاعتماد الأكاديمي

وصف البرنامج الأكاديمي لقسم هندسة الإلكترونيك  
للعام الدراسي 2024-2025  
نموذج وصف البرنامج الأكاديمي

اسم الجامعة: جامعة نينوى .....  
الكلية/ المعهد: كلية هندسة الإلكترونيات  
القسم العلمي: قسم هندسة الإلكترونيك  
اسم البرنامج الأكاديمي أو المهني: بكالوريوس علوم هندسة الإلكترونيك  
اسم الشهادة النهائية: بكالوريوس علوم هندسة الإلكترونيك  
النظام الدراسي: بولونيا الأول والثاني  
تاريخ اعداد الوصف: 2025-3-20  
تاريخ اعداد الملف: 2025-3-20

التوقيع:  
اسم معاون العلمي: أ.م.د. بلال علاء الدين جبر  
التاريخ:

التوقيع :  
اسم رئيس القسم: أ.م.د. حارث احمد محمد  
التاريخ:

دقق الملف من قبل شعبة ضمان الجودة والأداء الجامعي  
اسم مدير شعبة ضمان الجودة والأداء الجامعي:

التاريخ  
التوقيع

مصادقة السيد العميد

## 1. رؤية البرنامج

ان يكون قسم هندسة الالكترونيك متخصصا في علوم هندسة الالكترونيات وأن يتميز بتعليمه الهندسي والبحث العلمي المؤدي الى تقدم المعرفة وتطوير المهنة وخدمة المجتمع من خلال الشراكة مع الصناعات والمؤسسات الهندسية والخدمية وتخريج الكوادر لرفد المجتمع بكوادر عالية الكفاءة.

## 2. رسالة البرنامج

1. التعليم: توفير برامج هندسية تعليمية متخصصة ذات تخصص دقيق للدراسات الأولية والعليا. وتوفير بيئة تعليمية متميزة معترف بها بحيث يمتلك خريجوها خبرة مهنية عالية وتعليم هندسي اساسي تمكنهم من المساهمة بفعالية في خدمة مجتمعهم ورفع مستوى وتقدم مهنتهم. كل ذلك يقع ضمن المواصفات العالمية القياسية ويتم تنفيذه باعتماد نظام الجودة ABET في مجال التعليم الهندسي

2. البحث: توفير بيئة بحثية عالية المستوى بحيث يتمكن اساتذته وباحثوه وطلابه من قيامهم بالبحوث في المجالات الهندسية الاساسية والتطبيقية والاستكشافية ونشر وتطبيق المعرفة المتوافرة والجديدة بما يخدم المجتمع والمنطقة ويتفاعل مع العالم.

3. القيادة: تطوير الامكانات القيادية للمنتسبين والطلاب وزرع امكانيات التعليم الذاتي والاستقراء والاستنتاج بحيث يتمكن من يمتلك الموهبة في مجال المهنة.

خدمة المجتمع: التفاعل مع المجتمع والانخراط في مجال تطوير صناعة البلاد والمؤسسات الهندسية الذي يؤدي الى التطوير الاجتماعي والاقتصادي للبلاد من خلال الاستشارات والتعليم المستمر والتزام المشاكل الصناعية كبحوث لتقديم الحلول لها.

## 3. اهداف البرنامج

أ. تخريج مهندسين مختصين في مجال علوم هندسة الإلكترونيك وتطبيقاتها وبمواصفات عالية ويمتلكون القدرة على العمل في القطاع العام والخاص

ب. المساهمة الفعالة في نهضة وتقدم المجتمع من خلال عقد الندوات والمؤتمرات والتعليم المستمر

ج. انتاج بحوث علمية رصينة تطبيقية في تخصص الهندسة الإلكترونية لغرض حل المشاكل الصناعية والخدمية في المجتمع

د. تعزيز جانب القيادة لدى المنتسبين والخريجين وبث روح التعاون بينهم

هـ. منح شهادات عليا في تخصصات القسم المختلفة وبمواصفات عالية

و-. اعتماد منهج التحديث في المناهج الدراس وتحسين الاداء في الفعاليات والانشطة لضمان تحقيق الاهداف المنشودة للقسم

اهداف قسم هندسة الالكترونيك

- تخريج مهندسين أكفاء في مجال الهندسة الإلكترونية يمتلكون القدرة على تحديد المشكلات وتحليلها وتطوير حلول فعالة لها، مع المهارة في استخدام التقنيات الحديثة.
  - إعداد مهندسين قادرين على العمل بشكل تعاوني واحترافي مع المختصين وصُنّاع القرار وغيرهم ضمن بيئتهم المهنية.
  - تأهيل الخريجين للالتحاق ببرامج الدراسات العليا محليًا ودوليًا، والعمل في مراكز البحوث.
  - إعداد مهندسين يلتزمون بالمعايير المهنية والمسؤوليات الأخلاقية في ممارسة الهندسة الإلكترونية.
- المساهمة الفعّالة في تقدم المجتمع من خلال عقد الندوات والمؤتمرات والتعليم المستمر في مجال الهندسة الإلكترونية، مع تبني نهج التحسين المستمر لجميع الأنشطة والبرامج

4. الاعتماد البرامجي
البرنامج غير حاصل ع الاعتماد البرامجي لحد الان

5. المؤثرات الخارجية الأخرى
وزارة التعليم العالي العلمي هي الجهة الراعية للبرنامج

6. هيكلية البرنامج				
هيكل البرنامج	عدد المقررات	وحدة دراسية	النسبة المئوية	ملاحظات *
متطلبات المؤسسة	4	12	7.8%	مقرر اساسي
متطلبات الكلية	8	36	23.4%	مقرر أساسي
متطلبات القسم	25	106	68.8%	مقرر أساسي
التدريب الصيفي	في المرحلة الثالثة			أساسي
أخرى				

\* ممكن ان تتضمن الملاحظات فيما إذا كان المقرر أساسي او اختياري.

## 7. وصف البرنامج

المرحلة الدراسية		رمز المقرر أو المساق	اسم المقرر أو المساق	الساعات المعتمدة	
				نظري	عملي
الاول / الفصل الدراسي 1	NVEE206	Mathematics I	3	-	
الاول / الفصل الدراسي 1	NVEE215	DC Circuits Analysis	3	3	
الاول / الفصل الدراسي 1	NVEE218	Physical Electronics	2	-	
الاول / الفصل الدراسي 1	NVEEEL114	Computer science	2	2	
الاول / الفصل الدراسي 1	NVEE203	Mechanical engineering principles	2	-	
الاول / الفصل الدراسي 1	NV12	Democracy and Human Rights	2	-	
الاول / الفصل الدراسي 2	NVEE216	AC Circuits Analysis	3	3	
الاول / الفصل الدراسي 2	NVEE207	Mathematics II	3		
الاول / الفصل الدراسي 2	NVEE219	Physics Of Semiconductor	2	-	
الاول / الفصل الدراسي 2	NVEE217	Digital Techniques	2	2	
الاول / الفصل الدراسي 2	NVEE201	Engineering Drawing	-	3	
الاول / الفصل الدراسي 2	NVU11	English	2		
الثاني /الكترونيات الاجهزة الطبيه					
الثاني / الفصل الدراسي 1	NVEE208	Engineering Analysis I	2	-	
الثاني / الفصل الدراسي 1	NVEEELM211	Signal Analysis	2	2	
الثاني / الفصل الدراسي 1	NVEEELM212	Electronic I	2	3	
الثاني / الفصل الدراسي 1	NVEE223	Digital design	3	-	
الثاني / الفصل الدراسي 1	NVEE215	Electromagnetic fields I	2	2	
الثاني / الفصل الدراسي 1	NVEEELM 213	Human Physiology	2	-	
الثاني / الفصل الدراسي 2	NVEE210	signals and systems	2	2	
الثاني / الفصل الدراسي 2	NVEE209	Engineering Analysis II	2	-	
الثاني / الفصل الدراسي 2	NVEEELM221	Electronic II	2	-	
الثاني / الفصل الدراسي 2	NVEEELM222	programming	2	2	
الثاني / الفصل الدراسي 2	NVEE221	Electromagnetics FieldsII	2		
الثاني / الفصل الدراسي 2	NVU13	The Crimes of the Defunct Baath Party	2	-	
الثاني /صناعي					
الثاني / الفصل الدراسي 1	NVEE208	Engineering Analysis I	2	-	
الثاني / الفصل الدراسي 1	NVEEELI212	Electronic I	2	3	
الثاني / الفصل الدراسي 1	NVEEELI213	DC Machines	2	2	
الثاني / الفصل الدراسي 1	NVEEELI214	Computer Programming	2	2	
الثاني / الفصل الدراسي 1	NVU13	The crimes of the defunct Baath Party	2	-	
الثاني / الفصل الدراسي 1	NVEE221	Fundamentals of Electromagnetics	2	-	
الثاني / الفصل الدراسي 2	NVEE209	Engineering Analysis II	2	-	
الثاني / الفصل الدراسي 2	NVEEELI222	Electronics II	2	2	
الثاني / الفصل الدراسي 2	NVEEELI223	AC Machines	2	2	
الثاني / الفصل الدراسي 2	NVEEELI224	Computer Languages	2	2	

-	3	Digital Design	NVEE223	الثاني / الفصل الدراسي 2
2	2	Signals and Systems	NVEE210	الثاني / الفصل الدراسي 2

## 8. مخرجات التعلم المتوقعة للبرنامج

### المعرفة

- تخريج مهندسين حاسبات مؤهلين يمتلكون المعرفة في الرياضيات والمنطق وعلوم الهندسة
- تخريج مهندسين قادرين على تصميم وإجراء التجارب وتحليل النتائج

### المهارات

- القدرة على تصميم وإجراء التجارب المناسبة، وتحليل البيانات وتفسيرها، وتطبيق مبادئ ضمان الجودة
- القدرة على التواصل بفاعلية مع جماهير متنوعة وعبر مختلف المستويات التنظيمية والإدارية

### القيم

- تخريج مهندسين قادرين على التعرف على المسؤوليات الأخلاقية والمهنية في المواقف الهندسية، مع الأخذ في الاعتبار التأثيرات الاقتصادية والبيئية والاجتماعية للحلول الهندسية
- تخريج مهندسين قادرين على التعرف على الحاجة المستمرة للتطوير المهني وتحديد المعرفة ذات الصلة وتطبيقها بشكل فعال
- القدرة على العمل بفاعلية كعضو أو قائد في فريق، وتحديد الأهداف، والتخطيط، والالتزام بالمواعيد النهائية

### نواتج التعلم

- تخريج مهندسين أكفاء في مجال الهندسة الإلكترونية يمتلكون القدرة على تحديد المشكلات وتحليلها وتطوير حلول فعالة لها، مع المهارة في استخدام التقنيات الحديثة.
- إعداد مهندسين قادرين على العمل بشكل تعاوني واحترافي مع المختصين وصُنَّاع القرار وغيرهم ضمن بيئتهم المهنية.
- تأهيل الخريجين للالتحاق ببرامج الدراسات العليا محلياً ودولياً، والعمل في مراكز البحوث.
- إعداد مهندسين يلتزمون بالمعايير المهنية والمسؤوليات الأخلاقية في ممارسة الهندسة الإلكترونية.
- المساهمة الفعالة في تقدم المجتمع من خلال عقد الندوات والمؤتمرات والتعليم المستمر في مجال الهندسة الإلكترونية، مع تبني نهج التحسين المستمر لجميع الأنشطة والبرامج.

### الأهداف الوجدانية والقيمية

- 1- تطوير قدرة الطالب للعمل على أداء الاعمال المناطة له وانجازها في الموعد المحدد بدقة وإخلاص.
- 2- لتفكير التحليلي العلمي المبني على قواعد اساسية علمية ومنطقية.
- 3- تمكين الطالب من الحوار والمناقشة بالمسائل المتعلقة بتخصصه بشكل مثمر.

4- تبادل الآراء وافساح المجال للأخريين لتوضيح وجهات النظر المختلفة في المسائل المطروحة.

5- القابلية على العمل ضمن فريق العمل الواحد

6- تقديم التوعية الكاملة للتحلي بأخلاقيات العمل والمهنة

7- القابلية على عرض الأفكار ومناقشتها والدفاع عنها بالأسلوب الإداري والعلمي الصحيح

## 9. استراتيجيات التعليم والتعلم

### استراتيجيات التعليم والتعلم

- المتابعة من خلال سير تنفيذ الواجبات والدقة في التعامل معها
- تشكيل مجاميع صغيرة من الطلبة لحل معضلة معينة وتبادل الآراء مع الزملاء بخصوصها
- فتح باب النقاش في بعض المسائل وبالشكل الذي يضمن مشاركة الجميع والتعود على سماع آراء مختلفة
- الامتحانات الفصلية والنهائية
- الامتحانات اليومية القصيرة
- اجراء التجارب المختبرية وكتابة التقارير ومناقشة النتائج المختبرية
- المشاركة في مؤتمرات علمية والنشاطات الصفية التي تتضمن تصميم بعض أنظمة الالكترونية
- امتحانات الكترونية وتكليفات ضمن وقت محدد على المنصات التعليمية

## 10. طرائق التقييم

- المشاركة في قاعة الدرس الحضورية او الالكترونية.
- تقديم التقارير المختبرية.
- تقييم التنفيذ العملي للتجارب.
- تقديم الانشطة المختلفة.
- اختبارات يومية وفصلية ونهائية حضوريا والكترونيا.

## 11. الهيئة التدريسية

ت	الاسم الكامل واللقب	العنوان الوظيفي	اللقب العلمي	الشهادة	الاختصاص العام	الاختصاص الدقيق
1	خالد خليل محمد جاسم	تدريسي	أستاذ	دكتوراه	هندسة كهرباء	الالكترونيك واتصالات
2	قيس ذنون نجم عبد الله ال احمد جاسم	تدريسي	أستاذ	دكتوراه	علوم فيزياء	بلازما
3	احمد ذنون يونس حسين النقيب	تدريسي	أستاذ مساعد	دكتوراه	هندسة كهرباء	الالكترونيات الدقيقة
4	مجاهد فهمي إبراهيم إسماعيل العزو	تدريسي	أستاذ مساعد	دكتوراه	هندسة كهرباء	اتصالات
5	اوس زهير يونس سليمان	تدريسي	أستاذ مساعد	دكتوراه	هندسة حاسوب	اتصالات
6	حارث احمد محمد احمد البدراني	تدريسي	أستاذ مساعد	دكتوراه	هندسة كهرباء	الالكترونيات القدرة
7	احمد محمد احمد سلامة	تدريسي	أستاذ مساعد	دكتوراه	هندسة كهرباء	اتصالات
8	هشام سوادي هاشم	تدريسي	أستاذ مساعد	دكتوراه	تاريخ	تاريخ حديث

9	عمر بدر محمد خضر النعيمي	تدريسي	مدرس	دكتوراه	هندسة كهرباء	الالكترونيات الدقيقة
10	إيهاب عصام داود سليمان الراوحي	تدريسي	مدرس	دكتوراه	هندسة حاسبات	اتصالات
11	سحر لازم قدوري خضير الدليمي	تدريسي	مدرس	دكتوراه	هندسة حاسبات	حاسوب ومعلوماتيه
12	سرمد فخر الدين إسماعيل جاسم المولى	تدريسي	مدرس	دكتوراه	هندسة حاسبات	تحليل ومعالجة الصورة الرقمي
13	سنان خالد محمد حسن شنشل	تدريسي	مدرس	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
14	نور طلال محمود عزيز كداوي	تدريسي	مدرس	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
15	خالد فزع محمود محمد	تدريسي	مدرس	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
16	عماد عبد الحليم عبدو علي ال ملا خضر	تدريسي	مدرس	ماجستير	هندسة الكترونيك	الالكترونيك
17	عبد الحميد محمد جاسم محمد الجبوري	تدريسي	مدرس	ماجستير	هندسة الكترونيك	الالكترونيك
18	همسة فواز ذنون محمد الرحو	تدريسي	مدرس	ماجستير	هندسة كهرباء	حاله صلبه
19	هبة عبد الخالق حمدون عبد الصواف	تدريسي	مدرس	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
20	شوكت محمد يونس مال الله	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيات القدرة
21	زهراء صديق يحيى احمد الصانغ	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
22	أمنة إدريس كنعان سليمان حيرو	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
23	أسماء نبيل خليل عمر	تدريسي	مدرس مساعد	ماجستير	هندسة حاسبات	حاسوب ومعلوماتيه
24	سدرة عبد الجبار يوسف	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	هندسة كهرباء
25	همام ماهر عبد شاهين الحمداني	تدريسي	مدرس مساعد	ماجستير	هندسة الكترونيك	الالكترونيك
26	يونس صابر عثمان خطاب الرفاعي	تدريسي	مدرس مساعد	ماجستير	هندسة حاسبات	حاسبات
27	حارث حازم ذنون يونس	تدريسي	مدرس مساعد	ماجستير	هندسة الكترونيك	الالكترونيك
28	يعرب عبد المحسن احمد حسين الشلاوي	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيك
29	سنان محمود ايوب محمود الرحو	تدريسي	مدرس مساعد	ماجستير	هندسة الكترونيك	الالكترونيك
30	محمد صالح سفر رسول	تدريسي	مدرس مساعد	ماجستير	هندسة ميكانيك	هندسة الحرارية
31	عامر طلال علي احمد	تدريسي	مدرس مساعد	ماجستير	هندسة الكترونيك	حاسوب ومعلوماتيه
32	هاني محمد صالح سلمان	تدريسي	مدرس مساعد	ماجستير	هندسة ميكانيك	هندسة الحرارية
33	رشا وليد حمد	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
34	عمر نجيب سعدي	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيك و اتصالات
35	ميسرة عبدالجبار قاسم	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	قدرة ومكان
36	هشام محمد محمود	تدريسي	مدرس مساعد	ماجستير	هندسة كهرباء	الالكترونيات القدرة
37	هاجر خليل ابراهيم احمد	تدريسي	مدرس مساعد	ماجستير	هندسة الكترونيك	الالكترونيك
38	محمد صالح سفر رسول	تدريسي	مدرس مساعد	ماجستير	هندسة ميكانيك	هندسة الحرارية
39	نجم عبيد ضحوي	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
40	طارق حسين خضر	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
41	مروه عصام احمد	موظف	لا يوجد	دبلوم عالي	هندسة	هندسة
42	عادل غازي شريف	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
43	محمد موفق هادي	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
44	أسعد عبد الغني صالح	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
45	يثرى وليد قاسم خليل	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
46	سيف الدين كمال	موظف	لا يوجد	بكلوريوس	هندسة	هندسة

47	عمار احمد عبد الله	موظف	لا يوجد	بكلوريوس	هندسة	هندسة
48	لولوه حازم فتح الله	موظف	لا يوجد	بكلوريوس	إدارة واقتصاد	إدارة واقتصاد
49	ادريس محمد يونس احمد	موظف	لا يوجد	دبلوم	معهد	معهد

التطوير المهني	
الأهداف توجيه أعضاء هيئة التدريس الجدد	
<p>مشاركة التدريسي في دورة طرائق التدريس .</p> <p>اجتياز التدريسي لدورة طرائق التدريس.</p> <p>مشاركة التدريسي في المختبرات العملية.</p> <p>مشاركة التدريسي في إعطاء محاضرات نقاشية</p>	
التطوير المهني لأعضاء هيئة التدريس	
<p>التطوير الأكاديمي والمهني لأعضاء هيئة التدريس.</p> <p>المشاركة في المؤتمرات وورش العمل العلمية الدولية والعربية والمحلية.</p> <p>إمكانية استخدام بعض المهارات العلمية المحلية في التدريس أو إجراء البحوث العلمية.</p> <p>استخدام التكنولوجيا الحديثة والأساليب التعليمية المتطورة في التدريس.</p>	
12. معيار القبول	
<p>ان خطة القبول المعتمدة للطلبة الجدد في برامج القسم تتبع بطبيعة الحال خطة القبول المركزي لوزارة التعليم العالي والبحث العلمي وتنفذ من قبل الجامعة والكلية. يمكن القول بان الطلبة الملتحقين ببرامج القسم يمثلون المستويات العليا بمعدلاتهم من المتقدمين الى كلية هندسة الالكترونيات، حيث يعتمد مبدأ المفاضلة على المعدل للدراسة الاعدادية ورغبة الطالب في تحديد البرنامج الدراسي ضمن برامج كلية هندسة الالكترونيات. لذا فان طبيعة من يقبل من الطلبة في برامج القسم هم متميزون بمستوياتهم الدراسية والفكرية وعطائهم طوال فترة البرنامج .</p>	
13. أهم مصادر المعلومات عن البرنامج	
<p>يمكن الحصول على بيانات وافية عن برامج القسم من خلال زيارة الموقع الالكتروني الرسمي لجامعة نينوى وتصفح موقع كلية هندسة الالكترونيات:</p> <p><a href="http://www.uoninevah.edu.iq">www.uoninevah.edu.iq</a></p> <p>تقرير التقييم الذاتي ودليل القسم السنوي ضمن دليل الجامعة والكلية..</p>	



#### 14. خطة تطوير البرنامج

تحديث المقرر سنويا حسب احدث الكتب العلمية ذات الصلة ومتطلبات سوق العمل بالإضافة الى استخدام شبكة المعلومات الدولية للاطلاع على مفردات مناهج المادة في الجامعات العالمية الأخرى والرائدة في هذا المجال.

## مخطط مهارات المنهج

يرجى وضع اشارة في المربعات المقابلة لمخرجات التعلم الفردية من البرنامج الخاضعة للتقييم

مخرجات التعلم المطلوبة من البرنامج

مخرجات التعلم المطلوبة من البرنامج																أساسي أم اختياري	اسم المقرر	رمز المقرر	السنة / المستوى
المهارات العامة والتأهيلية المنقولة (المهارات الأخرى المتعلقة بقابلية التوظيف والتطور الشخصي)				الاهداف الوجدانية والقيمية				الاهداف المهارات الخاصة بالبرنامج				الاهداف المعرفة							
د4	د3	د2	د1	ج4	ج3	ج2	ج1	ب4	ب3	ب2	ب1	أ4	أ3	أ2	أ1				
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Mathematics I	NVEE206	الاول
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	DC Circuits Analysis	NVEE215	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Physical Electronics	NVEE218	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Computer science	NVEEELM111	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	Mechanical engineering principles	NVEE203	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	Democracy and Human Rights	NVU12	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	AC Circuits Analysis	NVEE216	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Mathematics II	NVEE207	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Physics Of Semiconductor	NVEE219	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital Techniques	NVEE217	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	Engineering Drawing	NVEE201	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	ثانوي	English	NVU11	

																	الثاني/ طبي
*			*		*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Analysis I	NVEE208
	*			*	*		*	*	*	*	*	*	*	*	أساسي	Signal Analysis	NVEEELM211
	*	*		*	*	*		*	*	*	*	*	*	*	أساسي	Electronic I	NVEEELM212
	*	*	*		*	*	*	*	*	*	*	*	*	*	أساسي	Digital design	NVEE223
	*	*		*	*	*	*	*		*	*	*		*	أساسي	Electromagnetic fields I	NVEE215
	*	*	*	*			*		*		*	*		*	أساسي	Human Physiology	NVEEELM 213
*	*	*	*	*	*	*	*	*	*	*	*	*	*		أساسي	signals and systems	NVEE210
	*		*		*	*	*		*	*		*	*	*	أساسي	Engineering Analysis II	NVEE209
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic II	NVEEELM221
	*	*			*		*	*	*	*	*	*	*	*	أساسي	programming	NVEEELM222
	*	*			*		*	*		*	*	*	*	*	أساسي	Electromagnetics FieldsII	NVEE221
	*	*			*		*		*	*			*	*	ثانوي	The Crimes of the Defunt Baath Part	NVU13
																	الثاني/ صناعي
*	*	*			*		*	*		*	*	*	*	*	أساسي	Engineering Analysis I	NVEE208
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic I	NVEEELI212
	*	*			*		*	*	*	*		*	*	*	أساسي	DC Machines	NVEEELI213
*	*	*	*	*	*	*	*		*	*	*		*	*	أساسي	Computer Programming	NVEEELI214
*	*	*	*	*	*	*	*	*	*	*	*	*		*	ثانوي	The crimes of the defunct Baath Party	NVU13
*	*	*	*	*	*	*	*	*	*	*	*	*		*	أساسي	Fundamentals of Electromagnetics	NVEE221
	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Analysis II	NVEE209
*	*		*	*	*	*	*	*	*		*	*	*		أساسي	Electronics II	NVEEELI222

	*	*			*	*	*	*	*	*	*	*	*	*	*	أساسي	AC Machines	NVEEELI223	
*	*	*	*	*	*	*	*	*	*	*		*	*	*	*	أساسي	Computer Languages	NVEEELI224	
	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital Design	NVEE223	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Signals and Systems	NVEE210	
	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Electronic II	EE3301	الثالث
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Digital Signal Processing	EE3201	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Control Engineering	EE3302	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microprocessors	EE3303	
*			*	*	*	*		*	*			*	*		*	أساسي	DIGITAL SYSTEM DESIGN	EE3304	
*		*	*	*	*	*	*	*	*		*	*		*	*	أساسي	Communications	EE3305	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	ELECTRONIC INSTRUMENTATION	EE3306	
	*	*		*	*	*		*	*	*	*	*	*	*	*	أساسي	Laboratory	EE3307	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Industrial Electronic	EE4301	الرابع
*	*		*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	DATA TRANSMISSION& COMPUTER ETWORKS	EE4302	
*		*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microprocessor & Micro Controller	EE4303	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microelectronics	EE4304	
*	*	*		*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Microwave Engineering	EE405	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Computer aided design	EE4306	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Engineering Project	EE4201	
*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	أساسي	Laboratory	EE4307	

يرجى وضع إشارة في المربعات المقابلة لمخرجات التعلم الفردية من البرنامج الخاضعة للتقييم

## وصف المقررات

للعام الدراسي 2024 - 2025

جامعة نينوى

كلية هندسة الالكترونيات

قسم هندسة الالكترونيات

## Courses specification for first class (First Course)

Module Information			
<b>Module Title</b>	<b>D.C Circuits Analysis</b>		<b>Module Delivery</b>
<b>Module Type</b>	Base	<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	NVEE215		
<b>ECTS Credits</b>	5		
<b>SWL (hr/sem)</b>	125		
<b>Module Level</b>	1	<b>Semester of Delivery</b>	1
<b>Administering Department</b>		<b>College</b>	
<b>Module Leader</b>	Zahraa Siddiq Yahya	<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>	Lecturer assistant	<b>Module Leader's Qualification</b>	
<b>Module Tutor</b>	Zahraa Siddiq Yahya	<b>e-mail</b>	E-mail
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	E-mail
<b>Scientific Committee Approval Date</b>	01/06/2023	<b>Version Number</b>	1.0

Relation with other Modules			
<b>Prerequisite module</b>		<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	<ol style="list-style-type: none"> <li>1. To identify the basic concepts of DC Electrical Eng. circuits.</li> <li>2. To understand how is the calculation of current, voltage, and power.</li> <li>3. To understand and cover the basic DC circuit analysis methods and theorems.</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Explain the function of each element in DC Electrical circuits.</li> <li>2. Use the basic circuit analysis methods and theorems to simplified the DC Electrical circuits.</li> <li>3. Explain the different between transformation methods.</li> <li>4. Applying the appropriate analysis method to reach the aim in its simplest form.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A – BASIC CONCEPTS:</u></p> <p>Voltage &amp; current; Power &amp; Energy; Dependent and Independent sources; Ohm's laws series &amp; parallel connections; Delta- star connections and transformations. [15 hrs]</p> <p><u>Part B - D.C. Network Theorems:</u></p> <p>Source transformation; Linearity &amp; superposition; Thevenin's &amp; Norton's Theorems; Source transportation; source superposition; Nodal analysis; Mesh analysis. [35 hrs]</p>

## Learning and Teaching Strategies

<b>Strategies</b>	Type something like: The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering types of simple experiments involving some sampling activities that are interesting to the students.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b> ∪	30	<b>Structured SWL (h/w)</b> 1	4
<b>Unstructured SWL (h/sem)</b>	30	<b>Unstructured SWL (h/w)</b>	4
<b>Total SWL (h/sem)</b>	<b>60</b>		

## Module Evaluation

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	<b>Assignments</b>	1	10% (10)	14	LO #4, #7, #(10-13)
	<b>Projects / Lab.</b>	0	0% (0)		
	<b>Report</b>	1	10% (10)	12	LO #11
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5 hr	10% (10)	10	LO #(1-8)
	<b>Final Exam</b>	3 hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		



Delivery Plan (Weekly Syllabus)	
	Material Covered
<b>Week 1</b>	Voltage & current
<b>Week 2</b>	Power & Energy
<b>Week 3</b>	Dependent and Independent sources
<b>Week 4</b>	Ohm's laws
<b>Week 5</b>	series & parallel connections
<b>Week 6</b>	Delta- star connections and transformations
<b>Week 7</b>	Kirchhoff's Current & Voltage Laws (KCL), (KVL)
<b>Week 8</b>	Source transformation
<b>Week 9</b>	Linearity & superposition
<b>Week 10</b>	Nodal analysis
<b>Week 11</b>	Mesh analysis
<b>Week 12</b>	Thevenin's Theorem
<b>Week 13</b>	Norton's Theorem
<b>Week 14</b>	Max. power transfer
<b>Week 15</b>	Preparatory week before the final Exam

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	" Engineering Circuit Analysis" By W. Hayt	Yes
<b>Recommended Texts</b>	"Introductory Circuit Analysis" By Boylested	Yes

Grading Scheme				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information					
Module Title	Computer science		Module Delivery		
Module Type	Core		<div><input type="checkbox"/> Theory</div> <div><input checked="" type="checkbox"/> Lecture</div> <div><input checked="" type="checkbox"/> Lab</div> <div><input checked="" type="checkbox"/> Tutorial</div> <div><input checked="" type="checkbox"/> Practical</div> <div><input type="checkbox"/> Seminar</div>		
Module Code	NVEEELM114				
ECTS Credits	4				
SWL (hr/sem)	100				
Module Level		1	Semester of Delivery		1
Administering Department		ELM	College	NE	
Module Leader	Asmaa Nabeel		e-mail	asmaa.khaleel@uoninevah.edu.iq	
Module Leader’s Acad. Title		Lecturer Assist	Module Leader’s Qualification		M.Sc.
Module Tutor			e-mail		
Peer Reviewer Name		Name	e-mail	E-mail	
Scientific Committee Approval Date		4/7/2023	Version Number		1.0

### Relation with other Modules

<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

### Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<p>General overview of personal computer architecture</p> <p>Computer peripherals, keyboard, screen, mouse, and storage media</p> <p>Computer busses, ports, interfaces</p> <p>Overview of MSDOS operating system</p> <p>MSDOS internal commands</p> <p>MSDOS external commands</p> <p>Introduction to computer languages</p> <p>Overview of windows operating system</p> <p>Windows desktop, changing settings, starting programs</p> <p>Creating, deleting, copying, moving, searching for files and folders</p> <p>Using my computer, my document, and help facility</p> <p>Using windows control panel</p> <p>Using the windows accessories paint, notepad, word pad, .....etc</p> <p>Setup applications to windows, remove applications from windows</p> <p>Connecting to the internet, using the windows explorer</p> <p>Using the Microsoft Word</p> <p>Using the Microsoft Excel</p> <p>Using the Matlab</p>
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<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Understanding the important components of the computer and its operating system.</li> <li>2. Understanding the meaning of MSDOS operating system and its commands.</li> <li>3. Understanding the windows operating system</li> <li>4. Understanding the Microsoft office (word, power point, excel).</li> <li>5. Understanding the high and low level languages</li> <li>6. Learn about how the strings represented in C language.</li> <li>7. introduction to matlab</li> </ol>
<b>Indicative Contents</b>	<ol style="list-style-type: none"> <li>1. explain the components of computer hardware and software</li> <li>2. introduction to the types of computers</li> <li>3. storage media</li> <li>4. computer ports</li> <li>5. computer networks and the types of it</li> <li>6. the internal and external MSDOS commands</li> <li>7. windows operating system</li> <li>8. word office program</li> <li>9. power point office program</li> <li>10. Excel program</li> <li>11. Matlab</li> </ol>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>	125	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	65	<b>Unstructured SWL (h/w)</b>	2
<b>Total SWL (h/sem)</b>	190		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	2, 4, 5,6	LO #1, 2, 10 and 11
	Assignments	1	10% (10)	14	LO # 3, 4, 6 and 7
	Projects / Lab.	0	0	0	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	1	20	14	
Summative assessment	Midterm Exam	1.5hr	30% (20)	10	LO # 1-4
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Introduction to the part of computers in hardware and software ,computer types, storage media
Week 2	Explain the computer ports , computer networks
Week 3	Introduction to MSDOS operating system and the internal commands of it
Week 4	External Ms DOS command , file and folder related commands and the editor
Week 5	Windows operating system
Week 6	Windows orders(change the background, screen saver, resolution), change the status of files, printing files, copy and save of files, backups, Recycle bin ,compressing files, viruses
Week 7	Microsoft office word (creating new word file, bars , types and styles of fonts, copy and select of texts ,save of word file)
Week 8	MS WORD: spell checking, inserting symbols, add borders, change the document setup , insert table, page numbering, insert equations and effects)
Week 9	MS Power point:(how to design professional presentation, change the layout of presentation and background of it, numbering slides, insert charts , insert table and audio )

<b>Week 10</b>	MS Power point(insert an effect to the object in slide, transition between slides , grouping of objects, insert equation, copy ,save and printing the slides then how to start the presentation )
<b>Week 11</b>	MS EXCEL (getting started with excel, how to create a spreadsheet, copy and rename the work book, entering and deleting of data in sheet, insert and delete of rows& columns, selecting cells, adding border to sheet)
<b>Week 12</b>	MS EXCEL:how to write a formule in sheet, functions, summation of data in row or column ,average function, max& min functions, count& counta, round function, save and print the spread sheet
<b>Week 13</b>	Overview of High &Low level languages
<b>Week 14</b>	<b>Matlab</b>
<b>Week 15</b>	
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

### Delivery Plan (Weekly Lab. Syllabus)

	<b>Material Covered</b>
<b>Week 1-15</b>	The application of each part of the covered drawing subject theoretically and according to the weekly sequence of the curriculum in the AutoCAD laboratory  <b>Note: By two hours a week</b>

### Learning and Teaching Resources

	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	1. "Computer Science"	No
<b>Recommended Texts</b>	2. "MATLAB Handbook"	No
<b>Websites</b>	<a href="https://www.tutorialsmate.com/2021/12/parts-of-computer">https://www.tutorialsmate.com/2021/12/parts-of-computer</a> <a href="https://www.koenig-solutions.com/matlab-programming">https://www.koenig-solutions.com/matlab-programming</a>	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information					
معلومات المادة الدراسية					
Module Title	Mathmatics1		Module Delivery		
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	NVEE206				
ECTS Credits	6				
SWL (hr/sem)	150				
Module Level		1	Semester of Delivery		1
Administering Department		Electronic Eng. Dep.	College	Electronics Engineering	
Module Leader	Hani M. S. Salman		e-mail	hani.mohamed@uoninevah.edu.iq	
Module Leader’s Acad. Title		Assistant Lecturer	Module Leader’s Qualification		MSc
Module Tutor	Name (if available)		e-mail	E-mail	
Peer Reviewer Name		Name	e-mail	E-mail	
Scientific Committee Approval Date			Version Number	1.0	

### Relation with other Modules

العلاقة مع المواد الدراسية الأخرى

Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

### Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<b>Module Objectives</b> أهداف المادة الدراسية	<ol style="list-style-type: none"> <li>Gain proficiency in differentiating trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.</li> <li>Master differentiation techniques for various types of functions.</li> <li>To learn how to sketch curves and to deal with the transcendental functions.</li> <li>To increase the skills related to differentiation applications.</li> <li>Develop a strong foundation in Integration of trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.</li> <li>Understand the concept of Application of the definite integral, including finding volumes of revolution, lengths of curves, and surface areas of revolution.</li> <li>To learn the methods of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions.</li> <li>Apply calculus principles to solve real-world engineering problems, developing problem-solving skills and the ability to apply calculus concepts to practical situations.</li> </ol>
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	<ol style="list-style-type: none"> <li>Understand the concept of differentiation as a rate of change and slope of the curve.</li> <li>Understand the basic differentiation rules, chain rule, implicit differentiation, higher order differentiation, partial differentiation, Differentiation of trigonometric functions and Hyperbolic Functions.</li> <li>Learn the applications of differentiation.</li> <li>Solve Maximum and Minimum problems.</li> <li>Learn how to Plot the Curve.</li> <li>Learn Transcendental functions: graphs, and derivative.</li> <li>Understand the concept of integration: types of integrals. definite integrals, infinite Integrals. Integration of trigonometric function,</li> </ol>



	<p>inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.</p> <p>8. Apply definite integration to as areas between curves, volumes of revolution, length of the curve and surface area of revolution.</p> <p>9. Learn Methods of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions.</p> <p>10. Develop critical thinking and problem-solving skills by applying calculus.</p>
<p><b>Indicative Contents</b> المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p><u>Part A – Differentiation:</u> Definitions and notations, basic differentiation rules, chain rule, implicit differentiation, higher order differentiation, partial differentiation, Differentiation of trigonometric functions and Hyperbolic Functions: . Applications of differentiation – slope tangents and normal, rate of change, velocity and acceleration, maxima and minima and inflexion points, and Curve plotting. [16 hrs]</p> <p>Transcendental Functions – definitions, properties, graphs, derivative. [4 hrs]</p> <p><u>Part B – Integration:</u> Definitions and notations, types of integrals: definite integrals, infinite Integrals. Integration of trigonometric function, inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function. [12 hrs] Application of the definite integral – areas between curves, volumes of revolution, length of the curve and surface area of revolution. [12 hrs]</p> <p>Methods Of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions. [16 hrs]</p>

<p><b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم</p>	
<b>Strategies</b>	<p>This module's major aim is to foster student engagement, improve critical thinking abilities, and promote collaborative learning. Interactive seminars, interesting tutorials, and exercises encourage active participation, allowing students to hone their critical thinking skills and apply engineering mathematics principles to problem solving. Furthermore, students collaborate on engineering mathematics issues, examine real-world scenarios, and explore the practical applications of the principles acquired through group activities, projects, and conversations. This method not only increases</p>

	students' comprehension of engineering mathematical concepts, but it also fosters cooperation, communication, and key interpersonal skills that will be useful in their future engineering activities.
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Student Workload (SWL)			
الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	62	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	88	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	5.9
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation					
تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	30% (30)	5 and 10	LO #1 #2 #3 and #4, #7, #8
	<b>Assignments</b>	1	10% (10)	12	LO #7 - #9
	<b>Projects / Lab.</b>	-	-	-	-
	<b>Report</b>	-	-	-	-
<b>Summative assessment</b>	<b>Midterm Exam</b>	2hr	10% (10)	7	LO #1 - #6
	<b>Final Exam</b>	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
<b>Week 1</b>	Introduction – Differentiation definitions and notations, review of basic differentiation rules, chain rule, and Implicit differentiation.

<b>Week 2</b>	Partial differentiation and higher order differentiation.
<b>Week 3</b>	Differentiation of trigonometric functions and Hyperbolic Functions. Applications of differentiation; slope, tangents and normal.
<b>Week 4</b>	Rate of change, velocity and acceleration, maxima and minima, inflexion points and Curve plotting
<b>Week 5</b>	Transcendental Functions – definitions, properties, and graphs, derivative.
<b>Week 6</b>	Definitions and notations of integration, Types of integrals: definite integrals and infinite integrals. Integration of trigonometric function.
<b>Week 7</b>	Integration of inverse trigonometric function, hyperbolic function. <a href="#">Mid-term Exam</a>
<b>Week 8</b>	Integration of inverse trigonometric function, hyperbolic function, natural logarithm, exponential function, and general exponential function.
<b>Week 9</b>	Application of the definite integral – areas between curves, volumes of revolution, length of the curve and surface area of revolution.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	Methods Of Integration – Trigonometric Substitutions, Quadratics, Partial fractions, Integration by parts, and Further Substitutions.
<b>Week 13</b>	
<b>Week 14</b>	
<b>Week 15</b>	

<b>Learning and Teaching Resources</b> <b>مصادر التعلم والتدريس</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	G. B. Thomas Jr., M. D. Weir, J. Hass, and F. R. Giordano, "Thomas' Calculus," 12th ed., Pearson, 2019.	Yes
<b>Recommended Texts</b>		
<b>Websites</b>	<a href="https://www.coursera.org/learn/introduction-to-calculus#syllabus">https://www.coursera.org/learn/introduction-to-calculus#syllabus</a> <a href="https://www.edx.org/learn/calculus">https://www.edx.org/learn/calculus</a> <a href="https://www.khanacademy.org/math/calculus-1">https://www.khanacademy.org/math/calculus-1</a>	

Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information					
Module Title	Physical Electronics		Module Delivery		
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	NVEE218				
ECTS Credits	6				
SWL (hr/sem)	150				
Module Level		1	Semester of Delivery		1
Administering Department		Electronic	College	Ninevah university	
Module Leader	Hamsa Fawaz Thanoon		e-mail	hamsa.thanoon@uoninevah.edu.iq	
Module Leader’s Acad. Title		Lecturer	Module Leader’s Qualification		M.Sc
Module Tutor			e-mail	E-mail	
Peer Reviewer Name			e-mail	E-mail	
Scientific Committee Approval Date		04/07/2023	Version Number		1.0

### Relation with other Modules

Prerequisite module		Semester	
Co-requisites module	None	Semester	

### Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ol style="list-style-type: none"> <li>1. To develop problem solving skills and understanding of Atomic Structure</li> <li>2. To understand Energy band structure of metal, insulator, and semiconductor.</li> <li>3. To understand Properties of intrinsic P and N type semiconductors.</li> <li>4. To understand Electrical conduction in intrinsic semiconductor.</li> <li>5. To understand Properties of extrinsic semiconductors.</li> <li>6. To understand Electrical conduction in extrinsic semiconductor</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Recognize how semiconductors works in electronics circuits.</li> <li>2. List the various terms associated with electronics circuits.</li> <li>3. Summarize what is meant by a basic of semiconductors.</li> <li>4. Discuss the reaction and involvement of semiconductors in generate the currents.</li> <li>5. Describe mobility of electrons and conductivity in metals.</li> <li>6. Define Ohm's law.</li> <li>7. Identify the pure semiconductors.</li> <li>8. Identify the impure semiconductors</li> <li>9. Discuss the impure semiconductors N and P types</li> <li>10. Explain the type of electronic emission.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A - Energy Bands in Solids</u>  <u>Describe the structure of an atom ♦ Discuss insulators, conductors, and semiconductors and how they differ. [9 hrs]</u></p> <p><u>Revision problem classes [3 hrs]</u></p> <p><u>Part B - Transport Phenomena in Semiconductor</u>  <u>Describe how current is produced in a semiconductor ♦ Describe the properties of n-type and p-type semiconductors. [30 hrs]</u></p>

Learning and Teaching Strategies	
<b>Strategies</b>	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills.

Student Workload (SWL)			
Structured SWL (h/sem)	111	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	65	Unstructured SWL (h/w)	2
Total SWL (h/sem)	176		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10	[2,4,5,6]	LO (#1- #12)
	Assignments	2	10	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
Summative assessment	Midterm Exam	1.5 hr	20% (20)	10	LO #(1-8)
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Energy Bands in Solids
Week 2	Fermi-Dirac distribution function
Week 3	Properties of intrinsic P and N type semiconductors
Week 4	Mobility and conductivity
Week 5	Electrical conduction in intrinsic semiconductor
Week 6	Hall Effect
Week 7	Generation and recombination of charges
Week 8	Diffusion current continuity equation
Week 9	Injection minority carrier charges
Week 10	N-type semiconductor
Week 11	Solved problems
Week 12	P-type semiconductor
Week 13	Solved problems
Week 14	Photo-conductivity
Week 15	Preparatory week before the final Exam

Learning and Teaching Resources		
	Text	Available in the Library?
Required Texts	1. "INTEGRATED ELECTRONICS" By MILLMAN & HALKIES 2. "SEMICONDUCTOR DEVICES & CIRCUITS" , JOHN WILEY & SONS	Yes
Recommended Texts	1. (Floyd) 2. تیراجا فصل 51	Yes

Grading Scheme				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information					
Module Title	Mechanical Engineering Principle			Module Delivery	
Module Type	<u>Base</u>			<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE203				
ECTS Credits	<u>6</u>				
SWL (hr/sem)	<u>150</u>				
Module Level		1	Semester of Delivery		1
Administering Department		Type Dept. Code	College	Type College Code	
Module Leader			e-mail		
Module Leader’s Acad. Title		Lecturer	Module Leader’s Qualification		M.Sc.
Module Tutor			e-mail		
Peer Reviewer Name		Name	e-mail	E-mail	
Scientific Committee Approval Date		2/07/2023	Version Number		1.0



## Relation with other Modules

<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<p style="text-align: right;">Students will be able to:</p> <ol style="list-style-type: none"> <li>Students will be able to:</li> <li>Knowing the different methods of making calculations related to forces and their effects on two- and three-dimensional systems</li> <li>Clarify that the subject represents a very important introduction to other subjects for the later stages of the student's study and building a scientific base for the student to ensure the possibility of understanding the relevant subjects in the later stages.</li> <li>The student will learn different applications of commonly used Mechanical machinery.</li> <li>The student will learn strong basics of Mechanical Engineering fundamentals.</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>Have understood and overcome any misconceptions about basic concepts in physics (force, energy, work etc).</li> <li>Restate existing problem solving skills in a form more suitable for engineering applications.</li> <li>Interpret basic engineering applications of mechanics in more detail.</li> <li>Acquire four basic thinking skills: <ol style="list-style-type: none"> <li>Perceive, or resolve, contradictions involving their preconceptions about mechanics.</li> <li>Organize the basic ideas of mechanics in a form suitable for problem solving.</li> <li>Apply basic principles in mechanics to realistic engineering situations.</li> <li>Solve realistic engineering problems.</li> </ol> </li> </ol>
<b>Indicative Contents</b>	<p style="text-align: right;"><b>Indicative content includes the following:-</b></p> <p style="text-align: right;"><b>Statics – Introduction [25 hrs]</b></p> <ul style="list-style-type: none"> <li>○ Vectors</li> <li>○ Newton's Laws</li> <li>○ Fundamental Units</li> <li>○ Types of force</li> <li>○ Parallelogram law</li> </ul>

	<ul style="list-style-type: none"> <li>○ Resultant forces</li> <li>○ Moments and couples</li> <li>○ Moment of couples</li> <li>○ Equilibriums</li> <li>○ Free body diagram</li> <li>○ Coplanar system</li> <li>○ Friction: Nature of friction; Theory of friction; Coefficient of friction</li> </ul> <p style="text-align: right;"><b>Dynamics – Introduction [20 hrs]</b></p> <ul style="list-style-type: none"> <li>○ Basic concepts</li> <li>○ Newton's Laws</li> <li>○ Formulation and solution of problems</li> <li>○ Kinematics of Particles</li> <li>○ Rectilinear motion</li> <li>○ Curvilinear motion</li> <li>○ Relative motion</li> <li>○ Kinetics of Particles</li> <li>○ Newton's second Law</li> <li>○ Work and energy</li> <li>-</li> </ul>
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### Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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### Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	25	<b>Structured SWL (h/w)</b>	2
<b>Unstructured SWL (h/sem)</b>	20	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	45		

### Module Evaluation

As	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
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<b>Formative assessment</b>	<b>Quizzes</b>	6	5% (5)	, 5, 2 ,12,13,159	LO #1, 2, 10 and 11
	<b>Assignments</b>	6	5% (5)	, 5, 2 ,12,13,159	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	0	0%		
	<b>Report</b>	0	0%	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	3hr	30% (30)	10	LO # 1-7
	<b>Final Exam</b>	3hr	60% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

Week	Material Covered
Week 1	Force system; Units system;
Week 2	Parallelogram law; Forces + components
Week 3	Resultant of coplanar forces
Week 4	Components of force in space
Week 5	Moment of a force
Week 6	Moment of a force
Week 7	Moment of a force
Week 8	Free body diagram; Coplanar system
Week 9	Friction: Nature of friction; Theory of friction
Week 10	Coefficient of friction
Week 11	Coefficient of friction
Week 12	Coefficient of friction
Week 13	Normal and tangential components of acceleration
Week 14	Normal and tangential components of acceleration
Week 15	
Week 16	Normal and tangential components of acceleration

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	1. Engineering Mechanics (statics) By: J.L. MERIAM 2. Engineering Mechanics (Dynamics) By: J.L. MERIAM	Yes
<b>Recommended Texts</b>	➤	No
<b>Websites</b>		

## Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Module Information

### معلومات المادة الدراسية

Module Title	Democracy and Human Rights			Module Delivery	
Module Type	Basic			<div><input checked="" type="checkbox"/> Theory</div> <div><input type="checkbox"/> Lecture</div> <div><input type="checkbox"/> Lab</div> <div><input type="checkbox"/> Tutorial</div> <div><input type="checkbox"/> Practical</div> <div><input type="checkbox"/> Seminar</div>	
Module Code	NV12				
ECTS Credits	2				
SWL (hr/sem)	50				
Module Level		1	Semester of Delivery		
Administering Department		Dept. of Electronic	College	EE	
Module Leader	Husham swadi hashim		e-mail	<a href="mailto:Husham.hashim@uoninevah.edu.iq">Husham.hashim@uoninevah.edu.iq</a>	
Module Leader’s Acad. Title		Assistant Professor	Module Leader’s Qualification		PHD
Module Tutor			e-mail		
Peer Reviewer Name			e-mail		
Scientific Committee Approval Date		01/06/2023	Version Number	1.0	

## Relation with other Modules

### العلاقة مع المواد الدراسية الأخرى

Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

### أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<b>Module Aims</b> أهداف المادة الدراسية	1 - شرح مفهومي حقوق الإنسان والديمقراطية 2 - بيان أهمية حقوق الإنسان في حياتنا العامة وعلى جميع الصعد ( الدراسية و الوظيفية و الاجتماعية .. الخ ) 3 - بيان أهمية إيجاد مفهوم واعي لمصطلح الديمقراطية ضمن أنظمة الحكم وتأثيرها على الاستقرار السياسي 4 - ضرورة فهم الترابط الوثيق مابين حقوق وبناء مجتمع ديمقراطي يضمن حرية افرادة وضمان مصالحهم 5- ضرورة التركيز على ان بناء مفهوم حقيقي لحقوق الانسان ومجتمع ديمقراطي لا يكون الا من خلال ين قوانين تضمن ذلك واهمية هذه القوانين في بناء مجتمع مستقر يضمن لجميع افرادة حقوقهم ضمن نظام سياسي ديمقراطي
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	1 - ترسيخ قيم الحرية والمساواة في اسس المشاركة الفعلية في بناء المجتمع 2 - العمل على بناء بيئة حقيقية مستقرة من خلال تطبيق القوانين ضمن مجتمع ديمقراطي 3 - والسعي لتوفير اسس لحماية الافراد ضمن المجتمعات الديمقراطية
<b>Indicative Contents</b> المحتويات الإرشادية	-القسم الأول:- التطور التاريخي لحقوق الإنسان أولاً:- المجتمعات البدائية - مرحلة ما قبل التاريخ - الحضارات الشرقية (بلاد وادي الرافدين والحضارة الفرعونية نموذجاً) ) نموذجاً - الحضارات الغربية (اليونانية والرومانية ثانياً:- الشرائع السماوية - الديانة اليهودية - الديانة المسيحية - الديانة الإسلامية(بصوره أكثر تفصيلاً) ثالثاً:- تطور حقوق الانسان في القوانين الوضعية نظرية العقد الاجتماعي - الحروب العالمية وأثرها في حقوق الانسان - التنظيم الدولي القسم الثاني :- حقوق الإنسان التعريف بها وأنواعها

تعريف حقوق	أولاً- التحديد والتعريف
	- الحق في الفقه الإسلامي
	- الحق في الفقه
	القانوني
	-
	الإنسان
	ثانياً- تقسيمات حقوق الإنسان (وتتم بدراسة مفصلة ومقارنة بين القانون والشرعية الإسلامية)
	الحقوق الجماعية(حق تقرير المصير، حق التنمية، الحق في بيئة مناسبة، حق الإنسان في العيش بسلام)-
	الحقوق الفردية (الحقوق الاقتصادية والثقافية، الحقوق المدنية والسياسية الحقوق الصيغة بالشخصية)-
	القسم الثالث:- ضمانات احترام وحماية حقوق
	الإنسان
	أولاً - الضمانات في الشريعة الإسلامية
	ثانياً:- الضمانات على الصعيد الوطني
	ثالثاً:- الضمانات على الصعيد الدولي
	مفردات مادة الديمقراطية
	الكورس الأول:- يتضمن مادة الحريات العامة بين الشريعة والقانون
	الكورس الثاني:- يتضمن مادة نظم إدارة الدولة بين الشريعة والقانون
	الحريات العامة (بين الشريعة والقانون )
	أولاً:- المقدمة
	ثانياً:- التعريف بالحريات العامة
	- الأصل اللغوي
	- الأصل التاريخي
	- الأساس القانوني
	- الأساس الشرعي
	ثالثاً:- أسس الحريات العامة
	- العدالة
	- المساواة
	- الحرية
	رابعاً:- الحريات العامة الو صفية
	- حرية الرأي
	- حرية الفكر
	- حرية الأعلام
	- المساواه

	خامساً:- الشريعة الإسلامية والحريات العامة
	- موقف الإسلام من المرأة (الميراث، الزواج، تولي الوظائف)
	- موقف الإسلام من حرية العقيدة
	نظم إدارة الدولة
	أولاً:- في تحديد النظم السياسية
	- فكره النظام السياسي
	- شرعية النظم السياسية
	- أنواع النظم السياسية
	ثانياً:- في النظام الديمقراطي
	- مقدمة تأصيلية
	- تعريف الديمقراطية
	- أركان ومركزات النظام الديمقراطي
	ثالثاً:- نماذج الديمقراطية
	- الديمقراطية المباشرة

Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	اتباع طريقة التعليم المباشر من خلال عرض المادة وشرحها والاستعانة بالادوات التعليمية لشرحها من خلال توضيح اليات المفهوم العلمي لمصطلحي الديمقراطية و حقوق الانسان

Student Workload (SWL) الحمل الدراسي للطلاب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطلاب خلال الفصل	16	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطلاب أسبوعياً	1
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطلاب خلال الفصل	9	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطلاب أسبوعياً	0.5
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطلاب خلال الفصل	25		

Module Evaluation تقييم المادة الدراسية
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		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Report	1	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	2hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
Week 1	التطور التاريخي لحقوق الانسان
Week 2	الشرائع السماوية
Week 3	تطور حقوق الانسان في القوانين الوضعية
Week 4	حقوق الانسان التعريف بها وانواعها
Week 5	ضمانات احترام وحماية حقوق الانسان
Week 6	الضمانات في الشريعة وعلى الصعيدين الوطني والدولي
Week 7	Mid-term Exam
Week 8	مفهوم الديمقراطية
Week 9	الحريات العامة بين الشريعة و القانون
Week 10	التعريف بالحريات العامة و اسس الحريات
Week 11	الشريعة الاسلامية والحريات العامة
Week 12	نظم ادارة الدولة
Week 13	الديمقراطية مقدمة تأصيلية
Week 14	اركان ومرتكزات النظام الديمقراطي
Week 15	نماذج الديمقراطية
Week 16	Preparatory week before the final Exam

Learning and Teaching Resources
مصادر التعلم والتدريس



	Text	Available in the Library?
Required Texts		Yes
Recommended Texts		No
Websites		

Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

## Courses specification for first class (Second Course)

Module Information					
Module Title	Digital Techniques		Module Delivery		
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	NVEE217				
ECTS Credits	5				
SWL (hr/sem)	125				
Module Level		1	Semester of Delivery		2
Administering Department			College	Type College Code	
Module Leader	(Younis Saber Othman), (Noor Alhuda Saad Abbas)		e-mail		
Module Leader's Acad. Title		Lecturer Assistant	Module Leader's Qualification		M.Sc.
Module Tutor			e-mail		
Peer Reviewer Name		Name	e-mail	E-mail	
Scientific Committee Approval Date		4/7/2023	Version Number	1.0	

Relation with other Modules				
Prerequisite module	None		Semester	
Co-requisites module	None		Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ol style="list-style-type: none"> <li>1. To learn new number systems and how to convert between them</li> <li>2. To identify and learn the logic gates and Boolean algebra</li> <li>3. How to minimize the Boolean functions using Boolean algebra and Karnaugh maps</li> <li>4. To understand, draw, and identify the combinational logic circuits using the discrete logic</li> <li>5. To understand, draw, and identify the combinational logic circuits using the MSI integrated circuits</li> <li>6. To use the 3-variables and 4-variables Karnaugh map for Boolean minimization</li> </ol>
<b>Module Learning Outcomes</b>	<p>Students will be able to:</p> <ol style="list-style-type: none"> <li>1. Learning new number systems and how to convert between them</li> <li>2. Identify the logic gates and learn the Boolean algebra</li> <li>3. Minimize the Boolean functions</li> <li>4. Understand, draw, and identify the combinational logic circuits using the discrete logic and MSI integrated circuits</li> <li>5. Identify and use the 3-variables and 4-variables Karnaugh map</li> </ol>
<b>Indicative Contents</b>	<p><b>Indicative content includes the following:-</b></p> <p><b>NUMBER SYSTEMS:- [10 Hrs]</b>            Decimal number system; Binary; Octal and hexadecimal number systems; Conversion from one number to another number system; Addition; Subtraction; Multiplication and division using different number system; Representation of binary number insignia-magnitude; Sign 1's Complement and align 2's complement notation; Rules for addition and subtraction with complement Representation; BCD; EBCDIC; ASCII; Extended ASCII; Gray and other codes.</p> <p><b>LOGIC GATES AND BOOLEAN ALGEBRA:- [10 Hrs]</b>            AND; OR; NOT; NAND; NOR; Ex-OR logic gates; Positive and negative logic; Fundamental concepts of Boolean algebra; De-murrage's laws; Principles of duality; Simplification of Boolean expressions; Canonical and standard forms for Boolean function; SOP and POS, forms; Realization of Boolean functions using only NAND and NOR gates.</p> <p><b>BOOLEAN FUNCTION MINIMIZATION:- [10 Hrs]</b>            Objectives of the minimization procedures; Karnaugh map method; The 3-Variable Karnaugh Map; The 4-Variable Karnaugh Map; Karnaugh Map SOP Minimization; Don't care conditions; Karnaugh Map POS Minimization; Converting Between POS and SOP Using the Karnaugh Map.</p>

	<p><b>COMBINATIONAL LOGIC CIRCUITS USING DISCRETE LOGIC GATES:- [5 Hrs]</b> Parity generator and checker; Code converters; Majority circuits; magnitude comparator.</p> <p><b>COMBINATIONAL LOGIC CIRCUIT USING MSI INTEGRATED CIRCUITS:- [10 Hrs]</b> Encoder; priority encoder; decoder; Multiplexer and demultiplexer circuits; Implementation of Boolean functions using decoder and Multiplexer; BCD to 7-segment decoder; Common anode and common cathode 7-segment displays; Random access memory; Read only memory and erasable programmable ROMS</p>
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Learning and Teaching Strategies	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

Student Workload (SWL)					
Structured SWL (h/sem)		45	Structured SWL (h/w)		4
Unstructured SWL (h/sem)		45	Unstructured SWL (h/w)		4
Total SWL (h/sem)		90			
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	1-14	LO #1-14
	Assignments	1	5% (5)	6	LO # 1-6
	Projects / Lab.	10 Lab	10% (10)	5-14	LO # 5-14
	Report	3	5% (5)	5-14	LO # 5-14
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-10
	Final Exam	2hr	50% (50)	16	All

Total assessment	100% (100 Marks)		
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Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	<b>NUMBER SYSTEMS:-</b> Decimal number system; Binary; Octal and hexadecimal number systems; Conversion from one number to another number system; Addition; Subtraction; Multiplication and division using different number system; Representation of binary number insignia-magnitude; Sign 1's Complement and align 2's complement notation; Rules for addition and subtraction with complement Representation; BCD; EBCDIC; ASCII; Extended ASCII; Gray and other codes.
Week 2	
Week 3	
Week 4	<b>LOGIC GATES AND BOOLEAN ALGEBRA:-</b> AND; OR; NOT; NAND; NOR; Ex-OR logic gates; Positive and negative logic; Fundamental concepts of Boolean algebra; De-murrage's laws; Principles of duality; Simplification of Boolean expressions; Canonical and standard forms for Boolean function; SOP and POS, forms; Realization of Boolean functions using only NAND and NOR gates.
Week 5	
Week 6	
Week 7	<b>BOOLEAN FUNCTION MINIMIZATION:-</b> Objectives of the minimization procedures; Karnaugh map method; The 3-Variable Karnaugh Map; The 4-Variable Karnaugh Map; Karnaugh Map SOP Minimization; Don't care conditions; Karnaugh Map POS Minimization; Converting Between POS and SOP Using the Karnaugh Map.
Week 8	
Week 9	
Week 10	
Week 11	<b>COMBINATIONAL LOGIC CIRCUITS USING DISCRETE LOGIC GATES:-</b> Parity generator and checker; Code converters; Majority circuits; magnitude comparator.
Week 12	
Week 13	<b>COMBINATIONAL LOGIC CIRCUIT USING MSI INTEGRATED CIRCUITS:-</b> Encoder; priority encoder; decoder; Multiplexer and demultiplexer circuits; Implementation of Boolean functions using decoder and Multiplexer; BCD to 7-segment decoder; Common anode and common cathode 7-segment displays; Random access memory; Read only memory and erasable programmable ROMS
Week 14	
Week 15	
Week 16	Preparatory week before the final Exam

Delivery Plan (Weekly Lab. Syllabus)
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	Material Covered
<b>Week 5-14</b>	Introduction to KL-31001 DIGITAL LOGIC LAB Exp. 1: Logic Gates Exp. 2: NAND, NOR, XOR Gates Exp. 3: AND-OR-INVERTER(A-O-I) Circuits Exp. 4: Bit Parity Generator Circuits Exp. 5: Comparator Circuits Exp. 6: Decoder Exp. 7: Encoder Exp. 8: Multiplexer Exp. 9: Demultiplexer

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	<b>Digital Fundamentals</b>   Eleventh Edition Global Edition   by Thomas L. Floyd   Pearson Education 2015	PDF
<b>Recommended Texts</b>	<b>Logic and Computer Design Fundamentals</b>   Fifth Edition Global Edition   by Morris Mano • Charles R. Kime • Tom Martin   Pearson Education 2016	PDF
<b>Websites</b>	(Telegram Group and Google classroom)	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information				
Module Title	A.C circuits Analysis		Module Delivery	
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE216			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	1	Semester of Delivery		2
Administering Department	Type Dept. Code	College	Type College Code	
Module Leader	Zahraa Siddiq Yahya		e-mail	
Module Leader's Acad. Title	Lecturer assistant	Module Leader's Qualification		
Module Tutor	Zahraa Siddiq Yahya		e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail	
Scientific Committee Approval Date	01/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module		Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<p>12. To identify the basic concepts of energy storage elements.</p> <p>13. To identify the basic of Alternating Current AC.</p> <p>14. To understand and cover the basic AC circuit analysis methods and theorems.</p>
<b>Module Learning Outcomes</b>	<p>11. Explain the function of each element in AC Electrical circuits.</p> <p>12. Use the basic circuit analysis methods to simplified the AC Electrical circuits.</p> <p>13. Applying the appropriate analysis method to reach the aim in its simplest form.</p>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A – energy storage elements:</u></p> <p>The capacitor; The Inductor; Analysis of RC-transient circuits; Analysis of RL-transient circuits; RLC transient circuits. [15 hrs]</p> <p><u>Part B - A.C. circuit analysis:</u></p> <p>the basic of Alternating Current AC; The Phasor equivalent circuit; series &amp; parallel connections and equivalent impedance; Methods of Ac-circuit Analysis; superposition;</p> <p>Nodal &amp; Mesh analysis; Thevenin's Theorem; Norton's Theorem; Power factor and average power in the sinusoidal Ac-circuits; Complex power; Series &amp; parallel resonance. [35 hrs]</p>

## Learning and Teaching Strategies



<b>Strategies</b>	Type something like: The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering types of simple experiments involving some sampling activities that are interesting to the students.
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<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>	30	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	30	<b>Unstructured SWL (h/w)</b>	4
<b>Total SWL (h/sem)</b>	<b>60</b>		

<b>Module Evaluation</b>					
		<b>Time/Number</b>	<b>Weight (Marks)</b>	<b>Week Due</b>	<b>Relevant Learning Outcome</b>
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	<b>Assignments</b>	1	10% (10)	14	LO #4, #7, #(10-13)
	<b>Projects / Lab.</b>	0	0% (0)		
	<b>Report</b>	1	10% (10)	12	LO #11
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5 hr	10% (10)	10	LO #(1-8)
	<b>Final Exam</b>	3 hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

	Material Covered
<b>Week 1</b>	The capacitor & The inductor
<b>Week 2</b>	Analysis of RC & RL -transient circuits
<b>Week 3</b>	Analysis of RLC transient circuits
<b>Week 4</b>	The basic of Alternating Current AC
<b>Week 5</b>	The Phasor equivalent circuit
<b>Week 6</b>	series & parallel connections and equivalent impedance
<b>Week 7</b>	Methods of Ac-circuit Analysis
<b>Week 8</b>	superposition
<b>Week 9</b>	Nodal & Mesh analysis
<b>Week 10</b>	Thevenin's Theorem
<b>Week 11</b>	Norton's Theorem
<b>Week 12</b>	Power factor and average power in the sinusoidal Ac-circuits
<b>Week 13</b>	Complex power
<b>Week 14</b>	Series & parallel resonance
<b>Week 15</b>	<b>Preparatory week before the final Exam</b>

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	" Engineering Circuit Analysis" By W. Hayt	Yes
<b>Recommended Texts</b>	"Introductory Circuit Analysis" By Boylested	Yes

### Grading Scheme

Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information				
معلومات المادة الدراسية				
Module Title	Engineering Drawing		Module Delivery	
Module Type	Core		<input type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE201			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	1	Semester of Delivery		
Administering Department		College		
Module Leader	Noor Yassar		e-mail	
Module Leader's Acad. Title		Module Leader's Qualification		
Module Tutor	Name (if available)		e-mail	E-mail
Peer Reviewer Name	Name		e-mail	E-mail
Scientific Committee Approval Date	01/06/2023		Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	<p>Students will be able to:</p> <ol style="list-style-type: none"> <li>6. Drawing engineering shapes manually and clearly, including the effective use of the computer-aided drawing program (AutoCAD).</li> <li>7. Develop a solid understanding of the basic principles of engineering drawing, Included the ability to work with concepts, analytically, and visualize them and a functional understanding of how these ideas will manifest in the real world.</li> <li>8. Determine the strategies to be used and the assumptions to be made.</li> <li>9. Use both manual and computer approaches in drawing figures.</li> <li>10. Develop the ability to use engineering tools flexibly and creatively.</li> <li>11. Develop an integrated understanding of the AutoCAD module.</li> <li>12. Developing their ability to communicate scientific ideas.</li> <li>13. Develop expertise in experimental methodologies.</li> </ol>
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	<ol style="list-style-type: none"> <li>14. Understand and apply the basics of drawing types of lines.</li> <li>15. Define, explain and apply engineering drawing operations.</li> <li>16. Understand the basics of drawing an ogee curves</li> <li>17. Understand and apply the basic idea of central projection theory.</li> <li>18. Explanation of the central and parallel projection theory to understand the projection process.</li> <li>19. Explain Different Views are Front View (FV), Top View (TV) and Side View (SV) FV is a view projected on VP. TV is a view projected on HP. SV is a view projected on PP.</li> <li>20. Ability to draw using AutoCAD.</li> </ol>
<b>Indicative Contents</b> المحتويات الإرشادية	<p><b>Introduction to engineering drawing and its tools</b>  Introduction and introducing students to the subject of engineering drawing, which includes Identification of engineering tools and how to use them.</p> <p><b>Engineering shapes and the arcs , lamina. , Dimensions:</b></p> <ul style="list-style-type: none"> <li>- Various engineering operations: -</li> <li>- Drawing a straight line parallel to a known straight line</li> <li>- The division of the rectum into two halves</li> <li>- Angle division is known.</li> <li>- Drawing a straight line parallel to a known straight line from a point that does not belong to the known straight line.</li> <li>- Draw a tangent to a circle from a point that does not belong to it.</li> </ul>

	<ul style="list-style-type: none"> <li>- Draw a tangent to two contiguous circles from the outside.</li> <li>- Draw a tangent to two contiguous circles from the inside</li> </ul> <p><b>Multi view projection</b></p> <ul style="list-style-type: none"> <li>- Perpendicular Projection Theory of Objects:</li> <li>- Types of projections resulting from vertical projection and approved in the projection of various engineering objects</li> <li>- Front view</li> <li>- Side view.</li> <li>- Top view</li> </ul> <p><b>Using AutoCAD</b></p> <ul style="list-style-type: none"> <li>- Apply everything that has been explained in the manual engineering drawing on the AutoCAD program and drawing the three-dimensional models</li> </ul>
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<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b> الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	45	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	3
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	55	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	3.7
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	100		

<b>Module Evaluation</b> تقييم المادة الدراسية
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		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	20% (20)	5 and 10	LO #1, 2, 10 and 11
	Assignments	3	10% (10)	2 and 12	LO # 3, 4, 6 and 7
	Projects / Lab.	3	10% (10)	Continuous	All
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	2hr	10% (10)	7	LO # 1-4
	Final Exam	3hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

#### المنهاج الاسبوعي النظري

	Material Covered
Week 1	Introduction and introducing students to the subject of engineering drawing, which includes identification of engineering tools and how to use them.
Week 2	Teach students how to apply and draw the following engineering operations: Drawing a straight line parallel to a known straight line, the division of the rectum into two halves, angle division and drawing a straight line parallel to a known straight line.
Week 3	Teach students how to draw a tangent to two contiguous circles from the outside, Draw a tangent to two contiguous circles from the inside
Week 4	Draw a tangent to one circle from the inside and the other from the outside and draw a tangent to a circle passing through a straight line.
Week 5	Multi view projection Perpendicular Projection Theory of Objects: • Types of projection in drawing and its practical importance
Week 6	Types of projections resulting from vertical projection and approved in the projection of various engineering objects: Front view, Side view ,Top view
Week 7	Mid-term Exam + Introduction to AutoCAD
Week 8	Apply everything that has been explained in the manual engineering drawing on the AutoCAD program and drawing the three-dimensional models
Week 9	
Week 10	
Week 11	
Week 12	
Week 13	

Week 14	
Week 15	Preparatory week before the final Exam

<b>Delivery Plan (Weekly Lab. Syllabus)</b> المناهج الاسبوعي للمختبر	
	Material Covered
Week 1-15	The application of each part of the covered drawing subject theoretically and according to the weekly sequence of the curriculum in the AutoCAD laboratory

<b>Learning and Teaching Resources</b> مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	ENGINEERING DRAWING AND GRAPHIC TECHNOLOGY", Fourteenth Edition, By: THOMAS E.FRENCH, CHARLES .VIERCK, ROBERT J.FOSTER,McGRAW-HILL	Yes
Recommended Texts	➤ William D.CallisterJr.&David D.Rethwisch.(2010)"Material Science and Engineering An introduction", eightEdition.	No
Websites	ENGINEERING DRAWING Any edition	

<b>Grading Scheme</b> مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
معلومات المادة الدراسية			
Module Title	MathematicsII		Module Delivery
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE 207		
ECTS Credits	6		
SWL (hr/sem)	150		
Module Level	1	Semester of Delivery	
Administering Department	Electronic Eng. Dep.	College	Electronics Engineering
Module Leader	Hani M. S. Salman	e-mail	hani.mohamed@uoninevah.edu.iq
Module Leader's Acad. Title	Assistant Lecturer	Module Leader's Qualification	MSc
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date		Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	NEEM1211	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	1. To obtain a good knowledge of dealing with complex numbers.



	<ol style="list-style-type: none"> <li>2. Establish a strong foundation in matrices and their operations, determinants, and inverse matrices. This includes covering definitions, notations, properties, types, and basic operations on matrices, enabling effective application in problem-solving.</li> <li>3. enhancing students' proficiency in matrix-based solutions for linear systems of equations using Cramer's rule, the inverse method, and the Gauss elimination method</li> <li>4. To provide the students with the knowledge to deal with vectors and their mathematical operations.</li> <li>5. To Learn about the polar coordinates, and the graphs of polar equations.</li> <li>6. Apply calculus principles to solve real-world engineering problems, developing problem-solving skills and the ability to apply calculus concepts to practical situations.</li> </ol>
<p><b>Module Learning Outcomes</b></p> <p>مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"> <li>21. Comprehend and utilize complex numbers within the Argand diagram, and</li> <li>22. master complex number operations (Addition, subtraction, product, quotient, power, and roots) and De Moivre's Theorem.</li> <li>23. Understand the concept of linear algebra and matrices.</li> <li>24. Identify the types of matrices such as square matrices, zero matrix and identity.</li> <li>25. Perform the common matrix operations such as addition, subtraction, scalar multiplication, and multiplication.</li> <li>26. Find the transpose of a matrix.</li> <li>27. Compute the determinants.</li> <li>28. Compute the inverse of the matrix.</li> <li>29. Identify whether the matrix is invertible or singular.</li> <li>30. Relate a matrix to a homogenous system of linear equation.</li> <li>31. Solve a system of linear equations by matrices: using Cramer's rule.</li> <li>32. Solve a system of linear equations by matrices: using the inverse method.</li> <li>33. Solve a system of linear equations by matrices: using Gauss Elimination Method.</li> <li>34. Identify the rank of the matrix and its relation to the solution of linear equations.</li> <li>35. Find the eigenvalues and eigenvectors of a matrix.</li> <li>36. Represent a vector in space.</li> <li>37. Compute dot and cross products in vectors.</li> <li>38. Understand the meaning of del operator, gradient, divergence, and curl and to compute the del operation, gradient, divergence, and curl.</li> <li>39. Learn about the vector functions.</li> <li>40. Convert from Cartesian to Polar coordinates and vice versa.</li> <li>41. Sketch in polar system.</li> <li>42. Utilize mathematical reasoning and critical thinking skills to analyze and interpret mathematical concepts and their applications in Electronics</li> </ol>

	<p>engineering.</p> <p>43. Develop proficiency in mathematical problem-solving, both independently and collaboratively, and communicate solutions effectively.</p>
<p><b>Indicative Contents</b></p> <p>المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p><u>Part A – Review of Complex Numbers:</u></p> <p>The Argand diagram, Addition, Subtraction; Product, Quotient, power and roots, and Demoiver’s Theorem. [4hrs]</p> <p><u>Part B – Matrices and Determinants:</u></p> <p>Matrices and Determinants: Definitions and notations, Properties, types of matrices, basic operations on matrices, computation of the determinants of matrices, properties of determinants. [8 hrs]</p> <p>Inverse of the Matrices. [4 hrs]</p> <p>Solution of the system of linear equations-solution of the system of linear equation using Cramer’s rule, solution of the system of linear equation using the inverse method. [12 hrs]</p> <p>Revision problem classes [4 hrs]</p> <p>solution of the system of linear equation using Gauss Elimination Method. [4 hrs]</p> <p>Eigenvalues and eigenvector. [4 hrs]</p> <p><u>Part C – Review of Vectors:</u></p> <p>Representation of vectors in space (i;j;k), unit vectors, Scalar product, and Vector product. [8 hrs]</p> <p><u>Part D – Vector Calculus:</u></p> <p>Vectors – del operator, Parametric Equations of Lines in Space, the distance from a Point to a line in Space, plane equation in space, the Distance from the Point to a Plane, Angles Between Planes, vector function versus Scalar function, del operator, Gradient, Divergence and Curl. [12 hrs]</p> <p><u>Part E – Polar Coordinates:</u></p> <p>Polar coordinates – polar coordinate system, transformation between polar and Cartesian coordinates, graphs of polar equations. [4 hrs]</p>

### Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

<b>Strategies</b>	This module's major aim is to foster student engagement, improve critical thinking abilities, and promote collaborative learning. Interactive seminars, interesting tutorials, and exercises encourage active participation, allowing students to hone their critical thinking skills and apply engineering mathematics principles to problem solving. Furthermore, students collaborate on engineering mathematics issues, examine real-world scenarios, and explore the practical applications of the principles acquired through group activities, projects, and conversations. This method not only increases students' comprehension of engineering mathematical concepts, but it also fosters cooperation, communication, and key interpersonal skills that will be useful in their future engineering activities.
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<b>Student Workload (SWL)</b> الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	62	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	88	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	5.9
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>150</b>		

<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	30% (30)	6 and 14	LO #1 - #11, #16- #19
	<b>Assignments</b>	1	10% (10)	13	LO #12-#15
	<b>Projects / Lab.</b>	-	-	-	-
	<b>Report</b>	-	-	-	-
<b>Summative assessment</b>	<b>Midterm Exam</b>	2hr	10% (10)	7	LO #1 - #11
	<b>Final Exam</b>	3hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

### المناهج الاسبوعي النظري

	Material Covered
<b>Week 1</b>	The Argand diagram, Addition, Subtraction; Product, Quotient, power and roots, and Demoiver's Theorem.
<b>Week 2</b>	Matrices and Determinants: Definitions and notations, Properties, types of matrices, basic operations on matrices, computation of the determinants of matrices, properties of determinants.
<b>Week 3</b>	
<b>Week 4</b>	Inverse of the Matrices.
<b>Week 5</b>	Solution of the system of linear equations-solution of the system of linear equation using Cramer's rule.
<b>Week 6</b>	solution of the system of linear equation using the inverse method.
<b>Week 7</b>	solution of the system of linear equation using Gauss Elimination Method.
<b>Week 8</b>	Revision problem classes, Mid-term Exam
<b>Week 9</b>	Eigenvalues and eigenvector. [4 hrs]
<b>Week 10</b>	Representation of vectors in space $(i;j;k)$ , unit vectors, Scalar product, and Vector product.
<b>Week 11</b>	
<b>Week 12</b>	Vectors – del operator, Parametric Equations of Lines in Space, the distance from a Point to a line in Space, plane equation in space, the Distance from the Point to a Plane, Angles Between Planes, vector function versus Scalar function, del operator, Gradient, Divergence and Curl.
<b>Week 13</b>	
<b>Week 14</b>	
<b>Week 15</b>	Polar coordinates – polar coordinate system, transformation between polar and Cartesian coordinates, graphs of polar equations.

## Learning and Teaching Resources

### مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	"Higher Engineering Mathematics", 7 <sup>th</sup> edition by John Bird	No
	G. B. Thomas Jr., M. D. Weir, J. Hass, and F. R. Giordano, "Thomas' Calculus," 12th ed., Pearson, 2019.	Yes
Recommended Texts	<p>"Introduction to Linear Algebra". 4th edition by Strang, Gilbert</p> <p>"Linear Algebra for Everyone". 2020 by Strang, Gilbert Zill, D. G., Wright, W. S., &amp; Cullen, M. R. (2011).</p> <p>Advanced Engineering Mathematics. Jones &amp; Bartlett Publishers.</p>	No
Websites	<a href="https://ocw.mit.edu/courses/18-06-linear-algebra-spring-2010">https://ocw.mit.edu/courses/18-06-linear-algebra-spring-2010</a> <a href="https://www.khanacademy.org/math/linear-algebra">https://www.khanacademy.org/math/linear-algebra</a> <a href="https://www.ohio.edu/mechanical-faculty/williams/html/PDF/MatricesLinearAlgebra.pdf">https://www.ohio.edu/mechanical-faculty/williams/html/PDF/MatricesLinearAlgebra.pdf</a>	

Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information				
Module Title	Physical of semiconductors		Module Delivery	
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE219			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery		
Administering Department	El	College	NE	
Module Leader	Hamsa Fawaz Thanoon		e-mail	hamsa.thanoon@uoninevah.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc	
Module Tutor		e-mail	E-mail	
Peer Reviewer Name		e-mail	E-mail	
Scientific Committee Approval Date	04/07/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module		Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents
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<b>Module Aims</b>	<ol style="list-style-type: none"> <li>1. To develop problem solving skills and understanding of Atomic Structure</li> <li>2. To understand Energy band structure of metal, insulator, and semiconductor.</li> <li>3. To understand Properties of intrinsic P and N type semiconductors.</li> <li>4. To understand Electrical conduction in intrinsic semiconductor.</li> <li>5. To understand Properties of extrinsic semiconductors.</li> <li>6. To understand Electrical conduction in extrinsic semiconductor</li> </ol>
<b>Module Learning Outcomes</b>	<ol style="list-style-type: none"> <li>1. Recognize how semiconductors works in electronics circuits.</li> <li>2. List the various terms associated with electronics circuits.</li> <li>3. Summarize what is meant by a basic of semiconductors.</li> <li>4. Discuss the reaction and involvement of semiconductors in generate the currents.</li> <li>5. Describe mobility of electrons and conductivity in metals.</li> <li>6. Define Ohm's law.</li> <li>7. Identify the pure semiconductors.</li> <li>8. Identify the impure semiconductors</li> <li>9. Discuss the impure semiconductors N and P types</li> <li>10. Explain the type of electronic emission.</li> </ol>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p><u>Part A - Energy Bands in Solids</u>  <u>Describe the structure of an atom ♦ Discuss insulators, conductors, and semiconductors and how they differ. [9 hrs]</u></p> <p><u>Revision problem classes [3 hrs]</u></p> <p><u>Part B - Transport Phenomena in Semiconductor</u>  <u>Describe how current is produced in a semiconductor ♦ Describe the properties of n-type and p-type semiconductors. [30 hrs]</u></p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	<p>The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills.</p>

Student Workload (SWL)			
Structured SWL (h/sem)	111	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	65	Unstructured SWL (h/w)	2
Total SWL (h/sem)	176		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10	[2,4,5,6]	LO (#1- #12)
	Assignments	2	10	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
Summative assessment	Midterm Exam	1.5 hr	20% (20)	10	LO #(1-8)
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	PN junction in equilibrium
Week 2	Volt Ampere characteristics; Temperature dependence
Week 3	diffusion capacitance



<b>Week 4</b>	Non-linear properties; Ideal diode; Basic theory and analysis of simple diode circuit; DC load line; Small signal analysis and concept of dynamic resistance; AC load line
<b>Week 5</b>	Diode capacitance ;Temperature effects of diode
<b>Week 6</b>	Different types of diodes (Zener; schottckey);
<b>Week 7</b>	(Varactor diode; Tunnel and negative resistance diodes).
<b>Week 8</b>	Circuit analysis of half wave and full wave rectifiers
<b>Week 9</b>	Bridge rectifier; Ripple and form factor calculations
<b>Week 10</b>	Types of filters; C filters , L filter ,L .C. filter, PIE filter; Analysis of filter and calculation of ripple and regulation.
<b>Week 11</b>	Solved problems
<b>Week 12</b>	Clipping and Clam Ping Circuit:
<b>Week 13</b>	Transistors: PNP; NPN
<b>Week 14</b>	The BJT as an Amplifier
<b>Week 15</b>	Preparatory week before the final Exam

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	1: " SOLID STATE DIVICES" ،PHI; 4TH EDITION ،1995.By STREETMAN ، 2: "SEMICONDUCTOR DEVICES & CIRCUITS" ،JOHN WILEY & SONS ،1992.By : M.S. TYAGI 3: " ELECTRONICS DEVICES & CIRCUITS THEORY" ،HI; By BOYLSTED & NASHESKY	Yes
<b>Recommended Texts</b>	3. (Floyed) 4. 51 ثیراجا فصل	Yes

### Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group</b>	<b>A - Excellent</b>	امتیاز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors

(50 - 100)	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information					
معلومات المادة الدراسية					
Module Title	<u>English</u>			Module Delivery	
Module Type	<u>Basic</u>			<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	<u>NVU11</u>				
ECTS Credits	<u>2</u>				
SWL (hr/sem)	<u>50</u>				
Module Level		1	Semester of Delivery		2
Administering Department		Dept. of Computer and Information	College	College of Electronics Engineering	
Module Leader	Noor Mothafar Hamid		e-mail	<a href="mailto:noorm.hame@duoninevah.edu.iq">noorm.hame@duoninevah.edu.iq</a>	
Module Leader's Acad. Title			Module Leader's Qualification		MA
Module Tutor			e-mail		
Peer Reviewer Name			e-mail		
Scientific Committee Approval Date		01/06/2023	Version Number	1.0	

Relation with other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<p><b>Module Aims</b></p> <p>أهداف المادة الدراسية</p>	<ol style="list-style-type: none"> <li>7. To develop skills, reading, writing and understanding of English language through the application of teaching techniques.</li> <li>8. To understand scientific subjects and technical terms through reading and comprehension.</li> <li>9. This course deals with the basic concepts of scientific subjects.</li> <li>10. This course handles how to write simple research and how to make a successful presentation.</li> <li>11. To understand the scientific language in English.</li> </ol>
<p><b>Module Learning Outcomes</b></p> <p>مخرجات التعلم للمادة الدراسية</p>	<ol style="list-style-type: none"> <li>5. Recognize parts of speech and tenses in English language.</li> <li>6. List the various terms associated with scientific texts.</li> <li>7. Summarize what is meant by a basic electric circuit.</li> <li>8. Discuss Electric currents, series and parallel circuits.</li> <li>9. Describe electrical power, charge, and current.</li> <li>10. Discuss computers, communication and the future of computers..</li> <li>11. Identify the basic circuit elements and their applications.</li> <li>12. Explain energy types and forms.</li> <li>13. Discuss the various properties of radio waves and vacuum tubes.</li> <li>14. Explain modulation.</li> <li>15. Discuss Electromagnetism.</li> </ol>
<p><b>Indicative Contents</b></p> <p>المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <ol style="list-style-type: none"> <li>1.parts of speech               <ul style="list-style-type: none"> <li>_verb</li> <li>_ noun</li> <li>_ pronoun</li> </ul> </li> <li>2.Tenses               <ul style="list-style-type: none"> <li>_Past</li> <li>_Present</li> <li>_future</li> </ul> </li> <li>3.Electric currents and circuit               <ul style="list-style-type: none"> <li>_AC/DC</li> <li>_parallel, serious</li> <li>_Grounding, fuse, short circuit</li> </ul> </li> <li>4.Radio waves and vacuum tubes</li> <li>5. Electromagnetism.</li> <li>6. The future of computers, communication applications.               <ul style="list-style-type: none"> <li>_fiber optics.</li> </ul> </li> <li>7. Induction.               <ul style="list-style-type: none"> <li>_Electric generator</li> <li>_Electric transformer</li> </ul> </li> </ol>

	_self-induction _servomechanism 8. Incandescent lamp. 9. Energy.    _types of energy        _forms of energy 10. Introduction to electron and electricity. 11. Electricity and electronics.
<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم	
<b>Strategies</b>	The main strategy that will be adopted in delivering this module is to encourage students' participation by reading, writing and comprehension in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, presentation, interactive tutorials, by considering type of simple experiments involving some sampling activities that are interesting to the students.

<b>Student Workload (SWL)</b> الحمل الدراسي للطالب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	33	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعياً	2
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	17	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعياً	1.4
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	50		

<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	4,6	LO #1, 2, 3,4 ,5and 6
	<b>Assignments</b>	2	5% (5)	9, 12	LO # 7,8,9,10,and 11
	<b>Presentation</b>	1	10% (10)	Continuous	
	<b>Report</b>	1	5% (5)	13	LO # 6,10
<b>Summative assessment</b>	<b>Midterm Exam</b>	2 hr	10% (10)	7	LO # 1-8
	<b>Final Exam</b>	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

### المنهاج الاسبوعي النظري

	Material Covered
<b>Week 1</b>	Parts of speech
<b>Week 2</b>	Tenses
<b>Week 3</b>	Electric currents and circuit
<b>Week 4</b>	Radio waves and vacuum tubes
<b>Week 5</b>	The future of computers, communication applications.
<b>Week 6</b>	Induction -Electric generator -Electric transformer
<b>Week 7</b>	Mid-term Exam
<b>Week 8</b>	Induction -Self-induction -Servomechanism
<b>Week 9</b>	Incandescent lamp.
<b>Week 10</b>	Energy. -types of energy -forms of energy
<b>Week 11</b>	Introduction to electron and electricity.
<b>Week 12</b>	Electricity and electronics
<b>Week 13</b>	The cathode ray tube
<b>Week 14</b>	Propagation
<b>Week 15</b>	Modulation
<b>Week 16</b>	<b>Preparatory week before the final Exam</b>

## Learning and Teaching Resources

### مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	English in electrical engineering and electronics. The language of electrical and electronic engineering in English.	Yes
<b>Recommended Texts</b>	English for electrical engineering and computing.	No
<b>Websites</b>	<a href="https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk">https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk</a>	

## Grading Scheme

### مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX</b> - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> - Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Courses specification for Second class Medical Engineering (First Course)

Module Information					
<b>Module Title</b>	Engineering analysisI		<b>Module Delivery</b>		
<b>Module Type</b>	Base		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
<b>Module Code</b>	NVEE208				
<b>ECTS Credits</b>	6				
<b>SWL (hr/sem)</b>	150				
<b>Module Level</b>		2	<b>Semester of Delivery</b>		1
<b>Administering Department</b>		Electronics dept	<b>College</b>	Electronics engineering college	
<b>Module Leader</b>	Dr. Omar B Mohammed		<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq	
<b>Module Leader's Acad. Title</b>		Lecturer	<b>Module Leader's Qualification</b>		Ph.D.
<b>Module Tutor</b>			<b>e-mail</b>		
<b>Peer Reviewer Name</b>			<b>e-mail</b>		
<b>Scientific Committee Approval Date</b>			<b>Version Number</b>		

Relation with other Modules			
<b>Prerequisite module</b>	Mathematics II	<b>Semester</b>	1
<b>Co-requisites module</b>	None	<b>Semester</b>	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	This course covers the following topics: Multiple Integrals, Vectors Functions, Numerical Analysis, Statistics and Probability. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	<p>Upon successful completion, students will:</p> <ol style="list-style-type: none"> <li>1. Improve their problem-solving skills.</li> <li>2. Apply that knowledge toward practical problems in different areas of science.</li> <li>3. Utilize the computer capabilities to solve such problems using proper methods.</li> <li>4. Learn how to deal with geometry in 3D; find areas and volumes.</li> <li>5. Solve ordinary and differential equations numerically.</li> <li>6. Learn the importance of probability and statistics in everyday use.</li> </ol>
<b>Indicative Contents</b>	<p>Vectors Functions  Multiple Integrals  Numerical Analysis  Statistics  Probability</p>

## Learning and Teaching Strategies

<b>Strategies</b>	<p>The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.</p>
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Student Workload (SWL)					
Structured SWL (h/sem)			Structured SWL (h/w)		
Unstructured SWL (h/sem)			Unstructured SWL (h/w)		
Total SWL (h/sem)					
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments				
	Projects / Lab.				
	Report				
Summative assessment	Midterm Exam				
	Final Exam				
Total assessment					

Delivery Plan (Weekly Syllabus)	
	Material Covered
<b>Week 1</b>	Vectors: Vector in space, dot and cross product. Lines and planes in space. Vector valued functions and motion in space: position, velocity and acceleration, tangential vectors, curvature and normal vector.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	
<b>Week 5</b>	Multiple Integrals: Double Integral in rectangular coordinates, areas and volumes. Double Integral in Polar Coordinates, areas and volumes. Triple Integrals in rectangular, cylindrical, and spherical coordinates, volumes.
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	Numerical Analysis: Solution of non-linear equations by iteration; bisection and Newton-Raphson. Numerical Integration; trapezoidal rule.
<b>Week 10</b>	
<b>Week 11</b>	

<b>Week 12</b>	Numerical solution of 1st order ordinary differential equations; Euler's method.
<b>Week 13</b>	Statistics and Probability: Definitions, mutually exclusive and conditional probability, permutations and combinations Probability distribution: binomial, normal and Poisson distributions.
<b>Week 14</b>	
<b>Week 15</b>	
<b>Week 16</b>	Preparatory week before the final Exam

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney & Thomas	Yes
<b>Websites</b>		

### Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information				
Module Title	Signal Analysis		Module Delivery	
Module Type	Core		Theory <input checked="" type="checkbox"/> Lecture Lab <input type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEEELM211			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery		1
Administering Department	Type Dept. Code	College	Type College Code	
Module Leader			e-mail	
Module Leader's Acad. Title	Assistant Professor	Module Leader's Qualification		Ph.D.
Module Tutor	Name (if available)	e-mail	E-mail	
Peer Reviewer Name	Name	e-mail	E-mail	
Scientific Committee Approval Date	25/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
Module Aims	Student will be able to: 15. identify signals concepts . 16. understand the classification of signals . 17. understand the different operations on signals. 18. perform Fourier and Laplace transformations of signals.

<b>Module Learning Outcomes</b>	44. Definition of the signal concept. 45. Introduction of mathematical models. 46. Explain Continuous time signals. Discrete time signals. 47. Categorize the signals. 48. Achieve operations on signals. 49. Introduction of basic signals. 50. Define convolution operation between two signals. 51. Introduction of frequency domain and Fourier analysis. 52. Laplace Transformation.
<b>Indicative Contents</b>	Indicative content includes the following.  Introduction to signals: <ul style="list-style-type: none"> <li>- Definition and mathematical models.</li> <li>- Categorization of signals.</li> <li>- Operation on signals.</li> <li>- Basic types of signals.</li> </ul> Convolution operation: <ul style="list-style-type: none"> <li>- Introduction of convolution.</li> <li>- Convolution properties.</li> </ul> Signal transformation: <ul style="list-style-type: none"> <li>- Fourier series and transform.</li> <li>- Laplace Transform.</li> </ul>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	To make students interesting with both types of signals: continuous and discrete. Also with classifications of signals and operations on them. To make them familiar with time and frequency domain and analysis of a signal . Also to make them familiar with different types of transforms of signals. Also to make them have an experience with solving different problems and examples .

Student Workload (SWL)			
Structured SWL (h/sem)	64	Structured SWL (h/w)	4
Unstructured SWL (h/sem)	86	Unstructured SWL (h/w)	1
Total SWL (h/sem)	150		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	2, 5, 9,12,13,15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Basic definitions. Mathematical models.
Week 2	Continuous time signals
Week 3	Discrete time signals
Week 4	Signal classifications

<b>Week 5</b>	Energy and power of signals
<b>Week 6</b>	Basic operations on continuous signals.
<b>Week 7</b>	Basic operations on discrete signals.
<b>Week 8</b>	Time domain representation of continuous signals; sinusoidal and complex exponential signals
<b>Week 9</b>	singularity function signals
<b>Week 10</b>	Convolution definition and operation
<b>Week 11</b>	Convolution properties
<b>Week 12</b>	Frequency domain representation of continuous signals. Spectra and bandwidth of the signal
<b>Week 13</b>	Fourier series representations of periodic signals.
<b>Week 14</b>	Fourier transform representations of non periodic signals.
<b>Week 15</b>	Laplace Transform of continuous signals. Laplace properties.

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	Signals and Systems. Simon S. Haykin	Yes
<b>Recommended Texts</b>	Signals and linear Systems. G. E. Carlson	

### Grading Scheme

Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information				
Module Title	Electronic I		Module Delivery	
Module Type	core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEEELM212			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	1	Semester of Delivery		1
Administering Department	Electronics	College	Electronic Engineering college	
Module Leader			e-mail	
Module Leader's Acad. Title	Assistant Prof.	Module Leader's Qualification		PhD
Module Tutor			e-mail	
Peer Reviewer Name	Name	e-mail	Ahmad.younis@uoninevah.edu.iq	
Scientific Committee Approval Date	12/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module	NEEI2212	Semester	
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ul style="list-style-type: none"> <li>12. To understand the basic analysis of bipolar transistor amplifier</li> <li>13. To be familiar with the dc and ac analysis of transistor amplifier</li> <li>14. To understand the dc and ac analysis of FET amplifier</li> <li>15. To illustrate and to understand the frequency response of amplifier</li> <li>16. To understand the basic concept of feedback concept</li> <li>17. To be able to deal with different feedback amplifier topologies</li> <li>18. To study the advantages of negative feedback on amplifier performance</li> <li>19. To be familiar with feedback amplifier ac analysis</li> <li>20. To understand the construction and ideal characteristic of operational amplifier</li> <li>21. To study and analyze op-amp equivalent circuit</li> <li>22. To be familiar with basic op-amp applications</li> <li>23. To start with studying power electronic devices</li> </ul>
<b>Module Learning Outcomes</b>	<ul style="list-style-type: none"> <li>16. Understand and apply the basic theory and operation of transistor amplifiers</li> <li>17. Define and explain the frequency response of bipolar transistor amplifier</li> <li>18. Understand the basic concept of negative feedback</li> <li>19. Understand and analyze the feedback amplifier</li> <li>20. Understanding the operation of ideal operational amplifier</li> <li>21. Dealing with dc and ac op-amp equivalent circuit</li> <li>22. Understanding the basic application of op-amp</li> <li>23. Power electronic devices principle overview</li> </ul>



<p><b>Indicative Contents</b></p>	<p><b>Transistor and FET amplifier analysis:</b></p> <p>Small signal model analysis, low frequency and high frequency analysis, hybrid model, hybrid -Pi model analysis.</p> <p><b>Amplifier with negative feedback:</b></p> <p>Basic concept, feedback analysis, feedback configurations, Feedback effects on gain , bandwidth, input and output resistances</p> <p><b>Operational amplifier:</b></p> <p>Ideal Op-amp equivalent circuit; Operational Amplifier Specification; Circuit analysis of an Op-amp;</p> <p>Closed loop Op-amp Circuit ( Inverting and Non-Inverting Circuit ).</p> <p><b>Op-amp Applications:</b> Summation &amp; subtraction Circuit, Differential circuit Buffer circuit, Ideal and practical Integrator circuits, ideal and practical Differentiator circuits, Examples.</p> <p><b>Power electronic devices:</b></p> <p>UJT Construction, Operation and characterises;</p> <p>Thyristor Equivalent Circuit ; Thyristor Characteristics and operation ;</p> <p>Application of the devices.</p>	

<p><b>Learning and Teaching Strategies</b></p>	
<p><b>Strategies</b></p>	<p>The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the</p>

	consideration of simple experiments involving sampling activities that students find interesting.
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Student Workload (SWL)			
Structured SWL (h/sem)	74	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	101	Unstructured SWL (h/w)	1
Total SWL (h/sem)	175		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	2, 5, 9,12,13,15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1:30hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Small signal model analysis
Week 2	Low and high frequency response of transistor amplifier
Week 3	Hybrid and hybrid-Pie equivalent circuit analysis
Week 4	Negative feedback concept and analysis
Week 5	Advantages of negative feedback on amplifier

<b>Week 6</b>	Amplifier feedback topologies
<b>Week 7</b>	Feedback effect on amplifier gain, bandwidth, and on input-output resistances
<b>Week 8</b>	operational amplifier construction and operation
<b>Week 9</b>	ideal and practical op-amp equivalent circuit
<b>Week 10</b>	Inverting and non inverting closed loop amplifier
<b>Week 11</b>	Integration and differentiation active circuits
<b>Week 12</b>	Summation and subtraction op-amp circuits
<b>Week 13</b>	UJT transistor construction
<b>Week 14</b>	Thyristor equivalent circuit and characteristics
<b>Week 15</b>	Subject review
<b>Week 16</b>	<b>Subject review</b>

### Delivery Plan (Weekly Lab. Syllabus)

	<b>Material Covered</b>
<b>Week 1-15</b>	<b>Practical experiments in transistor amplifier frequency response at lo and high frequency</b> <b>To measure the effect of feedback on amplifier performance</b> <b>To measure the performance of different op-amp circuits.</b>

### Learning and Teaching Resources

	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Electronic Devices By Millmann Electronic Devices By Floyd	yes
<b>Recommended Texts</b>	<b>SOLID STATE DIVICES", PHI; 4TH EDITION, 1995.By STREETMAN,</b> <b>SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992.By : M.S. TYAGI</b>	Yes
<b>Websites</b>	Electronic circuits	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information					
معلومات المادة الدراسية					
Module Title	Digital Design			Module Delivery	
Module Type	Core			<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE223				
ECTS Credits	4				
SWL (hr/sem)	100				
Module Level		2	Semester of Delivery		1
Administering Department		Electronic Eng. Dep.	College	Electronics Engineering	

<b>Module Leader</b>	Amer Talal Ali	<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>	Lecturer assistant	<b>Module Leader's Qualification</b>	
<b>Module Tutor</b>	Amer Talal Ali	<b>e-mail</b>	
<b>Peer Reviewer Name</b>	Name	<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>	01/06/2023	<b>Version Number</b>	

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
<b>Prerequisite module</b>		<b>Semester</b>	
<b>Co-requisites module</b>		<b>Semester</b>	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	19. To understand Advanced Minimization techniques for large number of bits to simplify the large designs. 20. Understand how to Design an Arithmetic and Logic unit. 21. Understand how to Design using programmable logic device. 22. To understand the sequential Logic Circuits. 23. To understand how to Design synchronous and asynchronous counters. 24. To understand the Design of Registers.
<b>Module Learning Outcomes</b>	1. Using Advanced Minimization techniques for large number of bits to simplify the large designs. 2. Design an Arithmetic and Logic unit. 3. Design using programmable logic device.

مخرجات التعلم للمادة الدراسية	4. Design sequential Logic Circuits synchronous and asynchronous. 5. Design Registers. 6. Design synchronous and asynchronous counters.
<b>Indicative Contents</b>  المحتويات الإرشادية	Indicative content includes the following.  <u>Part A</u> – minimization techniques for large number of bits [14 hrs]  <u>Part B</u> – Initialization to design and Design an Arithmetic and Logic unit. [14 hrs]  <u>Part C</u> – Design using programmable logic device. [6 hrs]  <u>Part D</u> – sequential Logic Circuits. [18 hrs]

Learning and Teaching Strategies			
استراتيجيات التعلم والتعليم			
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking and digital designing skills. This will be achieved through classes and interactive tutorials.		
Student Workload (SWL)			
الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
Structured SWL (h/sem)	60	Structured SWL (h/w)	4
الحمل الدراسي المنتظم للطالب خلال الفصل		الحمل الدراسي المنتظم للطالب أسبوعيا	
Unstructured SWL (h/sem)	60	Unstructured SWL (h/w)	4
الحمل الدراسي غير المنتظم للطالب خلال الفصل		الحمل الدراسي غير المنتظم للطالب أسبوعيا	

<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	<b>120</b>
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<b>Module Evaluation</b> تقييم المادة الدراسية					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	<b>Assignments</b>	1	10% (10)	14	LO #4, #7, #(10-13)
	<b>Projects / Lab.</b>	0	0% (0)		
	<b>Report</b>	1	10% (10)	12	LO #11
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5 hr	10% (10)	10	LO #(1-8)
	<b>Final Exam</b>	2 hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

### المنهاج الاسبوعي النظري

	Material Covered
<b>Week 1</b>	Introduction to Digital Design
<b>Week 2</b>	The 5-Variable Karnaugh Map; The 5-Variable Karnaugh Map with don't care conditions
<b>Week 3</b>	Map Entered variable Karnaugh Map
<b>Week 4</b>	ADDITIONAL MINIMIZATION TECHNIQUES: Tabular method; Quine-McCluskey
<b>Week 5</b>	Design using multiplexer: - Shannon Expansion
<b>Week 6</b>	top-down design of combinational CIRCUITS: - Gate Level: Adders; Subtractor
<b>Week 7</b>	Design an Arithmetic and Logic unit
<b>Week 8</b>	memory and type of memories
<b>Week 9</b>	Design using programmable logic device (PLD): - PROM; PAL; PLA;
<b>Week 10</b>	sequential LOGIC: - Type of flip-flops; Timing Diagram; Basic concepts of counters; Binary counters; BCD counters; Up down counter
<b>Week 11</b>	sequential LOGIC: -Design of counters using state diagrams and tables;
<b>Week 12</b>	sequential LOGIC: -Mealy and Moore Circuits;
<b>Week 13</b>	synchronous CIRCUITS: Shift left and right register; Registers with parallel load; Serial –in parallel-out (SIPO) and parallel-in-serial-out (PISO).
<b>Week 14</b>	synchronous CIRCUITS: Shift Registers; Twisted Ring Counter; Maximum Length Shift Counter.
<b>Week 15</b>	Preparatory week before the final Exam



Learning and Teaching Resources		
مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	"Digital and analog communication" 2001 By L. W. Couch Sixth Edition	Yes
Recommended Texts	- Digital Communications Fifth Edition, 2008, John G. Proakis, and Masoud Salehi.  Introduction to Communication Systems" 1992 By F. Stremler.  -ELEMENTS OF INFORMATION THEORY" 2006 By THOMAS M. COVER and JOY A. THOMAS  -Digital Communication, 2004 by Abbas Kattoush.	Yes
Websites		

Grading Scheme				
مخطط الدرجات				
Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to				

condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information			
Module Title	Electromagnetic Fields I		Module Delivery
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEE215		
ECTS Credits	4		
SWL (hr/sem)	100		
Module Level	2	Semester of Delivery	1
Administering Department	Type Dept. Code	College	Type College Code
Module Leader	SINAN KHALID SHANSHAL		e-mail sinan.mohammed@uoninevah.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc.
Module Tutor		e-mail	
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	02/07/2023	Version Number	1.0

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

### Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	To develop knowledge of the laws governing the behavior of electric fields, and to relate the laws governing the fields to applications in a range of electrical and electronic engineering application.
<b>Module Learning Outcomes</b>	<p>On completion of the course the students should be able:</p> <ul style="list-style-type: none"> <li>• to have detailed knowledge of the physical background and terminology of the electrostatic field theory for electrical engineering problems</li> <li>• to understand the electrostatic field behavior</li> <li>• to select and use appropriate theoretical models for analysis, problem solving and finding solutions related to the electrostatic fields</li> <li>• to understand how laws of electrostatic can be applied to problems arising in engineering.</li> </ul>
<b>Indicative Contents</b>	<p>Electric charge and the electric field  Electric flux density and Gauss's Law  Electric potential  Electric field in matter and boundary conditions  Capacitance</p>

### Learning and Teaching Strategies

<b>Strategies</b>	Through the presentation of a theoretical explanation with the aid of white board and 'Data Show', to illustrate syllabus (examples and exercises) and using text books.
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### Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	<b>45</b>	<b>Structured SWL (h/w)</b>	<b>4</b>
<b>Unstructured SWL (h/sem)</b>	<b>?</b>	<b>Unstructured SWL (h/w)</b>	<b>1</b>
<b>Total SWL (h/sem)</b>	<b>?</b>		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	15% (10)	5,8,10,12	LO #1-5, 9 and 11
	Assignments	4	15% (10)	6,9,11,13	LO # 1-5, 6, 10 and 12
	Projects	0	0% (0)		
	Report	0	0% (0)		
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-8
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Review of Vector Calculus
<b>Week 2</b>	Review of Vector Calculus
<b>Week 3</b>	Experimental law of coulomb; Electric field intensity;
<b>Week 4</b>	Field of a continuous and volume charge distributions; line charge and sheet charge;
<b>Week 5</b>	Field of a continuous and volume charge distributions; line charge and sheet charge;
<b>Week 6</b>	Electric flux density; Gauss's law;
<b>Week 7</b>	Application of Gauss's law; some symmetrical charge distributions.
<b>Week 8</b>	Energy expended in moving a point charge in an electric field;
<b>Week 9</b>	Definition of potential difference and potential;
<b>Week 10</b>	Potential field of a point charge and system of charges; Potential gradient.
<b>Week 11</b>	Conductor Properties and boundary conditions;
<b>Week 12</b>	Nature of Dielectric Materials; Boundary Conditions for Perfect dielectric Materials;
<b>Week 13</b>	Capacitance; Several Capacitance Examples.
<b>Week 14</b>	Poisson and Laplace 's equations; Examples of the solution of Laplace equation
<b>Week 15</b>	Examples of the solution of Laplace equation; Examples of the solution of Poisson's equation.
<b>Week 16</b>	Preparatory week before the final Exam

## Delivery Plan (Weekly Lab. Syllabus)

	Material Covered
Week 1-15	

## Learning and Teaching Resources

	Text	Available in the Library?
Required Texts	1-ENGINEERING ELECTROSTATICS, Mc- Graw Hill, By WILLIAM H. HAYT. 2-Elements of engineering electrostatic, Prentice Hall, By Matthew N. O. SADIKU	No
Recommended Texts	1-Electrostatics (Schaum's Outlines), McGraw-Hill Education By Edminister, Joseph_ Nahvi, Mahmood.	No
Websites		

## Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information				
Module Title	Human Physiology		Module Delivery	
Module Type	Support		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEEELM 213			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	2	Semester of Delivery		
Administering Department	Electronics dept	College	Electronics engineering college	
Module Leader			e-mail	
Module Leader's Acad. Title			Module Leader's Qualification	
Module Tutor			e-mail	
Peer Reviewer Name			e-mail	
Scientific Committee Approval Date			Version Number	

Relation with other Modules			
Prerequisite module	None		Semester
Co-requisites module	None		Semester

Module Aims, Learning Outcomes and Indicative Contents
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<b>Module Aims</b>	<p>This subject introduces engineering students to human anatomy and physiology, with direct application of the knowledge to considerations for designing and manufacturing medical devices and equipment to assist in overcoming physical disabilities.</p>
<b>Module Learning Outcomes</b>	<p>Upon completion this unit, the student should be able to:</p> <ol style="list-style-type: none"> <li>1. Demonstrate correct usage of the terminology used to describe anatomical structures.</li> <li>2. Describe the organization of cells and tissues.</li> <li>3. Describe the principles relating to the structure of connective tissues, skeletal muscle, bones and joints.</li> <li>4. Describe the principles of excitable tissues.</li> <li>5. Describe the structure and function of the human eye and ear and the mechanisms of vision and hearing.</li> <li>6. Describe the principles of sensorimotor control.</li> <li>7. Describe cardiac mechanics and cardiac biophysics.</li> <li>8. Describe the application of technologies and techniques for investigating the structure and function of the body.</li> </ol>
<b>Indicative Contents</b>	<p>Anatomical terminology.  The structure and appearance of cells and tissues.  The appearance of bone and cartilage, the organization of dense connective tissues.  Skeletal muscle structure and function.  Principles of excitable tissues.  The structure and function of sensory systems, including the eye and vision and the ear and hearing.  Principles of sensory motor control.  Cardiac mechanics and cardiac biophysics.  Technologies, quantitative measurements and experimental techniques used to investigate the structure and function of different tissues, organs and organ systems.</p>

Learning and Teaching Strategies	
Strategies	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

Student Workload (SWL)					
Structured SWL (h/sem)			Structured SWL (h/w)		
Unstructured SWL (h/sem)			Unstructured SWL (h/w)		
Total SWL (h/sem)					
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments				
	Projects / Lab.				
	Report				
Summative assessment	Midterm Exam				
	Final Exam				
Total assessment					

Delivery Plan (Weekly Syllabus)	
	Material Covered



<b>Week 1</b>	Cells and their function
<b>Week 2</b>	Tissues, glands & membranes
<b>Week 3</b>	Muscle tissue
<b>Week 4</b>	The skeleton
<b>Week 5</b>	Nervous system
<b>Week 6</b>	Sensory Respiration The eye The joints
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	
<b>Week 10</b>	The skin Digestive system The urinary system and body fluids
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	The heart
<b>Week 14</b>	Blood Blood vessels Blood clotting
<b>Week 15</b>	
<b>Week 16</b>	
<b>Week 16</b>	Preparatory week before the final exam

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>		
<b>Recommended Texts</b>		
<b>Websites</b>		

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
	A - Excellent	امتياز	90 - 100	Outstanding Performance.

<b>Success Group (50 - 100)</b>	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

## Courses specification for Second class Medical Engineering (Second Course)

Module Information				
Module Title	Signals and Systems		Module Delivery	
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> X Practical <input type="checkbox"/> Seminar	
Module Code	NEEM210			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery	4	
Administering Department	Type Dept. Code	College	Type College Code	
Module Leader			e-mail	
Module Leader's Acad. Title	Assistant Professor	Module Leader's Qualification	Ph.D.	
Module Tutor	Name (if available)	e-mail	E-mail	
Peer Reviewer Name	Name	e-mail	E-mail	
Scientific Committee Approval Date	25/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
Module Aims	Student will be able to:

	<p>25. identify systems concepts .</p> <p>26. understand the properties of systems .</p> <p>27. understand the mathematical relation between input and output of a system.</p> <p>28. deal with Fourier and Laplace analysis of systems.</p> <p>29. perform z-transform of discrete signals .</p>
<b>Module Learning Outcomes</b>	<p>53. Definition of the system concept.</p> <p>54. Introduction of mathematical models.</p> <p>55. Explain Continuous time systems. Discrete time systems.</p> <p>56. Introduction of frequency response of systems.</p> <p>57. Definition of filters.</p> <p>58. Explain Ideal filters, Non ideal filters, and Butterworth filter design.</p> <p>59. Define Z-transform of discrete signals.</p> <p>60. Analyze of continuous system using Laplace Transform. System transfer function.</p> <p>61. Definition of transfer function of a discrete system.</p>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p>Introduction to systems:</p> <ul style="list-style-type: none"> <li>- Definition and mathematical models.</li> <li>- Properties of systems.</li> </ul> <p>Transformation used with continuous systems</p> <ul style="list-style-type: none"> <li>- Fourier transforms.</li> <li>- Filters.</li> <li>- Laplace transform.</li> </ul> <p>Z-transform:</p> <ul style="list-style-type: none"> <li>- Introduction of z- transform of discrete time signal.</li> <li>- Z-transform used with discrete systems.</li> </ul> <p>Convolution used for</p> <ul style="list-style-type: none"> <li>- Continuous systems.</li> <li>- Discrete systems</li> </ul>

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### Learning and Teaching Strategies

<b>Strategies</b>	To make students interesting with both types of systems: continuous and discrete. Also with properties of systems and operations . To make them familiar with time and frequency domain and analysis of a system. Also to make them familiar with different types of transforms of systems. Also to make them have an experience with solving different problems and examples.
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### Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	62	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	88	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	150		

### Module Evaluation

	<b>Time/Number</b>	<b>Weight (Marks)</b>	<b>Week Due</b>	<b>Relevant Learning Outcome</b>
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<b>Formative assessment</b>	<b>Quizzes</b>	6	10% (10)	2, 5, 9,12,13,15	LO #1, 2, 10 and 11
	<b>Assignments</b>	6	10% (10)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	6	20% (20)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	<b>Report</b>	0	0% (0)	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5hr	20% (20)	10	LO # 1-4
	<b>Final Exam</b>	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Basic definitions. Mathematical models.
<b>Week 2</b>	Continuous time systems.
<b>Week 3</b>	Discrete time systems.
<b>Week 4</b>	System properties.
<b>Weeks 5</b>	Mathematical system representation in time domain: Convolution representation.
<b>Week 6</b>	Convolution properties.
<b>Week 7</b>	System description by linear constant coefficient differential equations.
<b>Week 8</b>	Frequency domain analysis of continuous system.
<b>Week 9</b>	Frequency response of a system.
<b>Week 10</b>	Frequency response of electrical circuits.

<b>Week 11</b>	Filters. Distortion less transmission.
<b>Week 12</b>	Ideal filters. Non ideal filters. Butterworth filter design.
<b>Week 13</b>	Analysis of continuous system using Laplace Transform.
<b>Week 14</b>	System transfer function.
<b>Week 15</b>	Analysis of discrete system using z-Transform. System transfer function.

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Signals and Systems. Simon S. Haykin	Yes
<b>Recommended Texts</b>	Signals and linear Systems. G. E. Carlson	

Grading Scheme				
Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

### Module Information

<b>Module Title</b>	Engineering Analysis II		<b>Module Delivery</b>	
<b>Module Type</b>	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	NVEE209			
<b>ECTS Credits</b>	3			
<b>SWL (hr/sem)</b>	75			
<b>Module Level</b>	2	<b>Semester of Delivery</b>	2	
<b>Administering Department</b>	Electronics dept	<b>College</b>	Electronics engineering college	
<b>Module Leader</b>	Dr. Omar B Mohammed		<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq
<b>Module Leader's Acad. Title</b>	Lecturer	<b>Module Leader's Qualification</b>	Ph.D.	
<b>Module Tutor</b>		<b>e-mail</b>		
<b>Peer Reviewer Name</b>		<b>e-mail</b>		
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>		

### Relation with other Modules

<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	



Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	This course covers the following topics: ordinary differential equations, sequences and series, solution of differential equations by power series, and matrix analysis. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	<p>Upon successful completion, students will:</p> <ol style="list-style-type: none"> <li>7. Improve their problem-solving skills.</li> <li>8. Apply that knowledge toward practical problems in different areas of science.</li> <li>9. Utilize the computer capabilities to solve such problems using proper methods.</li> <li>10. Learn how to represent any function as a power series, then use computer to solve it.</li> <li>11. Learn the importance of differential equations for modeling almost any system, and how to solve it to find the properties of that system.</li> <li>12. Learn the linear algebra and its importance in science.</li> </ol>
<b>Indicative Contents</b>	<p>Ordinary Differential Equations.  Sequences and Series.  Solution of Differential Equations by Power Series.  Matrix Analysis.</p>

Learning and Teaching Strategies	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

### Student Workload (SWL)

Structured SWL (h/sem)		Structured SWL (h/w)	
Unstructured SWL (h/sem)		Unstructured SWL (h/w)	
Total SWL (h/sem)			

### Module Evaluation

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments				
	Projects / Lab.				
	Report				
Summative assessment	Midterm Exam				
	Final Exam				
Total assessment					

### Delivery Plan (Weekly Syllabus)

		Material Covered
Week 1	Ordinary Differential Equations: 1. First order (variables separable, homogeneous, linear and exact). 2. Second order homogeneous. 3. Second order nonhomogeneous; indeterminant coefficients, variation of parameters.	
Week 2		
Week 3		
Week 4		
Week 5	Infinite Sequences and Series: 1. Limit laws, indeterminate forms and L'hospital rule. 2. Infinite series; convergence test.	
Week 6		
Week 7		
Week 8		

	3. Power series; Taylor and Maclaurin series.
<b>Week 9</b>	Solution of Differential Equations by Power Series: Power series method, Legendre's equation; Legendre s polynomials.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	Matrix Analysis: 1. Review of matrix theory, solving system of equations; Cramer's rule, inverse of the matrix method, Gauss elimination.  2. Eigen values and eigen vectors.  3. Diagonalization of matrices  4. Application of matrices to electric circuits.
<b>Week 14</b>	
<b>Week 15</b>	
<b>Week 16</b>	
	Preparatory week before the final Exam

### Learning and Teaching Resources

	Text	Available in the Library?
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney& Thomas	Yes
<b>Websites</b>		

### Grading Scheme

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.

<b>(0 – 49)</b>	<b>F – Fail</b>	راسب	<b>(0-44)</b>	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information				
Module Title	Electronic II		Module Delivery	
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEEELM221			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	1	Semester of Delivery		2
Administering Department	Electronics	College	Electronic Engineering college	
Module Leader			e-mail	
Module Leader's Acad. Title	Assistant Prof.	Module Leader's Qualification		PhD
Module Tutor			e-mail	
Peer Reviewer Name	Name	e-mail	Ahmad.younis@uoninevah.edu.iq	
Scientific Committee Approval Date	12/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module	Electronic I	Semester	1
Co-requisites module	None	Semester	

## Module Aims, Learning Outcomes and Indicative Contents

<b>Module Aims</b>	<ul style="list-style-type: none"> <li>24. To understand the basic theory and operation of bipolar transistor</li> <li>25. To be familiar with current mechanism in an npn and pnp transistors</li> <li>26. To concentrate transistor physical and electrical characteristics</li> <li>27. To illustrate and design different dc biasing circuits</li> <li>28. To understand the biasing stability conditions</li> <li>29. To be able to deal with the mathematical behavior of transistor model</li> <li>30. To understand small signal analysis of transistor amplifier</li> <li>31. To deal with different transistor amplifier configuration</li> <li>32. To be able to deal with the frequency response of transistor amplifier</li> <li>33. To understand the basic operation of field effect transistor and MOS device</li> <li>34. To understand the dc and ac behavior of FET and MOS amplifiers</li> </ul>
<b>Module Learning Outcomes</b>	<ul style="list-style-type: none"> <li>24. Understand and apply the basic theory and operation of transistor amplifiers</li> <li>25. Define and explain the electrical characteristic of bipolar transistor</li> <li>26. Understand the basic structure of npn and pnp transistors</li> <li>27. Understand and analyze the electrical transistor model</li> <li>28. Understanding the dc and ac analysis of transistor amplifier</li> <li>29. Dealing with dc biasing and ac amplifiers</li> <li>30. Understanding the effect of frequency on amplifier response</li> <li>31. Familiar with other FET and MOS circuits</li> </ul>

Indicative Contents	<p><b>Bipolar junction transistors,</b>  Transistor construction, transistor operation,  NPN &amp; PNP Bipolar Transistor; Current Flow Mechanism in Transistor Junctions; Transistor configurations; Current Gain Calculation [Alpha] and [Beta]; Transistor input/ output characteristics; DC Load line ; Operating point; Different DC circuit biasing. Bias circuit, voltage divider circuit, dc bias with feedback</p> <p><b>DC biasing,</b>  Operating point, fixed bias circuit, emitter bias circuit, voltage divider circuit, dc bias with feedback.</p> <p><b>Biasing stability</b>  Stability factor analysis due to temperature variation ( Effect of <math>I_{co}</math>, <math>V_{be}</math> and <math>\beta</math> ); Temperature compensation using diode biasing.</p> <p><b>Small signal analysis,</b>  Small signal equivalent circuit for CB, CE and CC configuration; Input/Output resistance; Calculation of current and voltage Gain in small signal amplifier; Graphical Analysis for voltage gain; Hybrid parameters to analyze transistor circuits.</p> <p><b>Field Effect Transistor (FET) and MOS transistor :</b>  <b>FET biasing configurations, Depletion and Enhanced mode operation,</b>  Introduction to the theory and operations of JFET &amp; MOSFET; FET Transistor configurations; Transistors transfer characteristics; Amplifier Circuit Biasing; transistor Equivalent circuit; Small signal analysis of FET transistor.</p> <p><b>FREQUENCY RESPONSE:</b>  Definition and Concepts; Gain in decibel; Bode plot for the gain; The effect of the Coupling capacitor; Low frequency analysis due to the R-C Coupled amplifier in BJTs; the Effect of emitter bypass capacitor; Calculation of the Low cut-off frequency. Transistor amplifier at high frequencies; Hybrid PIE equivalent circuit at high frequency; High frequency behavior of CB &amp; CE amplifier; High cut-off frequency; Gain Band-Width products for the above circuits; FET at high frequencies; CD and CS amplifier at high frequency;</p>	

## Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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Student Workload (SWL)					
Structured SWL (h/sem)		88	Structured SWL (h/w)		3
Unstructured SWL (h/sem)		76	Unstructured SWL (h/w)		1
Total SWL (h/sem)		164			
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	2, 5, 9,12,13,15	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1:30hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>
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	Material Covered
Week 1	Transistor construction and operation
Week 2	Bipolar transistor current flow mechanism
Week 3	Transistor configurations, current gain calculation, and input and output resistances
Week 4	Dc biasing circuits, operating point calculation
Week 5	Biasing stability, stability factor calculation
Week 6	Temperature compensation using diode biasing
Week 7	Small signal equivalent circuit for CB, CC, CE configurations
Week 8	Calculation of voltage and current gains
Week 9	Hybrid model ac analysis of transistor amplifier
Week 10	FET and MOS transistors operation
Week 11	FET biasing configurations
Week 12	Depletion and enhancement mode operation
Week 13	Definition and analysis of amplifier frequency response
Week 14	Low frequency and high frequency analysis
Week 15	Hybrid-Pie equivalent circuit at high frequency
Week 16	Subject review

Delivery Plan (Weekly Lab. Syllabus)	
	Material Covered
Week 1-15	<p>Practical experiments in transistor amplifiers to measure the current and voltage gains.</p> <p>To measure the input and output amplifier resistances</p> <p>To measure the amplifier frequency response.</p>

Learning and Teaching Resources		
	Text	Available in the Library?
Required Texts	<p>Textbook1:INTEGRATED ELECTRONICS" MCGRAW HILL;9T HREPRINT,1995.By MILLMAN&amp;HALKIES</p> <p>2: " ELECTRONICS DEVICES AND COMPONENTS", PITMAN, 1995 By MOTTERSLED.,</p>	yes



<b>Recommended Texts</b>	3: " <b>SOLID STATE DIVICES</b> ", PHI; 4TH EDITION, 1995.By STREETMAN, 4" <b>SEMICONDUCTOR DEVICES &amp; CIRCUITS</b> ", JOHN WILEY & SONS, 1992.By : M.S. TYAGI	Yes
<b>Websites</b>	Electronic circuits	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information			
معلومات المادة الدراسية			
Module Title	Programming		Module Delivery
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	NVEEELM222		
ECTS Credits	6		
SWL (hr/sem)	150		
Module Level	UGx11 2	Semester of Delivery	2
Administering Department	Dept. of Electronic Eng. (Med. Ele)	College	College of Electronic Engineering
Module Leader	Qais Thanon	e-mail	Qais.najim@uoninevah.edu.iq
Module Leader's Acad. Title	Porf.	Module Leader's Qualification	Ph. D.
Module Tutor	Name (if available)	e-mail	E-mail
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	20/06/2023	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	1. Learning about the algorithms types and how building the algorithms. 2. Learning how to command computers to perform tasks using C++ language (Programming/coding). 3. Become acquainted with the designed programming including sequencing, condition and iteration. 4. Learn about the 1d and 2d arrays in C++ language. 5. Learn about the functions in C++ language. 6. Learn about the strings in C++ language.

<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	1. Understanding the meaning of the algorithms in programming languages. 2. Understanding the basics concepts of C language programming such as variables, data types, operators, control 3. Understanding the utilities of each one of sequencing, condition, and loops, and basic input/output operations. 4. Understanding how represent the data in 1d arrays and 2d arrays. 5. Learn about how the strings represented in C language. 6. Learn about divide any problem in sub-program and execute this problem by using function. 7. In advance practical experience by working on programming exercises and projects.
<b>Indicative Contents</b> المحتويات الإرشادية	<p>Indicative content includes the following.</p> <ul style="list-style-type: none"> <li>• Visualization via flowchart and Pseudocode [4 hrs]</li> <li>• Keywords, identifier, format specifier, and naming variables and constants [8 hrs]</li> <li>• Use standard libraries to take input and display output [8 hrs]</li> <li>• Operators in C++ programming [10 hrs]</li> <li>• Priorities in C++ programming [4 hrs]</li> <li>• Math functions [4 hrs]</li> <li>• Conditional operations [8 hrs]</li> <li>• Iterations (Loop operators) [10 hrs]</li> <li>• Arrays [10 hrs]</li> <li>• Functions [8 hours]</li> <li>• Review classes and problem solving [8 hrs]</li> </ul>

Learning and Teaching Strategies			
استراتيجيات التعلم والتعليم			
Strategies	The main strategy being focused on is developing conceptual programming thinking, meanwhile refining and expanding their mathematical thinking skills. This will be achieved through classes, online lectures, interactive tutorials. Additionally, working on complex projects that challenge students’ skills and require to apply advanced concepts. Such projects would help students exploring various aspects of C++ programming and gain hands-on experience in solving complex problems. some sampling activities that are interesting to the students.		
Student Workload (SWL)			
الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
Structured SWL (h/sem)	77	Structured SWL (h/w)	5.1
الحمل الدراسي المنتظم للطالب خلال الفصل		الحمل الدراسي المنتظم للطالب أسبوعيا	
Unstructured SWL (h/sem)	73	Unstructured SWL (h/w)	4.8
الحمل الدراسي غير المنتظم للطالب خلال الفصل		الحمل الدراسي غير المنتظم للطالب أسبوعيا	
Total SWL (h/sem)	150		
الحمل الدراسي الكلي للطالب خلال الفصل			

Module Evaluation					
تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5 and 10	LO #1, #2 and #10, #11
	Assignments	2	10% (10)	2 and 12	LO #3, #4 and #6, #7
	Projects / Lab.	1	10% (10)	Continuous	All
	Report	1	10% (10)	13	LO #5, #8 and #10
Summative assessment	Midterm Exam	2hr	10% (10)	7	LO #1 - #7
	Final Exam	3hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Introduction to computer languages and the structure of C program
Week 2	Flowchart and Pseudo-code
Week 3	Introduction to C++ programming: Declare variables and constants
Week 4	Take input and print output
Week 5	Assignment and Increment ,Decrement, Arithmetic, Logical, and Bitwise operators
Week 6	Standard math functions in math header <math.h>
Week 7	Priorities of operators in C++ programming
Week 8	Relational and conditional operators
Week 9	Mid-term Exam
Week 10	If statement versus switch case statement
Week 11	Examples of structured programming (sequencing and condition)
Week 12	Loop operators (For, while, do-while)
Week 13	Arrays
Week 14	Functions
Week 15	String of characters
Week 16	Preparatory week before the final Exam

## Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1-2	Learn the C++ language program compiler.
Week 3-4	Declare variables and constants and <iostream.h> including standard functions
Week 5-6	Arithmetic, logical, and bitwise operators
Week 7-8	Math header for math functions <math.h> and Assignment and increment & decrement operators
Week 9-10	Relational and conditional operators and Loop operators
Week 11-12	Examples about the Arrays
Week 13-14	Examples about Functions and string

## Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	C Programming Absolute Beginner's Guide, 3rd Edition 2014. BY: Greg Perry and Dean Miller.	Yes
Recommended Texts	C How to Program with an introduction to C++, 8 <sup>th</sup> Edition 2016. BY: Paul Deitel and Harvey Deitel. Global Edition contribution by Piyali Sengupta	No
Websites	1- <a href="https://www.programiz.com/c-programming">https://www.programiz.com/c-programming</a> 2- <a href="https://www.coursera.org/specializations/c-programming">https://www.coursera.org/specializations/c-programming</a>	

## Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information					
Module Title	Electromagnetic Fields II		Module Delivery		
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	NVEE221				
ECTS Credits	6				
SWL (hr/sem)	150				
Module Level		2	Semester of Delivery		2
Administering Department		Type Dept. Code	College	Type College Code	
Module Leader	SINAN KHALID SHANSHAL		e-mail	sinan.mohammed@uoninevah.edu.iq	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		M.Sc.
Module Tutor			e-mail		
Peer Reviewer Name		Name	e-mail	E-mail	
Scientific Committee Approval Date		02/07/2023	Version Number	1.0	

Relation with other Modules				
Prerequisite module	None		Semester	
Co-requisites module	None		Semester	

Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	To develop knowledge of the laws governing the behavior of magnetic and electromagnetic fields, and to relate the laws governing the fields to applications in a range of electrical and electronic engineering application.
<b>Module Learning Outcomes</b>	<p>On completion of the course the students should be able:</p> <ul style="list-style-type: none"> <li>• to have detailed knowledge of the physical background and terminology of the electromagnetic field theory for electrical engineering problems</li> <li>• to understand the electromagnetic field behavior</li> <li>• to select and use appropriate theoretical models for analysis, problem solving and finding solutions related to the electrostatic, magnetostatic and electromagnetic fields</li> <li>• to understand how laws of electromagnetism can be applied to problems arising in engineering.</li> </ul>
<b>Indicative Contents</b>	<p>Magnetic field and Ampere's Law  Magnetic flux and Gauss's Law for magnetic fields  Faraday's Law  Inductance  Maxwell's equations  Applications of Electromagnetics</p>

Learning and Teaching Strategies	
<b>Strategies</b>	Through the presentation of a theoretical explanation with the aid of white board and 'Data Show', to illustrate syllabus (examples and exercises) and using text books.

Student Workload (SWL)			
<b>Structured SWL (h/sem)</b>	45	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	?	<b>Unstructured SWL (h/w)</b>	1

Total SWL (h/sem)		?			
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	15% (10)	5,8,10,12	LO #1-5,6-7, 9 and 11
	Assignments	4	15% (10)	6,9,11,13	LO # 1-5, 6, 10 and 12
	Projects	0	0% (0)		
	Report	0	0% (0)		
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-8
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Review of Vector Calculus
Week 2	Review of Vector Calculus
Week 3	Boit – Savart law
Week 4	Amperes law; Magnetic Flux & Magnetic Flux Density
Week 5	Inductance
Week 6	Force on Moving Charge; Force on Differential Current. Elements
Week 7	Force and Torque on a Closed Circuit
Week 8	Magnetization and Permeability; Magnetic Boundary Conditions; Magnetic Circuit.
Week 9	Faraday's Law;
Week 10	Maxwell's Equations
Week 11	Example of Maxwell's Equations
Week 12	Wave Equations; Wave Propagation in Lossy Dielectrics
Week 13	Plane Waves in Lossless Dielectrics; Plane Waves in Free Space
Week 14	Plane Waves in Good Conductors;
Week 15	Power and the Poynting Vector.
Week 16	Preparatory week before the final Exam



Delivery Plan (Weekly Lab. Syllabus)	
	Material Covered
Week 1-15	

Learning and Teaching Resources		
	Text	Available in the Library?
Required Texts	1-ENGINEERING ELECTROMAGNETICES, Mc- Graw Hill, By WILLIAM H. HAYT. 2-Elements of engineering electromagnetic, Prentice Hall, By Matthew N. O. SADIKU	No
Recommended Texts	1-Electromagnetics (Schaum's Outlines), McGraw-Hill Education, By Edminister, Joseph_ Nahvi, Mahmood.	No
Websites		

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information			
معلومات المادة الدراسية			
Module Title	<u>English</u>		Module Delivery
Module Type	<u>Support</u>		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	<u>NVU11</u>		
ECTS Credits	<u>3</u>		
SWL (hr/sem)	<u>75</u>		
Module Level	UGx11 1	Semester of Delivery	1
Administering Department		College	NV
Module Leader		e-mail	
Module Leader's Acad. Title	Noor Mothafar Hamid	Module Leader's Qualification	MS.D.
Module Tutor	Name (if available)	e-mail	noorm.hame@duoninevah.edu.iq
Peer Reviewer Name	Name	e-mail	E-mail
Scientific Committee Approval Date	01/06/2023	Version Number	1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Aims</b> أهداف المادة الدراسية	35. To develop skills, reading, writing and understanding of English language through the application of teaching techniques. 36. To understand scientific subjects and technical terms through reading and comprehension. 37. This course deals with the basic concepts of scientific subjects. 38. This course handles how to write simple research and how to make a successful presentation. 39. To understand the scientific language in English.

<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	32. Recognize parts of speech and tenses in English language. 33. List the various terms associated with scientific texts. 34. Summarize what is meant by a basic electric circuit. 35. Discuss Electric currents, series and parallel circuits. 36. Describe electrical power, charge, and current. 37. Discuss computers, communication and the future of computers.. 38. Identify the basic circuit elements and their applications. 39. Explain energy types and forms. 40. Discuss the various properties of radio waves and vacuum tubes. 41. Explain modulation. 42. Discuss Electromagnetism.
<b>Indicative Contents</b> المحتويات الإرشادية	<p>Indicative content includes the following.</p> <p>1.parts of speech  _verb  _ noun  _ pronoun  2.Tenses  _Past  _Present  _future  3.Electric currents and circuit  _AC/DC  _parallel, serious  _Grounding, fuse, short circuit  4.Radio waves and vacuum tubes  5. Electromagnetism.  6. The future of computers, communication applications.  _fiber optics.  7. Induction.  _Electric generator  _Electric transformer  _self-induction  _servomechanism  8. Incandescent lamp.  9. Energy.  _types of energy  _forms of energy  10. Introduction to electron and electricity.  11.Electricity and electronics.</p>

<b>Learning and Teaching Strategies</b> استراتيجيات التعلم والتعليم	
<b>Strategies</b>	<p>The main strategy that will be adopted in delivering this module is to encourage students' participation by reading, writing and comprehension in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, presentation, interactive tutorials, by considering type of simple experiments involving some sampling activities that are interesting to the students.</p>

<b>Student Workload (SWL)</b> الحمل الدراسي للطالب			
<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل	30	<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	2
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل	70	<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	5
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	100		

<b>Module Evaluation</b> تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	3	10% (10)	4,6	LO #1, 2, 3,4 ,5and 6
	<b>Assignments</b>	2	10% (10)	9, 12	LO # 7,8,9,10,11and 12
	<b>Projects / Lab.</b>				
	<b>Report</b>	1	10% (10)	13	LO # 13,14
<b>Summative assessment</b>	<b>Midterm Exam</b>	2 hr	10% (10)	7	LO #
	<b>Final Exam</b>	2hr	50% (50)	16	All
<b>Total assessment</b>			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b> المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Parts of speech
Week 2	Tenses
Week 3	Electric currents and circuit
Week 4	Radio waves and vacuum tubes
Week 5	The future of computers, communication applications.
Week 6	Induction -Electric generator -Electric transformer
Week 7	Mid-term Exam
Week 8	Induction -Self-induction -Servomechanism
Week 9	Incandescent lamp.
Week 10	Energy. -types of energy -forms of energy
Week 11	Introduction to electron and electricity.
Week 12	Electricity and electronics
Week 13	The cathode ray tube
Week 14	Propagation
Week 15	Modulation
Week 16	Preparatory week before the final Exam

<b>Delivery Plan (Weekly Lab. Syllabus)</b> المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	
Week 2	
Week 3	
Week 4	
Week 5	

Week 6	
Week 7	

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	English in electrical engineering and electronics. The language of electrical and electronic engineering in English.	Yes
Recommended Texts	English for electrical engineering and computing.	No
Websites	<a href="https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk">https://www.askoxford.com/betterwriting/successfulcv/application/?view=uk</a>	

Grading Scheme مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

## Courses specification for Second class Industrial

## Electronic Engineering (Second Course)

Module Information					
<b>Module Title</b>	<u>DC Machines</u>			<b>Module Delivery</b>	
<b>Module Type</b>	<u>Core</u>			<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
<b>Module Code</b>	<u>NEEI2313</u>				
<b>ECTS Credits</b>	<u>6</u>				
<b>SWL (hr/sem)</b>	<u>114</u>				
<b>Module Level</b>		2    2		<b>Semester of Delivery</b>	
<b>Administering Department</b>		Electronic Dept.		<b>College</b>	
				Electronics collage	
<b>Module Leader</b>				<b>e-mail</b>	
<b>Module Leader's Acad. Title</b>				<b>Module Leader's Qualification</b>	
<b>Module Tutor</b>				<b>e-mail</b>	
<b>Peer Reviewer Name</b>				<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>				<b>Version Number</b>	

Relation with other Modules			
Prerequisite module	DC Circuit Analysis	Semester	1
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents		
<b>Module Aims</b>	40.	Understanding DC Machine Principles
	41.	Analyzing DC Machine Behavior
	42.	Control Strategies
	43.	System Integration
	44.	Practical Applications
	45.	Problem-Solving Skills
	46.	Laboratory Skills
	47.	Teamwork and Communication
	48.	Professional Development
<b>Module Learning Outcomes</b>	43.	Understand how voltage is induced in a rotating loop
	44.	Understand how curved pole faces contribute to a constant flux, and thus
	45.	more constant output voltages.
	46.	Understand how curved pole faces contribute to a constant flux, and thus
		more constant output voltages.
	47.	Understand the power flow diagram for de machines
	48.	Know the types of de motors in general use.
	49.	Understand the equivalent circuit of a de motor.
	50.	Understand how to derive the torque-speed characteristics of separately excited, shunt, series, and compounded de motors.
	51.	Understand how to control the speed of different types of de motors.
	52.	Understand the special characteristics of series de motors, and the applications.
	53.	Understand the methods of starting dc motors safely.
	54.	Understand the equivalent circuit of a dc generator.
	55.	Understand the purpose of a transformer in a power system.
	56.	Understand how real transformers approximate the operation of an ideal transformer.
	57.	Be able to explain how copper losses, leakage flux, hysteresis, and eddy currents are modeled in transformer equivalent circuits.
<b>Indicative Contents</b>	Introduction - A Simple Rotating Loop between Curved Pole Faces. The Voltage Induced in a Rotating Loop / Getting DC Voltage Out of the Rotating Loop / The Induced Torque in the rotating loop.(12 hrs.).	
	Commutation and Armature Construction in Real DC Machine. .(8 hrs.).	
	Power Flow and Losses in DC Machines. .(6 hrs.).	



	Introduction to DC Motors. The Equivalent Circuit of a DC Motor. The Magnetization Curve of a DC Machine. Separately Excited and Shunt DC Motors.(10 hrs.).
	Permanent-Magnet DC Motor. The Series DC Motor. The Compounded DC Motor. .(6 hrs.).
	Motor Starters. Solid-State Speed Controllers. .(12 hrs.).
	DC Motor Efficiency Calculations. .(4 hrs.).
	Mid-term Exam. .(3 hrs.).
	Introduction to DC Generators. The Separately Excited Generator. .(12 hrs.).
	The Shunt DC Generator. The Series DC Generator.(4 hrs.).
	The Cumulatively Compounded DC Generator. The Differentially Compounded DC Generator. .(4 hrs.).
	Types and Construction of Transformers. The Ideal Transformer. .(10 hrs.).
	Theory of Operation of Real Single-Phase Transformers. The Equivalent Circuit of a Transformer. .(18 hrs.).
	Transformer Voltage Regulation and Efficiency. .(12 hrs.).
	Instrument Transformers. .(4 hrs.).

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Learning and Teaching Strategies	
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<b>Strategies</b>	Visual Aids Problem-Solving Exercises Real-World Applications Group Projects
	Simulations and Virtual Labs Multimedia Resources Real-Life Examples

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Student Workload (SWL)			
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<b>Structured SWL (h/sem)</b>	74	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	40	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	114		

Module Evaluation					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	
	Assignments	2	10% (10)	2, 12	
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	13	
Summative assessment	Midterm Exam	2 hr	10% (10)	7	
	Final Exam	2hr	50% (50)	16	
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
Week	Material Covered
Week 1	Introduction - A Simple Rotating Loop between Curved Pole Faces. The Voltage Induced in a Rotating Loop / Getting DC Voltage Out of the Rotating Loop / The Induced Torque in the rotating loop.
Week 2	Commutation and Armature Construction in Real DC Machine.
Week 3	Power Flow and Losses in DC Machines.
Week 4	Introduction to DC Motors. The Equivalent Circuit of a DC Motor. The Magnetization Curve of a DC Machine. Separately Excited and Shunt DC Motors
Week 5	Permanent-Magnet DC Motor. The Series DC Motor. The Compounded DC Motor.
Week 6	Motor Starters. Solid-State Speed Controllers.
Week 7	DC Motor Efficiency Calculations.
Week 8	Mid-term Exam.
Week 9	Introduction to DC Generators. The Separately Excited Generator.
Week 10	The Shunt DC Generator. The Series DC Generator
Week 11	The Cumulatively Compounded DC Generator. The Differentially Compounded DC Generator.
Week 12	Types and Construction of Transformers. The Ideal Transformer.
Week 13	Theory of Operation of Real Single-Phase Transformers. The Equivalent Circuit of a Transformer.

<b>Week 14</b>	Transformer Voltage Regulation and Efficiency.
<b>Week 15</b>	Instrument Transformers.
<b>Week 16</b>	Preparatory week before the final Exam

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Electrical Machinery Fundamentals” edited by Stephen J. Chapman.	NO
<b>Recommended Texts</b>	electrical machines and transformer by: Ancieron and Macneil	NO
<b>Websites</b>	<a href="https://www.coursera.org">https://www.coursera.org</a>	

Module Information					
Module Title	<u>Electronic I</u>			Module Delivery	
Module Type	<u>core</u>			<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	<u>NVEEELI212</u>				
ECTS Credits	<u>7</u>				
SWL (hr/sem)	<u>164</u>				
Module Level		1	Semester of Delivery		1
Administering Department		Electronics	College	Electronic Engineering college	
Module Leader			e-mail		
Module Leader's Acad. Title		Assistant Prof.	Module Leader's Qualification		PhD
Module Tutor			e-mail		
Peer Reviewer Name		Name	e-mail	Ahmad.younis@uoninevah.edu,iq	
Scientific Committee Approval Date		12/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module	NEEI1223	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents		
Module Aims	49.	To understand the basic theory and operation of bipolar transistor
	50.	To be familiar with current mechanism in an npn and pnp transistors
	51.	To concentrate transistor physical and electrical characteristics
	52.	To illustrate and design different dc biasing circuits
	53.	To understand the biasing stability conditions
	54.	To be able to deal with the mathematical behavior of transistor model
	55.	To understand small signal analysis of transistor amplifier
	56.	To deal with different transistor amplifier configuration
	57.	To be able to deal with the frequency response of transistor amplifier
	58.	To understand the basic operation of field effect transistor and MOS device
	59.	To understand the dc and ac behavior of FET and MOS amplifiers

<b>Module Learning Outcomes</b>	58. Understand and apply the basic theory and operation of transistor amplifiers 59. Define and explain the electrical characteristic of bipolar transistor 60. Understand the basic structure of npn and pnp transistors 61. Understand and analyze the electrical transistor model 62. Understanding the dc and ac analysis of transistor amplifier 63. Dealing with dc biasing and ac amplifiers 64. Understanding the effect of frequency on amplifier response 65. Familiar with other FET and MOS circuits
<b>Indicative Contents</b>	<p><b>Bipolar junction transistors,</b>  Transistor construction, transistor operation,  NPN &amp; PNP Bipolar Transistor; Current Flow Mechanism in Transistor Junctions; Transistor configurations; Current Gain Calculation [Alpha] and [Beta]; Transistor input/ output characteristics; DC Load line ; Operating point; Different DC circuit biasing. Bias circuit, voltage divider circuit, dc bias with feedback</p> <p><b>DC biasing,</b>  Operating point, fixed bias circuit, emitter bias circuit, voltage divider circuit, dc bias with feedback.</p> <p><b>Biasing stability</b>  Stability factor analysis due to temperature variation ( Effect of <math>I_{co}</math>, <math>V_{be}</math> and <math>\beta</math> ); Temperature compensation using diode biasing.</p> <p><b>Small signal analysis,</b>  Small signal equivalent circuit for CB, CE and CC configuration; Input/Output resistance; Calculation of current and voltage Gain in small signal amplifier; Graphical Analysis for voltage gain; Hybrid parameters to analyze transistor circuits.</p> <p><b>Field Effect Transistor (FET) and MOS transistor :</b>  <b>FET biasing configurations, Depletion and Enhanced mode operation,</b>  Introduction to the theory and operations of JFET &amp; MOSFET; FET Transistor configurations; Transistors transfer characteristics; Amplifier Circuit Biasing; transistor Equivalent circuit; Small signal analysis of FET transistor.</p> <p><b>FREQUENCY RESPONSE:</b>  Definition and Concepts; Gain in decibel; Bode plot for the gain; The effect of the Coupling capacitor; Low frequency analysis due to the R-C Coupled amplifier in BJTs; the Effect of emitter bypass capacitor; Calculation of the Low cut-off frequency. Transistor amplifier at high frequencies; Hybrid <math>\pi</math> equivalent circuit at high frequency; High frequency behavior of CB &amp; CE amplifier; High cut-off frequency; Gain Band-Width products for the above circuits; FET at high frequencies; CD and CS amplifier at high frequency;</p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

Student Workload (SWL)			
Structured SWL (h/sem)	88	Structured SWL (h/w)	3
Unstructured SWL (h/sem)	76	Unstructured SWL (h/w)	1
Total SWL (h/sem)	164		

Module Evaluation					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	, 5, 2 ,12,13,159	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	, 5, 2 ,12,13,159	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	, 5, 2 ,12,13,159	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	
Summative assessment	Midterm Exam	1:30hr	20% (20)	10	LO # 1-4
	Final Exam	3hr	40% (40)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
Week	Material Covered
Week 1	Transistor construction and operation
Week 2	Bipolar transistor current flow mechanism
Week 3	Transistor configurations, current gain calculation, and input and output resistances
Week 4	Dc biasing circuits, operating point calculation
Week 5	Biasing stability, stability factor calculation
Week 6	Temperature compensation using diode biasing
Week 7	Small signal equivalent circuit for CB, CC, CE configurations
Week 8	Calculation of voltage and current gains
Week 9	Hybrid model ac analysis of transistor amplifier
Week 10	FET and MOS transistors operation
Week 11	FET biasing configurations
Week 12	Depletion and enhancement mode operation

<b>Week 13</b>	Definition and analysis of amplifier frequency response
<b>Week 14</b>	Low frequency and high frequency analysis Hybrid-Pie equivalent circuit at high frequency
<b>Week 15</b>	
<b>Week 16</b>	Subject review

<b>Delivery Plan (Weekly Lab. Syllabus)</b>	
<b>Week</b>	<b>Material Covered</b>
<b>Week 1-15</b>	<b>Practical experiments in transistor amplifiers to measure the current and voltage gains.</b> <b>To measure the input and output amplifier resistances</b> <b>To measure the amplifier frequency response.</b>

<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	<b>Textbook1:INTEGRATED ELECTRONICS" MCGRAW HILL; 9TH EDITION, 1995. By MILLMAN &amp; HALKIET</b> <b>2: " ELECTRONICS DEVICES AND COMPONENTS", PITMAN, 1995 By MOTTERSHED, .</b>	yes
<b>Recommended Texts</b>	<b>3: " SOLID STATE DEVICES", PHI; 4TH EDITION, 1995. By STREETMAN,</b> <b>4" SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992. By : M.S. TYAGI</b>	Yes
<b>Websites</b>	Electronic circuits	

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 - 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

### Module Information

<b>Module Title</b>	<b>Fundamentals of <u>Electromagnetics</u></b>		<b>Module Delivery</b>		
<b>Module Type</b>	<b><u>Base</u></b>		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
<b>Module Code</b>	NVEEELI221				
<b>ECTS Credits</b>	<b>4</b>				
<b>SWL (hr/sem)</b>	<b>45</b>				
<b>Module Level</b>		2	<b>Semester of Delivery</b>		1
<b>Administering Department</b>		Type Dept. Code	<b>College</b>	Type College Code	
<b>Module Leader</b>	SINAN KHALID SHANSHAL		<b>e-mail</b>	sinan.mohammed@uoninevah.edu.iq	
<b>Module Leader's Acad. Title</b>		Lecturer	<b>Module Leader's Qualification</b>		M.Sc.
<b>Module Tutor</b>			<b>e-mail</b>		
<b>Peer Reviewer Name</b>		Name	<b>e-mail</b>	E-mail	
<b>Scientific Committee Approval Date</b>		02/07/2023	<b>Version Number</b>	1.0	

### Relation with other Modules

<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	



Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	To develop knowledge of the laws governing the behavior of electric and electro-magnetic fields, and to relate the laws governing the fields to applications in a range of electrical and electronic engineering application.
<b>Module Learning Outcomes</b>	<p>On completion of the course the students should be able:</p> <ul style="list-style-type: none"> <li>• to have detailed knowledge of the physical background and terminology of the electromagnetic field theory for electrical engineering problems</li> <li>• to understand the electromagnetic field behavior</li> <li>• to select and use appropriate theoretical models for analysis, problem solving and finding solutions related to the electrostatic, magnetostatic and electromagnetic fields</li> <li>• to understand how laws of electromagnetism can be applied to problems arising in engineering and biomedical sciences.</li> </ul>
<b>Indicative Contents</b>	<p>Electric charge and the electric field  Electric flux density and Gauss's Law  Electric scalar potential  Electric field in matter and boundary conditions  Capacitance  Magnetic field and Ampere's Law  Magnetic flux and Gauss's Law for magnetic fields  Faraday's Law  Inductance  Maxwell's equations  Applications of Electromagnetics</p>

Learning and Teaching Strategies	
<b>Strategies</b>	Through the presentation of a theoretical explanation with the aid of white board and 'Data Show', to illustrate syllabus (examples and exercises) and using text books.

Student Workload (SWL)			
<b>Structured SWL (h/sem)</b>	45	<b>Structured SWL (h/w)</b>	4
<b>Unstructured SWL (h/sem)</b>	?	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	?		

Module Evaluation					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	15% (10)	5,8,10,12	LO #1-5, 9 and 11
	Assignments	4	15% (10)	6,9,11,13	LO # 1-5, 6, 10 and 12
	Projects	0	0% (0)		
	Report	0	0% (0)		
Summative assessment	Midterm Exam	1.5hr	20% (20)	10	LO # 1-8
	Final Exam	3hr	50% (40)	16	All
Total assessment			100% (100 Marks)		

<b>Delivery Plan (Weekly Syllabus)</b>	
<b>Week</b>	<b>Material Covered</b>
<b>Week 1</b>	Review of Vector Calculus
<b>Week 2</b>	Review of Vector Calculus
<b>Week 3</b>	Experimental law of coulomb; Electric field intensity;
<b>Week 4</b>	Field of a continuous and volume charge distributions; line charge and sheet charge;
<b>Week 5</b>	Electric flux law density; Gauss's law; Application of Gauss's law; Some symmetrical charge distributions.
<b>Week 6</b>	Energy expended in moving a point charge in an electric field
<b>Week 7</b>	Definition of potential difference and potential
<b>Week 8</b>	Potential field of a point charge and system of charges; Potential gradient;
<b>Week 9</b>	Boit – Savart law
<b>Week 10</b>	Amperes law
<b>Week 11</b>	Magnetic Flux and Magnetic Flux Density
<b>Week 12</b>	Force on Differential Current Elements; Force and Torque on a Closed Circuit;
<b>Week 13</b>	Faraday's Law; Maxwell's Equations
<b>Week 14</b>	Example of Maxwell's Equations
<b>Week 15</b>	Wave Equations.
<b>Week 16</b>	Preparatory week before the final Exam

<b>Delivery Plan (Weekly Lab. Syllabus)</b>
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Week	Material Covered
Week 1-15	

Learning and Teaching Resources		
	Text	Available in the Library?
Required Texts	1-ENGINEERING ELECTROMAGNETICES, Mc- Graw Hill, By WILLIAM H. HAYT. 2-Elements of engineering electromagnetic, Prentice Hall, By Matthew N. O. SADIKU	No
Recommended Texts	1-Electromagnetics (Schaum's Outlines), McGraw-Hill Education, By Edminister, Joseph_ Nahvi, Mahmood.	No
Websites		

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance.
	B - Very Good	جيد جدا	80 - 89	Above average with some errors.
	C - Good	جيد	70 - 79	Sound work with notable errors.
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	F – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information		
Module Title	Engineering analysisI	Module Delivery
Module Type	Base	

<b>Module Code</b>	NVEE208		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
<b>ECTS Credits</b>	6				
<b>SWL (hr/sem)</b>	150				
<b>Module Level</b>		2	<b>Semester of Delivery</b>		1
<b>Administering Department</b>		Electronics dept	<b>College</b>	Electronics engineering college	
<b>Module Leader</b>	Dr. Omar B Mohammed		<b>e-mail</b>	omar.mohammed@uoninevah.edu.iq	
<b>Module Leader's Acad. Title</b>		Lecturer	<b>Module Leader's Qualification</b>		Ph.D.
<b>Module Tutor</b>			<b>e-mail</b>		
<b>Peer Reviewer Name</b>			<b>e-mail</b>		
<b>Scientific Committee Approval Date</b>			<b>Version Number</b>		

Relation with other Modules			
Prerequisite module	Mathematics II	Semester	1
Co-requisites module	None	Semester	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>
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<b>Module Aims</b>	This course covers the following topics: Multiple Integrals, Vectors Functions, Numerical Analysis, Statistics and Probability. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.
<b>Module Learning Outcomes</b>	<p>Upon successful completion, students will:</p> <ul style="list-style-type: none"> <li>13. Improve their problem-solving skills.</li> <li>14. Apply that knowledge toward practical problems in different areas of science.</li> <li>15. Utilize the computer capabilities to solve such problems using proper methods.</li> <li>16. Learn how to deal with geometry in 3D; find areas and volumes.</li> <li>17. Solve ordinary and differential equations numerically.</li> <li>18. Learn the importance of probability and statistics in everyday use.</li> </ul>
<b>Indicative Contents</b>	<p>Vectors Functions  Multiple Integrals  Numerical Analysis  Statistics  Probability</p>

<b>Learning and Teaching Strategies</b>	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

<b>Student Workload (SWL)</b>			
<b>Structured SWL (h/sem)</b>		<b>Structured SWL (h/w)</b>	

Unstructured SWL (h/sem)			Unstructured SWL (h/w)		
Total SWL (h/sem)					
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments				
	Projects / Lab.				
	Report				
Summative assessment	Midterm Exam				
	Final Exam				
Total assessment					

<b>Delivery Plan (Weekly Syllabus)</b>	
	<b>Material Covered</b>
<b>Week 1</b>	Vectors: Vector in space, dot and cross product. Lines and planes in space. Vector valued functions and motion in space: position, velocity and acceleration, tangential vectors, curvature and normal vector.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	
<b>Week 5</b>	Multiple Integrals: Double Integral in rectangular coordinates, areas and volumes. Double Integral in Polar Coordinates, areas and volumes. Triple Integrals in rectangular, cylindrical, and spherical coordinates, volumes.
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	Numerical Analysis: Solution of non-linear equations by iteration; bisection and Newton-Raphson. Numerical Integration; trapezoidal rule. Numerical solution of 1st order ordinary differential equations; Euler's method.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	Statistics and Probability:

<b>Week 14</b>	Definitions, mutually exclusive and conditional probability, permutations and combinations Probability distribution: binomial, normal and Poisson distributions.
<b>Week 15</b>	
<b>Week 16</b>	Preparatory week before the final Exam

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney& Thomas	Yes
<b>Websites</b>		

Module Information					
معلومات المادة الدراسية					
Module Title	Compute Programming		Module Delivery		
Module Type	Core		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	NVEEELI214				
ECTS Credits	5				
SWL (hr/sem)	150				
Module Level		UGx11 2	Semester of Delivery		2
Administering Department		Dept. of Electronic Eng. (Med. Ele)	College	College of Electronic Engineering	
Module Leader	Qais Thanon		e-mail	Qais.najim@uoninevah.edu.iq	
Module Leader's Acad. Title		Porf.	Module Leader's Qualification		Ph. D.
Module Tutor	Name (if available)		e-mail	E-mail	
Peer Reviewer Name		Name	e-mail	E-mail	
Scientific Committee Approval Date		20/06/2023	Version Number		1.0

Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None		Semester
Co-requisites module	None		Semester

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	1. Learning about the algorithms types and how building the algorithms. 2. Learning how to command computers to perform tasks using C++ language (Programming/coding). 3. Become acquainted with the designed programming including sequencing, condition and iteration. 4. Learn about the 1d and 2d arrays in C++ language. 5. Learn about the functions in C++ language.



	6. Learn about the strings in C++ language.
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	8. Understanding the meaning of the algorithms in programming languages. 9. Understanding the basics concepts of C language programming such as variables, data types, operators, control 10. Understanding the utilities of each one of sequencing, condition, and loops, and basic input/output operations. 11. Understanding how represent the data in 1d arrays and 2d arrays. 12. Learn about how the strings represented in C language. 13. Learn about divide any problem in sub-program and execute this problem by using function. 14. In advance practical experience by working on programming exercises and projects.
<b>Indicative Contents</b> المحتويات الإرشادية	Indicative content includes the following. <ul style="list-style-type: none"> <li>• Visualization via flowchart and Pseudocode [4 hrs]</li> <li>• Keywords, identifier, format specifier, and naming variables and constants [8 hrs]</li> <li>• Use standard libraries to take input and display output [8 hrs]</li> <li>• Operators in C++ programming [10 hrs]</li> <li>• Priorities in C++ programming [4 hrs]</li> <li>• Math functions [4 hrs]</li> <li>• Conditional operations [8 hrs]</li> <li>• Iterations (Loop operators) [10 hrs]</li> <li>• Arrays [10 hrs]</li> <li>• Functions [8 hours]</li> <li>• Review classes and problem solving [8 hrs]</li> </ul>

Learning and Teaching Strategies			
استراتيجيات التعلم والتعليم			
Strategies	The main strategy being focused on is developing conceptual programming thinking, meanwhile refining and expanding their mathematical thinking skills. This will be achieved through classes, online lectures, interactive tutorials. Additionally, working on complex projects that challenge students’ skills and require to apply advanced concepts. Such projects would help students exploring various aspects of C++ programming and gain hands-on experience in solving complex problems. some sampling activities that are interesting to the students.		
Student Workload (SWL)			
الحمل الدراسي للطلاب محسوب لـ ١٥ اسبوعا			
Structured SWL (h/sem)	77	Structured SWL (h/w)	5.1
الحمل الدراسي المنتظم للطلاب خلال الفصل		الحمل الدراسي المنتظم للطلاب أسبوعيا	
Unstructured SWL (h/sem)	73	Unstructured SWL (h/w)	4.8
الحمل الدراسي غير المنتظم للطلاب خلال الفصل		الحمل الدراسي غير المنتظم للطلاب أسبوعيا	
Total SWL (h/sem)	150		
الحمل الدراسي الكلي للطلاب خلال الفصل			

## Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5 and 10	LO #1, #2 and #10, #11
	Assignments	2	10% (10)	2 and 12	LO #3, #4 and #6, #7
	Projects / <b>Lab.</b>	1	10% (10)	Continuous	All
	Report	1	10% (10)	13	LO #5, #8 and #10
Summative assessment	Midterm Exam	2hr	10% (10)	7	LO #1 - #7
	Final Exam	3hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

المناهج الاسبوعي النظري

Week	Material Covered
Week 1	Introduction to computer languages and the structure of C program
Week 2	Flowchart and Pseudo-code
Week 3	Introduction to C++ programming: Declare variables and constants
Week 4	Take input and print output
Week 5	Assignment and Increment ,Decrement, Arithmetic, Logical, and Bitwise operators
Week 6	Standard math functions in math header <math.h>
Week 7	Priorities of operators in C++ programming
Week 8	Relational and conditional operators
Week 9	Mid-term Exam
Week 10	If statement versus switch case statement
Week 11	Examples of structured programming (sequencing and condition)
Week 12	Loop operators (For, while, do-while)
Week 13	Arrays
Week 14	Functions
Week 15	String of characters
Week 16	Preparatory week before the final Exam

## Delivery Plan (Weekly Lab. Syllabus)

المناهج الاسبوعي للمختبر

Week	Material Covered
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<b>Week 1-2</b>	Learn the C++ language program compiler.
<b>Week 3-4</b>	Declare variables and constants and <iostream.h> including standard functions
<b>Week 5-6</b>	Arithmetic, logical, and bitwise operators
<b>Week 7-8</b>	Math header for math functions <math.h> and Assignment and increment & decrement operators
<b>Week 9-10</b>	Relational and conditional operators and Loop operators
<b>Week 11-12</b>	Examples about the Arrays
<b>Week 13-14</b>	Examples about Functions and string

<b>Learning and Teaching Resources</b> <b>مصادر التعلم والتدريس</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	C Programming Absolute Beginner's Guide, 3rd Edition 2014. BY: Greg Perry and Dean Miller.	Yes
<b>Recommended Texts</b>	C How to Program with an introduction to C++, 8 <sup>th</sup> Edition 2016. BY: Paul Deitel and Harvey Deitel. Global Edition contribution by Piyali Sengupta	No
<b>Websites</b>	1- <a href="https://www.programiz.com/c-programming">https://www.programiz.com/c-programming</a> 2- <a href="https://www.coursera.org/specializations/c-programming">https://www.coursera.org/specializations/c-programming</a>	

<b>Grading Scheme</b> <b>مخطط الدرجات</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks %</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 - 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

## Courses specification for Second class Industrial Engineering (Second Course)

Module Information					
Module Title	AC Machines		Module Delivery		
Module Type	Support or related learning activity		<input checked="" type="checkbox"/> Theory <input type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	NVEEELI223				
ECTS Credits	6				
SWL (hr/sem)	175				
Module Level		2	Semester of Delivery		2
Administering Department		Electronic Dept.	College	Electronics Collage	
Module Leader			e-mail		

<b>Module Leader's Acad. Title</b>		<b>Module Leader's Qualification</b>	
<b>Module Tutor</b>		<b>e-mail</b>	
<b>Peer Reviewer Name</b>		<b>e-mail</b>	
<b>Scientific Committee Approval Date</b>		<b>Version Number</b>	

<b>Relation with other Modules</b>			
<b>Prerequisite module</b>	DC Machine	<b>Semester</b>	1
<b>Co-requisites module</b>	None	<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>	
<b>Module Aims</b>	60. Understanding AC Machine Principles 61. Analyzing AC Machine Behavior 62. Control Strategies 63. System Integration 64. Practical Applications 65. Problem-Solving Skills 66. Laboratory Skills 67. Teamwork and Communication 68. Professional Development
<b>Module Learning Outcomes</b>	66. Understand how voltage is induced in a rotating loop 67. Understand how curved pole faces contribute to a constant flux, and thus 68. more constant output voltages. 69. Understand how curved pole faces contribute to a constant flux,

	<p>and thus</p> <p>more constant output voltages.</p> <p>70. Understand the power flow diagram for Ac machines</p> <p>71. Know the types of Ac machines in general use.</p> <p>72. Understand the equivalent circuit of a three phase induction motor.</p> <p>73. Understand how to derive the Torque speed characteristic of three phase induction motor.</p> <p>74. Understand how to control the speed of different types of AC motors.</p> <p>75. Understand the starting torque, condition for maximum torque, condition for maximum starting torque of the Ac motors.</p> <p>76. Understand the methods of starting AC motors safely.</p> <p>77. Understand the equivalent circuit of a AC generator.</p> <p>78. Understand of Single phase Induction motor . Construction , theories of operation, torque speed characteristic, Equivalent circuit.</p> <p>79. Understand how Test of single phase induction motor, no load test, blocked rotor test, power flow diagram, applications.</p> <p>80. Understand how Three phase synchronous generator, Construction, Equivalent circuit, applications.</p> <p>81. Understand how Single phase synchronous motors, Reluctance motor, Construction of reluctances motor, applications.</p> <p>82. Understand how Hysteresis motor, Construction of Hysteresis motor, application.</p> <p>83. Be able to explain how copper losses, leakage flux, hysteresis, and eddy currents are modeled in Ac machines circuits.</p>
<b>Indicative Contents</b>	<p>Introduction - The module further develops students' understanding of electrical machines by introducing the operational principles and characteristics of AC machines, three phase circuits and complex power. It introduces the principles, operation and design of common power electronic converter circuits.(12 hrs.)</p>

	Commutation and Armature Construction in Real Tree phase induction motor.(8 hrs.)
	Introduction of The Equivalent Circuit of a Tree phase induction motor. (10 hrs.).
	Power Flow and Losses in Tree phase induction motor. (6 hrs.)
	Torque speed characteristic, starting torque, condition for maximum torque, condition for maximum starting torque.(12 hrs.)
	Test of three phase induction motor, no load test, blocked rotor test, power flow diagram, applications.(12 hrs.)
	Mid-term Exam. .(3 hrs.).
	Single phase Induction motor.(4 hrs.).
	Introduction of Single phase Induction motor . Construction , theories of operation, torque speed characteristic, Equivalent circuit, (12 hrs.).
	Test of single phase induction motor, no load test, blocked rotor test, power flow diagram, applications.(12 hrs.).
	Three phase synchronous generator, Construction, Equivalent circuit, applications. (12 hrs.).
	Single phase synchronous motors, Reluctance motor, Construction of eluctance motor, applications.(10 hrs.).
	Hysteresis motor, Construction of Hysteresis motor , application .(9 hrs.).
	AC Commutator machine,Universal motor.(12 hrs.).

Learning and Teaching Strategies	
Strategies	Visual Aids Problem-Solving Exercises Real-World Applications Group Projects Simulations and Virtual Labs Multimedia Resources Real-Life Examples

Student Workload (SWL)			
Structured SWL (h/sem)	74	Structured SWL (h/w)	5
Unstructured SWL (h/sem)	101	Unstructured SWL (h/w)	4.6 4
Total SWL (h/sem)	175		

Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	
	Assignments	2	10% (10)	2, 12	
	Projects / Lab.	1	10% (10)	Continuous	
	Report	1	10% (10)	13	
Summative assessment	Midterm Exam	2 hr	10% (10)	7	
	Final Exam	2hr	50% (50)	16	
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
	Material Covered
Week 1	Introduction - The module further develops students' understanding of electrical machines by introducing the operational principles and characteristics of AC machines, three phase circuits and complex power.
Week 2	Commutation and Armature Construction in AC Machine.
Week 3	Introduction of The Equivalent Circuit of a Three phase induction motor.



<b>Week 4</b>	Power Flow and Losses in Three phase induction motor.
<b>Week 5</b>	Torque speed characteristic, starting torque, condition for maximum torque, condition for maximum starting torque in Three phase induction motor.
<b>Week 6</b>	Test of three phase induction motor, no load test, blocked rotor test. , <b>applications</b>
<b>Week 7</b>	Mid-term Exam.
<b>Week 8</b>	Introduction of Single phase Induction motor. Construction, theories of operation.
<b>Week 9</b>	Torque speed characteristic, Equivalent circuit, of single phase induction motor.
<b>Week 10</b>	power flow diagram of single phase induction motor & applications.
<b>Week 11</b>	Test of single phase induction motor, no load test, blocked rotor test of Single phase Induction motor.
<b>Week 12</b>	Three phase synchronous generator, Construction, Equivalent circuit, applications.
<b>Week 13</b>	Single phase synchronous motors, Reluctance motor, Construction of reluctance motor, applications.
<b>Week 14</b>	Hysteresis motor, Construction of Hysteresis motor , application
<b>Week 15</b>	AC Commutator machine, Universal motor
<b>Week 16</b>	Preparatory week before the final Exam

<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Electrical Machinery Fundamentals” edited by Stephen J. Chapman.	NO
<b>Recommended Texts</b>	electrical machines and transformer by: Ancieron and Macneil	NO
<b>Websites</b>	<a href="https://www.coursera.org">https://www.coursera.org</a>	

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group (50 - 100)</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance
	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings

	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information				
Module Title	Electronics II		Module Delivery	
Module Type	core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input checked="" type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEEELI222			
ECTS Credits	7			
SWL (hr/sem)	175			
Module Level	1	Semester of Delivery		1
Administering Department	Electronics	College	Electronic Engineering college	
Module Leader			e-mail	
Module Leader's Acad. Title	Assistant Prof.	Module Leader's Qualification		PhD
Module Tutor			e-mail	
Peer Reviewer Name	Name	e-mail	Ahmad.younis@uoninevah.edu,iq	
Scientific Committee Approval Date	12/06/2023	Version Number	1.0	

Relation with other Modules			
Prerequisite module	NEEI2212	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	69. To understand the basic analysis of bipolar transistor amplifier 70. To be familiar with the dc and ac analysis of transistor amplifier 71. To understand the dc and ac analysis of FET amplifier 72. To illustrate and to understand the frequency response of amplifier 73. To understand the basic concept of feedback concept 74. To be able to deal with different feedback amplifier topologies 75. To study the advantages of negative feedback on amplifier performance 76. To be familiar with feedback amplifier ac analysis 77. To understand the construction and ideal characteristic of operational amplifier 78. To study and analyze op-amp equivalent circuit 79. To be familiar with basic op-amp applications 80. To start with studying power electronic devices
<b>Module Learning Outcomes</b>	84. Understand and apply the basic theory and operation of transistor amplifiers 85. Define and explain the frequency response of bipolar transistor amplifier 86. Understand the basic concept of negative feedback 87. Understand and analyze the feedback amplifier

	<p>88. Understanding the operation of ideal operational amplifier</p> <p>89. Dealing with dc and ac op-amp equivalent circuit</p> <p>90. Understanding the basic application of op-amp</p> <p>91. Power electronic devices principle overview</p>	
<b>Indicative Contents</b>	<p><b>Transistor and FET amplifier analysis:</b></p> <p>Small signal model analysis, low frequency and high frequency analysis, hybrid model, hybrid -Pi model analysis.</p> <p><b>Amplifier with negative feedback:</b></p> <p>Basic concept, feedback analysis, feedback configurations, Feedback effects on gain , bandwidth, input and output resistances</p> <p><b>Operational amplifier:</b></p> <p>Ideal Op-amp equivalent circuit; Operational Amplifier Specification; Circuit analysis of an Op-amp;</p> <p>Closed loop Op-amp Circuit ( Inverting and Non-Inverting Circuit ).</p> <p><b>Op-amp Applications:</b> Summation &amp; subtraction Circuit, Differential circuit Buffer circuit, Ideal and practical Integrator circuits, ideal and practical Differentiator circuits, Examples.</p> <p><b>Power electronic devices:</b></p> <p>UJT Construction, Operation and characterises;</p> <p>Thyristor Equivalent Circuit ; Thyristor Characteristics and operation ;</p> <p>Application of the devices.</p>	

## Learning and Teaching Strategies

<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.
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## Student Workload (SWL)

<b>Structured SWL (h/sem)</b>	74	<b>Structured SWL (h/w)</b>	3
<b>Unstructured SWL (h/sem)</b>	101	<b>Unstructured SWL (h/w)</b>	1
<b>Total SWL (h/sem)</b>	175		

## Module Evaluation

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
<b>Formative assessment</b>	<b>Quizzes</b>	6	10% (10)	2, 5, 9,12,13,15	LO #1, 2, 10 and 11
	<b>Assignments</b>	6	10% (10)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7
	<b>Projects / Lab.</b>	6	20% (20)	2, 5, 9,12,13,15	LO # 3, 4, 6 and 7, 5, 8 and 10
	<b>Report</b>	0	0% (0)	0	
<b>Summative assessment</b>	<b>Midterm Exam</b>	1:30hr	20% (20)	10	LO # 1-4
	<b>Final Exam</b>	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

	Material Covered
<b>Week 1</b>	Small signal model analysis
<b>Week 2</b>	Low and high frequency response of transistor amplifier
<b>Week 3</b>	Hybrid and hybrid-Pie equivalent circuit analysis
<b>Week 4</b>	Negative feedback concept and analysis
<b>Week 5</b>	Advantages of negative feedback on amplifier
<b>Week 6</b>	Amplifier feedback topologies
<b>Week 7</b>	Feedback effect on amplifier gain, bandwidth, and on input-output resistances
<b>Week 8</b>	operational amplifier construction and operation
<b>Week 9</b>	ideal and practical op-amp equivalent circuit
<b>Week 10</b>	Inverting and non inverting closed loop amplifier
<b>Week 11</b>	Integration and differentiation active circuits
<b>Week 12</b>	Summation and subtraction op-amp circuits
<b>Week 13</b>	UJT transistor construction
<b>Week 14</b>	Thyristor equivalent circuit and characteristics
<b>Week 15</b>	Subject review
<b>Week 16</b>	<b>Subject review</b>

Delivery Plan (Weekly Lab. Syllabus)	
	Material Covered
<b>Week 1-15</b>	<b>Practical experiments in transistor amplifier frequency response at lo and high frequency</b> <b>To measure the effect of feedback on amplifier performance</b> <b>To measure the performance of different op-amp circuits.</b>

Learning and Teaching Resources		
	Text	Available in the Library?
<b>Required Texts</b>	Electronic Devices By Millmann Electronic Devices By Floyd	yes

<b>Recommended Texts</b>	<b>SOLID STATE DIVICES", PHI; 4TH EDITION, 1995.By STREETMAN, SEMICONDUCTOR DEVICES &amp; CIRCUITS", JOHN WILEY &amp; SONS, 1992.By : M.S. TYAGI</b>	Yes
<b>Websites</b>	Electronic circuits	

Grading Scheme				
Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance.
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors.
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors.
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F</b> – Fail	راسب	(0-44)	A significant amount of work is required.
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information				
معلومات المادة الدراسية				
Module Title	Digital Design		Module Delivery	
Module Type	Base		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar	
Module Code	NVEE223			
ECTS Credits	3			
SWL (hr/sem)	60			
Module Level	2	Semester of Delivery	1	
Administering Department	Electronic Eng. Dep.	College	Electronics Engineering	
Module Leader	Amer Talal Ali		e-mail	
Module Leader's Acad. Title	Lecturer assistant	Module Leader's Qualification		
Module Tutor	Amer Talal Ali		e-mail	
Peer Reviewer Name	Name	e-mail		
Scientific Committee Approval Date	01/06/2023	Version Number		

Relation with other Modules



العلاقة مع المواد الدراسية الأخرى			
Prerequisite module		Semester	
Co-requisites module		Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
<b>Module Objectives</b> أهداف المادة الدراسية	30. To understand Advanced Minimization techniques for large number of bits to simplify the large designs. 31. Understand how to Design an Arithmetic and Logic unit. 32. Understand how to Design using programmable logic device. 33. To understand the sequential Logic Circuits. 34. To understand how to Design synchronous and asynchronous counters. 35. To understand the Design of Registers.
<b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية	1. Using Advanced Minimization techniques for large number of bits to simplify the large designs. 2. Design an Arithmetic and Logic unit. 3. Design using programmable logic device. 4. Design sequential Logic Circuits synchronous and asynchronous. 5. Design Registers. 6. Design synchronous and asynchronous counters.
<b>Indicative Contents</b> المحتويات الإرشادية	Indicative content includes the following.  <u>Part A</u> – minimization techniques for large number of bits [14 hrs]  <u>Part B</u> – Initialization to design and Design an Arithmetic and Logic unit. [14 hrs]

	<p><u>Part C</u> – Design using programmable logic device. [6 hrs]</p> <p><u>Part D</u> – sequential Logic Circuits. [18 hrs]</p>
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Learning and Teaching Strategies			
استراتيجيات التعلم والتعليم			
Strategies	The main strategy that will be adopted in delivering this module is to encourage students’ participation in the exercises, while at the same time refining and expanding their critical thinking and digital designing skills. This will be achieved through classes and interactive tutorials.		
Student Workload (SWL)			
الحمل الدراسي للطالب محسوب لـ ١٥ اسبوعا			
Structured SWL (h/sem)	60	Structured SWL (h/w)	4
الحمل الدراسي المنتظم للطالب خلال الفصل		الحمل الدراسي المنتظم للطالب أسبوعيا	
Unstructured SWL (h/sem)	60	Unstructured SWL (h/w)	4
الحمل الدراسي غير المنتظم للطالب خلال الفصل		الحمل الدراسي غير المنتظم للطالب أسبوعيا	
Total SWL (h/sem)	120		
الحمل الدراسي الكلي للطالب خلال الفصل			

<b>Module Evaluation</b> تقييم المادة الدراسية
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		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	20% (20)	[3,6,9,12]	LO (#1- #12)
	Assignments	1	10% (10)	14	LO #4, #7, #(10-13)
	Projects / Lab.	0	0% (0)		
	Report	1	10% (10)	12	LO #11
Summative assessment	Midterm Exam	1.5 hr	10% (10)	10	LO #(1-8)
	Final Exam	2 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)	
المنهاج الاسبوعي النظري	
	Material Covered
Week 1	Introduction to Digital Design
Week 2	The 5-Variable Karnaugh Map; The 5-Variable Karnaugh Map with don't care conditions
Week 3	Map Entered variable Karnaugh Map
Week 4	ADDITIONAL MINIMIZATION TECHNIQUES: Tabular method; Quine-McCluskey
Week 5	Design using multiplexer: - Shannon Expansion
Week 6	top-down design of combinational CIRCUITS: - Gate Level: Adders; Subtractor
Week 7	Design an Arithmetic and Logic unit
Week 8	memory and type of memories

<b>Week 9</b>	Design using programmable logic device (PLD): - PROM; PAL; PLA;
<b>Week 10</b>	sequential LOGIC: - Type of flip-flops; Timing Diagram; Basic concepts of counters; Binary counters; BCD counters; Up down counter
<b>Week 11</b>	sequential LOGIC: -Design of counters using state diagrams and tables;
<b>Week 12</b>	sequential LOGIC: -Mealy and Moore Circuits;
<b>Week 13</b>	synchronous CIRCUITS: Shift left and right register; Registers with parallel load; Serial –in parallel-out (SIPO) and parallel-in-serial-out (PISO).
<b>Week 14</b>	synchronous CIRCUITS: Shift Registers; Twisted Ring Counter; Maximum Length Shift Counter.
<b>Week 15</b>	Preparatory week before the final Exam

<b>Learning and Teaching Resources</b> <b>مصادر التعلم والتدريس</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	"Digital and analog communication" 2001 By L. W. Couch Sixth Edition	Yes
<b>Recommended Texts</b>	- Digital Communications Fifth Edition, 2008, John G. Proakis, and Masoud Salehi.  Introduction to Communication Systems" 1992 By F. Stremler.  -ELEMENTS OF INFORMATION THEORY" 2006 By THOMAS M. COVER and JOY A. THOMAS  -Digital Communication, 2004 by Abbas Kattoush.	Yes
<b>Websites</b>		

<b>Grading Scheme</b> <b>مخطط الدرجات</b>
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Group	Grade	التقدير	Marks %	Definition
<b>Success Group</b> <b>(50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group</b> <b>(0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F – Fail</b>	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

Module Information					
Module Title	<u>Signals and Systems</u>		Module Delivery		
Module Type	<u>Core</u>		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar		
Module Code	<u>NVEE210</u>				
ECTS Credits	<u>6</u>				
SWL (hr/sem)	<u>150</u>				
Module Level		UGx11 1	Semester of Delivery		4
Administering Department		Type Dept. Code	College	Type College Code	
Module Leader			e-mail		
Module Leader's Acad. Title		Assistant Professor	Module Leader's Qualification		Ph.D.
Module Tutor	Name (if available)		e-mail	E-mail	
Peer Reviewer Name		Name	e-mail	E-mail	

<b>Scientific Committee Approval Date</b>	25/06/2023	<b>Version Number</b>	1.0
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<b>Relation with other Modules</b>			
<b>Prerequisite module</b>	None	<b>Semester</b>	
<b>Co-requisites module</b>	None	<b>Semester</b>	

<b>Module Aims, Learning Outcomes and Indicative Contents</b>	
<b>Module Aims</b>	<p>Student will be able to:</p> <p>36. identify systems concepts .</p> <p>37. understand the properties of systems .</p> <p>38. understand the mathematical relation between input and output of a system.</p> <p>39. deal with Fourier and Laplace analysis of systems.</p> <p>40. perform z-transform of discrete signals .</p>
<b>Module Learning Outcomes</b>	<p>62. Definition of the system concept.</p> <p>63. Introduction of mathematical models.</p> <p>64. Explain Continuous time systems. Discrete time systems.</p> <p>65. Introduction of frequency response of systems.</p> <p>66. Definition of filters.</p> <p>67. Explain Ideal filters, Non ideal filters, and Butterworth filter design.</p> <p>68. Define Z-transform of discrete signals.</p> <p>69. Analyze of continuous system using Laplace Transform. System transfer function.</p> <p>70. Definition of transfer function of a discrete system.</p>
<b>Indicative Contents</b>	<p>Indicative content includes the following.</p> <p>Introduction to systems:</p> <ul style="list-style-type: none"> <li>- Definition and mathematical models.</li> <li>- Properties of systems.</li> </ul> <p>Transformation used with continuous systems</p> <ul style="list-style-type: none"> <li>- Fourier transforms.</li> <li>- Filters.</li> <li>- Laplace transform.</li> </ul> <p>Z-transform:</p>

	<ul style="list-style-type: none"> <li>- Introduction of z- transform of discrete time signal.</li> <li>- Z-transform used with discrete systems.</li> <li>- Convolution used for</li> <li>- Continuous systems.</li> <li>- Discrete systems</li> </ul>
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Learning and Teaching Strategies	
<b>Strategies</b>	To make students interesting with both types of systems: continuous and discrete. Also with properties of systems and operations . To make them familiar with time and frequency domain and analysis of a system. Also to make them familiar with different types of transforms of systems. Also to make them have an experience with solving different problems and examples.

Student Workload (SWL)			
Structured SWL (h/sem)	62	Structured SWL (h/w)	4
Unstructured SWL (h/sem)	88	Unstructured SWL (h/w)	1
<b>Total SWL (h/sem)</b>	<b>150</b>		

Module Evaluation					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	6	10% (10)	, 5, 2 ,12,13,159	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	, 5, 2 ,12,13,159	LO # 3, 4, 6 and 7
	Projects / Lab.	6	20% (20)	, 5, 2 ,12,13,159	LO # 3, 4, 6 and 7, 5, 8 and 10
	Report	0	0% (0)	0	

<b>Summative assessment</b>	<b>Midterm Exam</b>	1.5hr	20% (20)	10	LO # 1-4
	<b>Final Exam</b>	3hr	40% (40)	16	All
<b>Total assessment</b>			100% (100 Marks)		

### Delivery Plan (Weekly Syllabus)

<b>Week</b>	<b>Material Covered</b>
<b>Week 1</b>	Basic definitions. Mathematical models.
<b>Week 2</b>	Continuous time systems.
<b>Week 3</b>	Discrete time systems.
<b>Week 4</b>	System properties.
<b>Weeks 5</b>	Mathematical system representation in time domain: Convolution representation.
<b>Week 6</b>	Convolution properties.
<b>Week 7</b>	System description by linear constant coefficient differential equations.
<b>Week 8</b>	Frequency domain analysis of continuous system.
<b>Week 9</b>	Frequency response of a system.
<b>Week 10</b>	Frequency response of electrical circuits.
<b>Week 11</b>	Filters. Distortion less transmission.
<b>Week 12</b>	Ideal filters. Non ideal filters. Butterworth filter design.
<b>Week 13</b>	Analysis of continuous system using Laplace Transform.
<b>Week 14</b>	System transfer function.
<b>Week 15</b>	Analysis of discrete system using z-Transform. System transfer function.

### Learning and Teaching Resources

	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Signals and Systems. Simon S. Haykin	Yes
<b>Recommended Texts</b>	Signals and linear Systems. G. E. Carlson	

### Grading Scheme



Group	Grade	التقدير	Marks %	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required
<b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

Module Information						
Module Title	Engineering Analysis II		Module Delivery			
Module Type	Core		<input type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input type="checkbox"/> Lab <input checked="" type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar			
Module Code	NVEE209					
ECTS Credits	3					
SWL (hr/sem)	75					
Module Level	2	Semester of Delivery		2		
Administering Department	Electronics dept	College	Electronics engineering college			
Module Leader	Dr. Omar B Mohammed		e-mail	omar.mohammed@uoninevah.edu.iq		
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	Ph.D.		
Module Tutor			e-mail			
Peer Reviewer Name			e-mail			
Scientific Committee Approval Date			Version Number			

Relation with other Modules			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
<b>Module Aims</b>	<p>This course covers the following topics: ordinary differential equations, sequences and series, solution of differential equations by power series, and matrix analysis. Those areas of mathematics which are most important in connection with practical problems for modeling different areas of science, computer can be easily utilized to find the properties of such systems.</p>
<b>Module Learning Outcomes</b>	<p>Upon successful completion, students will:</p> <ul style="list-style-type: none"> <li>19. Improve their problem-solving skills.</li> <li>20. Apply that knowledge toward practical problems in different areas of science.</li> <li>21. Utilize the computer capabilities to solve such problems using proper methods.</li> <li>22. Learn how to represent any function as a power series, then use computer to solve it.</li> <li>23. Learn the importance of differential equations for modeling almost any system, and how to solve it to find the properties of that system.</li> <li>24. Learn the linear algebra and its importance in science.</li> </ul>
<b>Indicative Contents</b>	<p>Ordinary Differential Equations. Sequences and Series. Solution of Differential Equations by Power Series.</p>

	Matrix Analysis.
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Learning and Teaching Strategies	
<b>Strategies</b>	The primary strategy for delivering this module will be to encourage students to participate in the exercises while refining and expanding their critical thinking skills. This will be accomplished through classes, interactive tutorials, and the consideration of simple experiments involving sampling activities that students find interesting.

Student Workload (SWL)					
Structured SWL (h/sem)			Structured SWL (h/w)		
Unstructured SWL (h/sem)			Unstructured SWL (h/w)		
Total SWL (h/sem)					
Module Evaluation					
		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments				
	Projects / Lab.				
	Report				
Summative assessment	Midterm Exam				
	Final Exam				
Total assessment					

Delivery Plan (Weekly Syllabus)	
	Material Covered

<b>Week 1</b>	<b>Ordinary Differential Equations:</b> 4. First order (variables separable, homogeneous, linear and exact). 5. Second order homogeneous. 6. Second order nonhomogeneous; indeterminant coefficients, variation of parameters.
<b>Week 2</b>	
<b>Week 3</b>	
<b>Week 4</b>	
<b>Week 5</b>	<b>Infinite Sequences and Series:</b> 4. Limit laws, indeterminate forms and L'hospital rule. 5. Infinite series; convergence test. 6. Power series; Taylor and Maclaurin series.
<b>Week 6</b>	
<b>Week 7</b>	
<b>Week 8</b>	
<b>Week 9</b>	<b>Solution of Differential Equations by Power Series:</b> Power series method, Legendre's equation; Legendre s polynomials.
<b>Week 10</b>	
<b>Week 11</b>	
<b>Week 12</b>	
<b>Week 13</b>	<b>Matrix Analysis:</b> 5. Review of matrix theory, solving system of equations; Cramer's rule, inverse of the matrix method, Gauss elimination. 6. Eigen values and eigen vectors. 7. Diagonalization of matrices 8. Application of matrices to electric circuits.
<b>Week 14</b>	
<b>Week 15</b>	
<b>Week 16</b>	
<b>Week 16</b>	Preparatory week before the final Exam

<b>Learning and Teaching Resources</b>		
	<b>Text</b>	<b>Available in the Library?</b>
<b>Required Texts</b>	Advanced Engineering Mathematics By KREYSIK	Yes
<b>Recommended Texts</b>	Calculus By Finney& Thomas	Yes
<b>Websites</b>		

<b>Grading Scheme</b>				
<b>Group</b>	<b>Grade</b>	<b>التقدير</b>	<b>Marks (%)</b>	<b>Definition</b>
<b>Success Group</b>	<b>A - Excellent</b>	امتياز	90 - 100	Outstanding Performance.

<b>(50 - 100)</b>	<b>B - Very Good</b>	جيد جدا	80 - 89	Above average with some errors.
	<b>C - Good</b>	جيد	70 - 79	Sound work with notable errors.
	<b>D - Satisfactory</b>	متوسط	60 - 69	Fair but with major shortcomings.
	<b>E - Sufficient</b>	مقبول	50 - 59	Work meets minimum criteria.
<b>Fail Group (0 – 49)</b>	<b>FX – Fail</b>	راسب (قيد المعالجة)	(45-49)	More work is required, but credit is given.
	<b>F – Fail</b>	راسب	(0-44)	A significant amount of work is required.
<p><b>Note:</b> Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.</p>				