Curriculum Vitae

Personal Information:

❖ **Name** : Emad Abdalhalem Abdo

Contact Information:

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Academic Achievements:

Education Background	University	Country	Year	College	Department
Bachelor	University Of Mosul	Iraq	17\06\2006	Electronics Engineering	Electronic Engineering
Master degree	University Of Mosul	Iraq	10\05\2012	Engineering	Electrical Engineering

Employment Status:

Status	**Employed**
(1)	
Place of Employment	University of Mosul / College of Electronics Engineering
Date of Employment	From 2007 to 2012
Job title	Engineer
Date of Employment	From 2012 to 2014
Job title	Lecturer
(2)	
Place of Employment	Ninevah University / College of Electronics Eng.
Date of Employment	Since 2014
Job title	Lecturer
Concern Ministry	Ministry Of Higher Education and Scientific Research http://www.en.mohesr.gov.iq

❖ ***Published papers :***

1. E. A. Abdo, A. T. Younis. *On the design and optimization of CMOS active inductor for RF applications*. *Journal of Engineering Science and Technology* (*JESTEC*), *June 2020***.**
2. A. T. Younis, E. A. Abdo. Low noise amplifier (LNA) performance optimization using genetic algorithms (GAs). *Journal of Engineering Science and Technology* (*JESTEC*), *October 2020.*
3. Emad A. Abdo, Ahmad T. Younis, Ahmad A. Ismael. *Optimum Design of 2.4GHz Low Noise Amplifier (LNA)*. Proceedings of the 1st International Multi-Disciplinary Conference Theme: Sustainable Development and Smart Planning, IMDC-SDSP 2020, Cyperspace, 28-30 June 2020. European Alliance for Innovation Journal (EAI). 2020.
4. H. M. Abd, E. A. Abdo, A. M. Jasim and A. M. A. Sabaawi, "*Incremental Conductance Algorithm Optimization for a Photovoltaic System with Fast Response Under Fast-Varying of Solar Power*," 2019 10th International Renewable Energy Congress (IREC), Sousse, Tunisia, 2019, pp. 1-6.
5- Younis, A.T. and Al–Halem, E.A., 2012. *Genetic algorithm application to analog integrated circuit design*. *International Journal of Reasoning-based Intelligent Systems*, *4*(4), pp.209-213.

❖ ***Research Interests:***

- Microelectronics Circuits Design
- Analog integrated circuit design
- Digital integrated circuit design
- Amplifier circuits design
- Optimization Intelligence techniques

❖ ***Teaching :***

- Engineering Analysis (2^{nd} class)
- Electronic LAB (2^{nd} class)
- Final year B.Sc. students projects as supervisor(4^{th} class)