السيرة الذاتية لأعضاء الهيئة التدريسية في قسم هندسة الالكترونيك

1) **الاسم الثلاثي واللقب: احمد ذنون يونس النقيب**
2) **الحالة الزوجية: متزوج**
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5) **الشهادات الحاصل عليها:**

ت	الشهادة	الجامعة	السنة
1	بكلوريوس هندسة كهرباء	الكلية الفنية العسكرية	1981
2	ماجستير هندسة الكترونية	نوتنكهام/ بريطانيا	1985
	دكتوراه هندسة الكترونية	اسكس/ بريطانيا	1989

6) **الاختصاص:**

العام	كهرباء
الدقيق	الكترونيك/ الكترونيات دقيقة

7) **الاسم المستخدم لنشر البحوث حسب الكوكل سكولر:ahmedyounis1959**
8) **الاتجاهات البحثية:**

ت	
1	تصيم الدوائر الالكترونية المتكاملة
2	التصيم باستخدام الامثلية للمكبرات LNA
3	التصيم باستخدام الامثلية لمكبرات القدرة للترددات الراديوية
4	

9) **البحوث المنشورة:**

1- **On the design of optimal SCF based on the use of FDNR; IEE SARAGA Coll. In electronic filter; London 1988.**
2- **Optimal design of novel class of SCF using SWAP; Electronic computer aided design ECAD, 1989.**
3- **On the design of lossy FDNR active RC prototype structures for realization as switched capacitor filters; proceeding of 31st IEEE Midwest symposium on circuit and system, USA 1988.**
4- **Design of optimal SCF based on the use of lossy frequency dependent negative resistance prototype structures; IEE proceeding , Dec 1989.**

5- **An efficient design method for optimal MOS integrated circuit switched capacitor ladder filters; proceeding of 33rd Midwest symposium on circuit and systems; IEEE university of Calgary, Canada 1990.**
6- **Automated synthesis of switched capacitor ladder filters with in an analog silicon compilation environment; IEE proceeding , April 1992.**
7- **On the application of thr TLM modeling for simulating thermal flow in a punch-through diode, Caledonian journal of engineering , 2010.**
8- **Genetic algorithm application to analog integrated circuit design, Ahmad T. Younis, Emad Abd Al-Halem *International Journal of Reasoning-based Intelligent Systems 2012 - Vol. 4, No.4 pp. 209 - 213* .**
9- **A 2.45GHz class-F power amplifier for CDMA systems, IEEE conference in internet technology and applications (ITA) , U .K. 2015.**
10- **Optimum design of charge pump circuits using genetic algorithm, IJEIT , 2016.**
11- **Optimum digital logic design based on the use of genetic algorithm, مجلة الاطروحة للعلوم الهندسية والتكنلوجيا المحكمة, No 6, April 2017.**
12- **Digital Logic design optimization based on the use of Genetic Algorithm, international journal on recent and innovative trends in computing and communication (IJIRTCC), march 2017.**
13- **Performance improvement of three stage 4:16 decoder using genetic algorithm, (IJERT), April 2017.**

(10 الاشراف على رسائل واطاريح الدراسا العليا:

1- Computer aided design of integrated active-RC and switched capacitor Filters, MCE, 1999.

2- Design and implementation of digital image recognition system, MCE, 1999.

3-Transmission line matrix modeling (TLM) of diffusion processes and thermal distribution in semiconductor devices, University of Technology, 1999.

4- Practical limitations in the design of analog MOS integrated circuits, University of Technology, 1999.

5- Computer aided analysis of SCC using equivalent continuous models, University of Technology, 2000.

6- Sensitivity analysis for large switched capacitor networks, University of Technology, 2000.

7- CMOS OP-AMP design for high frequency switched capacitor applications, University of Technology, 2001.

8- Solving optimization problems in real-time conventional analog networks including ANNs and SCNN, University of Technology, 2001.

9- CAD of SC filters based on prewarping techniques, MCE, 2001.

10- Design and analysis of RF power amplifier, MCE, 2002.

11- Design and realization of integrated filters based on the use of FDNR, MCE, 2002.

12- Optimum design of SCF based the use of lossy FDNR, University of Technology, 2002.

13- Design and implementation of microwave oscillator, MCE, 2003.

14- Design of radio system using (ALE) Technology, 2003.

15- Distributed arithmetic for the design of high speed FIR filter using FPGA (U.O.T) 2004.

16- Design and analysis of RF power amplifier (U.O.T) 2004.

17- Integrate circuit design and synthesis using genetic Algorithm (U.O.T) 2004.

18- Design and implementation of an airplane engine vibration Alerting system (U.O.T) 2004.

19- Design and simulation of high frequency low noise amplifier (University of Technology, 2004.

20- تصميم وبناء منظومة تنبيه وحماية الكترونية ضد الكشف الليزري. الجامعة التكنولوجية 2004.

21- CMOS integrated circuit realization based on genetic algorithm (U.O.T) 2005.

22- Low voltage CMOS OP-AMP design for high frequency switched capacitor application U.O.T. 2005.

23- High power microwave amplifier based on combining technique U.O.T.2005.

24- Improved DTC of three phase induction motor prototype of based on FPGA (U.O.T) 2006.

25- Object identification using wavelet transform implemented on FPGA (U.O.T) 2006.

26- chip area minimization of analog integrated circuits using Genetic Algorithm, University of Mosul, 2012

27- Optimal design of class_F power amplifier, university of mosul, 2014

28- Digital CMOS IC design using Genetic Algorithm, university of Mosul, 2017

11- **الخبرات التدريسية:**

1- **Undergraduate level: Electronic circuits, IC design, circuit design.**
2- **Postgraduate level: IC design , VLSI engineering, Computer aided circuit design, integrated filter design, analog IC design, Digital VLSI design. The above courses are given in many Iraqi universities such as Baghdad university, Al-Mustansyria university, university of technology as well as Mosul university and Ninevah university.**